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MICRO JOURNAL

VOLUME IV ISSUE III . Devoted to the 68XX User . March 1982 "Small Computers Doing Big Things" RVING THE 68XX USER WORLDWIDE



## YOUR CHOICE-smart either way

- Over 140 software driven functions
- 82 x 24 or 82 x 20 screen format software selectable
- High resolution 7 x 12 matrix characters P-31 green phosphor
- Upper/lower case character set plus graphics character set
- 56-key alphanumeric keyboard plus 12-key cursor, numeric pad
- Internal editing functions insert, delete, scroll, roll, slide, etc.
- Parallel printer I/O port
- 50 to 38,400 baud operation programmable
- Cursor type, cursor position, print control characters, protected fields, shift inversion, dual intensity and many other features

8212 - twelve-inch diagnonal screen or 8209 - nine-inch diagnonal screen



SOUTHWEST TECHNICAL PRODUCTS CORPORATION 219 W. RHAPSODY SAN ANTONIO, TEXAS 78216 (512) 344-0241



Pascal for the 6809 is a true native code compiler. Unlike the Pascal for the bauy is a true native code compiler. Unlike the usual P-code Pascals which run in an interpretive manner, usual P-code Pascals Which run in an interpretive manni-ours produces efficient assembly language mnemonics ours produces efficient assembly language mnemonics
which can be assembled and run directly. Exim Many feature
available for both 6800 Ft Exim and I IniEl Exim Many feature which can be assembled and run directly. This compiler is available for both 6809 FLEXTM and UniFLEXTM. Many features available for both 6809 FLEXTM and UniFLEXTM. available for both 6809 FLEX." and UniFLEX." Many features not found in other Pascal systems were implemented while avoiding those features completely populately pop not found in other Pascal systems were implemented while avoiding those features completely non-standard. Features the Pascal system include: Supports most of Jensen and Wirth specification

- Produces fast and efficient 6809, native code the Pascal system include:

  - FLEX run-time package may be trimmed Double precision real numbers (10.0 digits)
     Implements scalar, subrange and structured data types Double precision real numbers (16.8 digits)

    - Standard I/O using file buffer pointers
    - FLEX version may call assembly language programs Ability to call other Pascal programs Dynamic storage allocation
      - Standard math functions: SIN, COS, ARCTAN, EXP, LN,
         Standard math functions: Buffered or single character terminal input
      - Random number generator function Many usable, sample programs included

        - UniFLEX version supports:

Ability to call various UniFLEX system routines Ability to execute UniFLEX utility commands Random file positioning

Pascal on diskette for 5" and 8" 6809 FLEX is available Pascal on diskette for 5" and 8" 6809 FLEX is available for \$200.00 The 5" version requires two disk drives.

Tor \$200.00 The 5 version requires two disk drives.
The UniFLEX version is \$300.00 and includes one year of the UniFLEX version is \$400.00 include 3 percent for maintenance. me Uniffer version is \$300.00 and includes one year maintenance. All orders should include 3 percent for maintenance. All orders should include 3 percent or formal percent or maintenance. All orders should include 3 percent for postage and handling (10 percent on foreign orders). pustage and manufing (to percent on total Systems Consultants, Inc.



# 68

Portions of text prepared using the following.

SWTPC 6800-6809-DMAF2-CDSI-CT82-Sprint 3 Southwest Technical Products 219 W. Rhapsody San Antonio, Texas 78216

EDITOR - WORD PROCESSOR Technical Systems Consultants, Inc. Box 2573, W. Lafayette, IN 47906 FLEX Is TM of TSC

GIMIX Super Mainframe-Assorted memory boards GIMIX Inc. 1337 West 37th Place Chicago, II 60609

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#### MARCH 1982

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## MICRO JOURNAL

#### Send All Correspondence To:

68 MICRO JOURNAL 5900 Cassandra Smith Computer Publishing Center PO Box 849 Hixson, TN 37343

615 842-4600

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'68' Micro Journal is published 12 times a year by '68' Micro Journal, 6131 Airways Blvd., Chattanooga, TN 37421. Second Class postage paid at Chattanooga, TN. Postmaster: Send Form 3579 to '68' Micro Journal, PO Box 849, Hixson, TN 37343.

1-Year \$24.50,2-Year \$42.50,3-Year \$64.50

#### -ITEMS SUBMITTED FOR PUBLICATION-

(Letters to the Editor for Publication) All 'letters to the Editor' should be substantiated by facts. Opinions should be indicated as such. All letters must be signed. We are interested in receiving tetters that will benefit or alen our readers. Praise as well as gripes is always good subject matter. Your name may be withheld upon request. If you have had a good experience with a 6800 vendor please put it in a letter. If the experience was bad put that in a letter also. Remember, if you tell us who they are then it is only fair that your name 'not' be withheld. This means that all letters published, of a critical nature, cannot have a name withheld. We will attempt to publish 'verbatim' letters that are composed using 'good taste.' We reserve the right to define (for '68' Micro) what constitutes 'good taste.'

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#### GIMIX 2MHZ 6809 5Y51EMS



GIMIX offers you a variety of system packages including systems that feature BOTH MICROWARE'S OS-9 Level 1<sup>TM</sup> operating system and TECHNICAL SYSTEMS CONSULTANTS' FLEX<sup>TM</sup>. Switch between these two predominant 6809 Disk Operating Systems, under software control, without the need to change PROMS, switches, or system configuration. System packages are also available for MICROWARE'S OS-9 Level 2 and TECHNICAL SYSTEMS CONSULTANTS' UniFLEX<sup>TM</sup>. You can select one of our featured systems or select from our wide variety of system components to build a custom system to suit your needs.

All systems include any required CPU Board options and are completely configured to your specifications. They do not include disk drives or terminals. See pages 4 and 5 for information on 5%11 drives for installation in the CLASSY CHASSIS and/or 811 disk drives and cabinets. Any combination of 5%11 and 811 floppy disk drives, up to four drives total, can be used with systems that Include controller (except UniFLEXTM systems which require 811 drives).

For information and pricing on additional options see the appropriate pages of this brochure of contact the lactory,

#### 56KB 2MHZ 6809 SYSTEMS WITH GMXBUG/FLEX/OS:9 SOFTWARE SELECTABLE

INCLUDES: CLASSY CHASSIS, 6809 PLUS CPU Board, 56K Byte STATIC RAM, #43 Two Port Serial I/O board w/cables, and...
with #58 single density disk controller (System #59) \$2988.59
with #68 DMA double density disk controller (System #49) \$3248.49
To substitute Non-Volatile CMOS RAM with battery back-up, add \$150.00

#### 128KB 2Mhz 6809 DMA Systems for use with TSC's UNIFLEX or MICROWARE's OS-9 Level 2

INCLUDES: CLASSY CHASSIS, 6809 PLUS CPU Board, #68 DMA Disk Controller, Two 64K Byte STATIC RAM Boards, #43 two
Port Serial I/O board w/cables, (software not included, UniFLEXTM requires 8" disk drives) \$3798.39

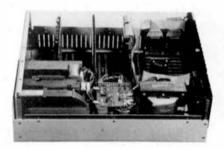
\*\*To substitute 128KB of Non-Volatile CMOS RAM w/Battery Back-Up add \$300.00

56 KB SYSTEM #29 This system can be used as the basis for a custom system to suit your special needs. It includes: CLASSY CHASSIS, 6809 PLUS CPU, 56KB STATIC RAM Board, and #43 TWO PORT SERIAL I/O board w/cables. You can add to this your choice of Disk Controllers, Memory, I/O, Software, etc. \$2498.29
50 Hz version of above add \$30.00

## The GIMIX CLASSY CHASSIS 6800 / 6809 SS-50 BUS MAINFRAME

#### The CLASSY CHASSIS includes:

A HEAVYWEIGHT, ALUMINUM CABINET (18" wide x 21" deep x 7" high) painted in a putty colored, durable baked enamel linish. The cabinet holds our 6800/6809 mother board. CV Ferro-resonant power supply, and has provisions for mounting one or two 5½" Floppy or Winchester disk drives. The back panel is punched for t5"0" type data connectors (25 pin) and has provisions for two removable connector plates that are available in a variety of connector configurations. Cabinets are normally supplied with two blank plates unless other types are required or specified. The cabinet includes a tan and ventilation slots which direct cooling air over the boards and power supply. The front panel has a 3 position, key locking, power switch that permits the reset switch to be locked out, preventing accidental system reset, and a three position RESET/ABORT switch. Optional filler plates are available for systems that do not use the 5¼" drive openings.



## The 6800 / 6809 SS-50 / C MOTHERBOARD includes:

This highly versatile motherboard is easily reconfigured for a variety of 6800 and 6809, SS-50 and SS-50C bus configurations.

GOLD PLATED connectors are used throughout to insure long lasting electrical contact and protection against corrosion.

It has filteen 50 pin slots, 8 DIP-switch addressable 30 pin I/O slots, and a special 10 pin slot for the baud rate generator board. The fully buffered I/O block can be configured for 4, 8, or 16 decoded addresses per slot, and is DIP-switch addressable to any 32, 64, or 128 byte boundary. Extended address decoding (SS-50C) allows the I/O block to be addressed anywhere in the 1M byte address space.

The baud rate generator board provides 11 standard (t6X) baud rates, from 75 to 38,4K, in 2 groups. Programming jumpers allow easy selection of up to five baud rates. The five baud rate lines on the 50 pin bus are easily disconnected from the 30 pin bus tor use with SS-50C extended addressing or as user defined lines. A slow 1/0 circuit, for the 6809 CPU, can be used to generate an MRDY signal whenever an I/O slot is accessed (This allows, for example, using PIO Disk Controllers with a 2MHz. 6809 CPU).

All data, address, and control lines are fully terminated and separated by noise reducing ground lines on the bottom of the board,

The .090" thick, double sided P.C. board has a full ground plane Faraday Shield on the top side to further reduce noise.

The CV Ferro-resonant Power Supply features a custom designed for GIMIX to GIMIX specs Constant Voltage. Ferro-resonant, faraday shielded, transformer that provides brown-out and overvoltage protection and permits the system to operate property, even under adverse AC power input conditions. It also includes an AC line litter and AC resonant capacitors, and GIMIX unique litter assembly board that has a clamping terminal block for easy wiring connectors. The power supply provides +8 Volts at 30 Amps, +16 Volts at 5 Amps, and -16 Volts at 5 Amps; enough to power a fully loaded system plus the two 5½." Disk drives, including Winchester types, that can be installed in the cabinet. All supply outputs are filtered and individually fused. The standard version operates over an AC input range of 90 to 140 Volts, 60 Hz. Export versions are available for inputs of 95 to 130 or 190 to 260 volts, 50 Hz.

#### 

Please see page 7 for information on optional front panel filter plates, disk regulator boards, back panel connector plates, and back panel cable sets.

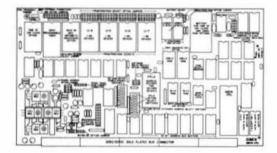
NOTE: Due to weight restrictions, CIMIX MAINFRAMES with 5." drives installed and GIMIX 8." DISK CABINETS with drives installed carried by shipping restrictions, all orders and separately or via air freight (EMERY) collect, with the drives installed. Please society the designal "indication restriction ordering, Reparations of the shipping restrict chases, all eysterists are assembled and lessed as complete units before shipping restrictions, all eysterists are assembled and lessed as complete units before shipping restrictions, and eysters are assembled and lessed as complete, units before shipping restrictions, and eysters are assembled and lessed as complete, units before shipping restrictions. All orders are assembled and lessed as complete, units of the control of the cont

Be sure to add \$30.00 for each 5.0Hz gower supply where needed

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## GIMIX 6809 CPU BOARD for the SS-50 BUS

The GIMIX 6809 PLUS CPU is an extremely versatile board that offers the user a great many features and options which make it an ideal choice for a variety of systems and applications.



- Any one of 3 memory management lechniques can be used: Straight Bank Select GIMIX Enhanced DAT w/software write project (optional) SWIPC compalible DAT (required for SBUG-E) (optional)
- Soltware write protect in 4K blocks, of the entire address space (when GIMIX enhanced DAT is installed)
- Jumper selectable progressor clock speeds (1, 1.5, 2 MHz.)
- Separate butters for the 6809 and the on card devices

- 4 PROM/ROM/RAM sockets for monitors and user software (up to 32K)
- PROM/RAM sockets individually jumper selectable for single or multiple supply voltage and 1, 2, 4 or 8K byte devices (Some FPLAs do net support 8K devices)
- 1K Dyles of scratchpad RAM
- 6840 programmable timer with provisions for external clock. gate and output connections
- Time of Day Clock (58167) w/Battery backup
- 9511A or 9512 Arithmetic Processor w/Jumper selectable 2, 3, or 4 MHz, elock speeds
- FPLA address deepding for the 8 on card devices 4 PROM/ROM/RAM sockets. 58167. 9511A/9512, 6840, 1K scratchpad RAM
- Software switching of address configurations for the 8 on card devices (allows software swifching between on board PROM/ROM/RAM resident system monitors)
- All FPLA decoded devices can be individually enabled/disabled
- FPLA decoded devices are available for DMA access
- Extended addressing for the FPLA decoded devices (can be disabled)
- Software switching between on and off board system monitors using extended addressing.
- Jumper selectable interrupts for the 6840, 58167, and 9511A/9512
- NMI input can be jumpered to the bus or to an external connector
- BA & BS jumper selectable for independent or gated operation
- User defined tatch output
- Gold MOLEX connectors for trouble free contact
- SS-50 and SS-50C compatible
- Full DMA compabilities (works with any of the 6809 DMA methods)
- Full Slow memory capabilities
- Fully assembled, tested and burned in

NOTE: GIMIX 6809 CPU BDARDS do not include a based rate generator. In systems that require a based rate generator, it must be provided elsewhere. The GIMIX 6800/6809 maintraine includes a based rate generator on the mother board

## 2 MHz 6809 PLUS CPU #05

The GIMIX 5809 PLUS CPU board has a variety of other options that may be ordered at the time of purchase or added later, it is fully socketed to allow adding the following options at any time.

GIMIX ENHANCED Dynamic Address Translation \$35.00 **\$**15.00 SWTPc Compatible DAT (required for SBUG-E) 1K CMOS Scratchpad RAM (1.5 MHz) Substitution

#### ARITHMETIC PROCESSORS

9512 (64 bit math only) 3 MHz **\$265.00** 

### GIMIX 6800 CPU BOARD

- 6800 MPU
- 4K EPROM (2708)
- 128 byte RAM
- 6840 Programmable timer (optional)
- DIP-switch EPROM addressing, compatible with most standard 6800 monitors.

\$224.03 With 5840 \$288.06

**Baud Pate** Option Add . \$ 30.00



### THE UNIQUE GIMIX 80 x 24 VIDEO BOARD

Upper and Lower Case with Descenders . Hardware Scrolling Contiguous 8 x 10 Character Cells . X-Y Addressable Hardware Cursor

IT IS THE ONLY VIDEO BOARD THAT GIVES YOU: A user programmable RAM character generator. Custom character sets, up to 128 characters each, can be stored and loaded into the board under software control, from disk, tape, etc. The ability to choose, under software control. 256 displayable characters from 384 available in the 3 on board (2 EPROM and 1 RAM). character generators

The ability to divide the 256 displayable characters into 6 groups, according to both ASCII Code and bit 6, lets your program deterrevine how each group is displayed. (Which character generator to use, and whether it will be normal or inverse video, full or reduced intensity or a egregination of these.)

GHOSTADHINE to place multiple boards at the same address and access them individually without affecting the display of the other boards. The ability to control all these features, on the fly, through software.

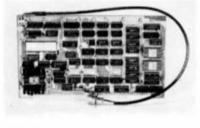
- + Fully sacketed Gold bus connectors
- Assembled, Burned In, and Tested at 2MHz

Deluxe Version with RAM Character Generator . . . \$458.76 Without RAM Character Generator . . . \$398.74 50 Hz Versions Available

Versions of GMXBUG-90/FLEX and OS-9 that use the GIMIX 80 x 24 VIDEO BOARO in place of a serial terminal are available. These versions require a user supplied video monitor and parallel ASCII keyboard. Contact GIMIX for more information.

Also Available: For Use with Master Antenna Systems. 



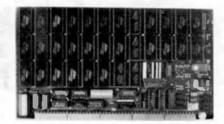


## 2MHz 64K BYTE STATIC RAM BOARD \$638.67

for 6800 and 6809 systems using the SS-50/SS-50C bus

56K																				\$578.57
48K													,							\$518.47
32K			•											-		٠	6			\$398.37
24K			•						•						٠					\$348.27

All versions have gold bus connectors and are fully socketed, assembled, burned in, and tested. Versions with less than 64K can be expanded at any time by adding additional RAM chips.



#### **FEATURES:**

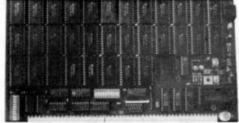
- \* ADDRESSABLE in two 32K sections with separate regular and extended address decoding for each section. Each section can be addressed to any 32K boundary in the address range (1M Byte with extended addressing). Each 32K section is divided into four 8K blocks that can be individually enabled or disabled. Disabled sections do not occupy address space.
  - ★ FULLY STATIC MEMORY does not require complicated refresh timing or clocks for data retention. Compatible with any of the 680076809 DMA techniques.
  - ★ GUARANTEED 2Mhz. OPERATION uses high speed (200 ns.) memory with no wait states or clock stretching required.
  - ★ LOW POWER NMOS RAM requires less than 3/4 AMP (750 ma) typical at 8V. for a fully populated 64K board.

Also available ...

## NON-VOLATILE 64K BYTE CMOS STATIC RAM BOARDS with BATTERY BACK-UP With all the versatility of the above boards...PLUS!

- NON-VOLATILE MEMORY with built in battery back-up. Retains data even with system power removed. With the battery fully charged, data remains intact for a minimum of 21 days.
- ULTRA-LOW POWER CMOS RAM requires less than 1/4 AMP (250 ma.) typical at 8V for a fully populated 64K board.
- LOW BUS VOLTAGE DETECTION Inhibits memory access during power up and power down to prevent takes writes to the memory.
- ★ WRITE PROTECT SWITCH permits the entire board to be write protected for PROM/ROM emulation and software debugging.

64K..\$798.64 - 56K..\$728.56 - 32K..\$518.36



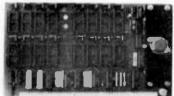
All above RAM Boards are guaranteed for 2MHz operation.

## 16 SOCKET EPROM/ROM/RAM BOARD

#### WITH EXTENDED ADDRESS DECODING

For Use With: Existing SS50 Systems and SS50C Extended Address Systems FEATURES: Up to 128K on a single board (using 8K devices)

Can be used with 2, 4, and 8K 24 pin, 2716/2516 pinout, single supply voltage EPROMs and most pincompatible ROMs and static RAMS.



- · Device sizes and types can be mixed on the same board
- 2 separate 8 socket sections
   DIP-switch selection of base address for each section including extended address decoding Bi-polar PROMs for address decoding allow mixing of device sizes within a section Separate slow memory generation for each section. (6809 only)
- Each socket is jumper programmable for device size and type (2, 4 or 8K PROM/ROM/RAM)

ASSEMBLED, BURNED-IN, AND TESTED

Fully Socketed
 Gold Bus Connectors

..\$238.32

**\$98.34** 



#### 8K PROM BOARD......

- Holds eight 2708 or 2708-compatible ROMS.
- . DIP-switch addressable to any 8K boundary.

Gold Bus Connectors

### HIGH RESOLUTION BIT MAP GRAPHICS BOARD SET

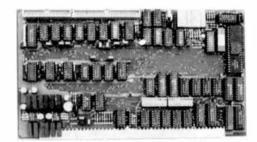
FEATURES: — 512 x 512 Dot resolution — A board set consisting of the Graphics Controller Board and the Screen Memory Board (32K of memory) — Does not tie-up the processor or system bus for screen refresh — Occupies 8K of address space plus 8 bytes for control ports — Separate DIP-switch selection for screen memory and control port addressing — GHOSTability allows multiple boards to be placed at the same address and be enabled/disabled under software control — Extended address decoding for SS50C extended address lines

ASSEMBLED BURNED IN AND TESTED ......\$996.7

NOTE: This Graphic Board Set requires a high resolution video monitor such as the MOROTOLA M4408 with a 30KHz horizontal scan rate.



Inc. 1337 WEST 37th PLACE • CHICAGO, ILLINOIS 60609 • (312) 927-5510 • TWX 910-221-4055



#### GIMIX DMA DOUBLE DENSITY DISK CONTROLLER #68

The GIMIX DMA (Direct Memory Access) DISK CON-TROLLER has the capabilities needed to realize the full potential of todays sophisticated multi-user/multi-tasking operating systems such as OS-9<sup>TM</sup> and UniFLEX<sup>TM</sup>.

HIGH SPEED using bi-polar logic DMA circuitry for guaranteed operation at 2MHz. DMA transfers take place at full bus speed using 6809 cycle steal DMA. Once the required parameters are passed to the controller and DMA transfer is initiated the processor is free for other tasks. Interrupts can be generated to indicate the completion of the transfer.

SINGLE AND DOUBLE DENSITY data storage on any combination of 51/4" and 8" floppy disk drives; single and double headed, single and double track density, up to 4 drives total.

LOW ERROR RATES are insured by a data recovery circuit (data separator) and adjustable write precompensation circuitry for drives that require precomp. Separate precomp adjustments are provided for 51/4" and 8" drives.

ADDRESSABLE to any 8 byte boundary in the address space (1M byte when extended address decoding is used). The board occupies only 8 bytes of address space.

EXTENDED ADDRESSING control using the SS-50C extended address lines. Control of the extended address lines allows the board to perform DMA transfers to and from any address in the 1M byte address space.

FULLY BUFFERED with separate 5 1/4" and 8" output buffers and schmidt trigger input buffers for the disk drive signals.

The DMA controller leaves the processor free to perform other tasks once the transfer is initiated, unlike programmed I/O disk controllers which require full time use of the processor during data transfers to and from disk.

This is extremely important in a multi-user/multi-tasking environment as the processor can perform other tasks such as console I/O while a disk transfer is in progress

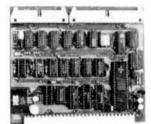
#### GIMIX DOUBLE DENSITY PIO DISK CONTROLLER #28

The GIMIX DOUBLE DENSITY PIO (PROGRAMMED I/O) DISK CONTROLLER is a versatile floopy disk interface for use in 6809 systems on the SS-50 or SS-50C bus. The board physically occupies one slot of the 30 pin I/O bus.

- Double the unformatted storage capacity of single density controllers
- · Single and double density operation
- Phase lock data recovery circuit (data separator)
- · Adjustable write precompensation (precomp)
- Controls up to four 5 1/4" drives
- Controls single and double headed drives
- Designed to meet the data hold-time requirements of the Western Digital 1797 floppy disk controller I.C.

The GIMIX DOUBLE DENSITY PIO DISK CONTROLLER is ideal for systems that require greater data storage than that provided by single density controllers, without increasing the number or type of drives. In most cases existing 6809 systems can be upgraded by adding only the controller and the appropriate operating system software.

## GIMIX 5/8 DISK CONTROLLER BOARD #58



The GIMIX 5/8 DISK CONTROLLER is a versatile floppy disk interface for use with both 6800 and 6809 systems on the SS-50 or SS-50C bus. The board physically occupies one slot of the 30 pin I/O bus.

- Hardware and software compatible with existing disk controllers (SWTPc DC-1, DC-2 and DC-3)
- Controls up to four 5%" drives in 6800 systems
  Controls any mix of 5%" and 8" drives, up to four drives total, in 6809 systems
- Provides for double headed drives
- Synchronous data separator for data reliability
- Designed to meet the data hold-time requirements of the 1771 floppy disk controller I.C.

The GIMIX 5/8 DISK CONTROLLER is ideal for a variety of applications including the replacement of controllers in existing systems. As a replacement it can provide the added advanteges of a data separator, double headed drive capability, and in 6809 systems the ability to use 8" drives. Double headed drives and 8" operation may require appropriate operating system software.

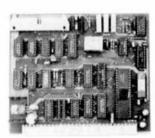
#58 fully	y assembled, burned in, end tested\$226.58	
ALSO	As above, but for 5 1/4" drives only	\$198.48
ILABLE:	As above, but without 1771, tested, not burned in	. \$158.38

NOTE: When ordering disk controllers please specify the make and model of the drives being used.

#### 5¼" DRIVES INSTALLED IN GIMIX SYSTEMS with all necessary cables SINGLE DENSITY DOUBLE DENSITY

	Formatted	Unformatted	Formatted	Unformatted	
40 track (48TPI) single sided	199,680	250,000	341,424	500,000	2 for \$700.00
40 track (48TPI) double sided	399,360	500,000	718,848	1,000,000	2 for 900.00
80 track (96TPI) single sided	404,480	500,000	728,064	1,000,000	2 for <b>900.00</b>
80 track (96TPI) double sided	808,960	1.000.000	1,456,128	2,000,000	2 for 1300.00

CHART SHOWS TOTAL CAPACITY IN BYTES FOR 2 DRIVES.



## SOFTWARE AVAILABLE FOR GIMIX DISK SYSTEMS

GIMIX VERSIONS OF TSC's 6809 FLEX operating systems are available for all three GIMIX disk controllers. They fully support all the features of each controller and are software compatible with other versions of FLEX. GiMIX FLEX includes a disk FORMAT program that allows the user to pick the number of tracks to format, single or double sided disks, and where appropriate single or double density. It also supports both single (48 TPI) and double (96 TPI) track 51/4" drives and allows 80 track (96 TPI) drives to read, write, and

format 40 track (48 TPI) disks. FLEX is single user and limited to 56KB systems. Specify controller and type of drive: 8"; or 51/4" 40 or 80 track

NOTE: FLEX requires a system monitor (e.g. GMXBUG or S-BUG E). When used with a SWTP CPU and S-BUG E and the GIMIX #68 DMA CONTROLLER, the GIMIX BOOTSTRAP PROM is also required.

GMXBUG 09 includes advanced debugging capabilities as well as utility and memory manipulation routines. The standard terminal based version can be upgraded to video based for use with the GIMIX 80 x 24 Video board by changing the bootstrap PROM to the Video/bootstrap Prom. It can be used with either GIMIX DAT or SWTP DAT, but they are not required.

Price Includes PROMs, Manual, and Source listing (Specify DAT)..... 

GIMIX' versions of MICROWARE's 059 Level 1 are available for all GIMIX disk controllers. OS9 includes PROMS and Disk. Microware's OS-9 Debugger is also included. Level 1 is multi-user, but limits user to 56KB Specify controller and type of drive; 8"; or \$195.00

#### SYSTEM SPECIAL \* GIMIX offers you GMXBUG/FLEX/OS-9 selectable under software control. See System prices elsewhere in this brochure.

UNIFLEX is available for QIMIX Systems using the GIMIX 6809 CPU board and the #68 DMA Controller with 8" drives, it reguires a minimum of 128KB of RAM. A signed license agreement with TSC is required before shipping. The SWTP DAT parts must be installed on the GIMIX CPU.

UNIFLEX ..... \$550.00 GIMIX boot PROM for UNIFLEX ..... \$50.00 MICROWARE'S OS-9 Level 2 requires a minimum of 128KB of RAM. The GIMIX DAT parts must be installed on the GIMIX CPU. 

#### A WIDE VARIETY OF LANGUAGES AND OTHER SOFTWARE IS AVAILABLE FOR THESE 6809 DISK OPERATING SYSTEMS FOR MICROWARE'S OS-9 LEVEL 1 & 2:

Macro Text Editor         \$125.00           OS-9 Assembler         125.00           BASIC09         195.00		OS-9 PASCAL         \$400.00           OS-9 C Compiler (Available Soon)         400.00
FOR TSC's FLEX 6809 Native-Code Pascal Complier \$200.00	Sort/Merge \$ 75.00	Standard Basic Precompiler \$ 50.00
	6809 Debug Package	
	6809 Diagnostics Package	
Text Processing System 75.00 Text Editing System 50.00		68000 Cross Assembler
FOR UNIFLEX	1 Year Maintenace Inclu	ded on all Uniflex Prices.
UniFLEX Operating System (6809) \$550.00	UniFLEX Sort/Merge \$150.00	Fortran 77 (requires relocating assembler) \$350.00
UniFLEX Basic	UniFLEX Pascal 300.00	6809 Relocating Assembler & Linking Loader 175.00
UniFLEX Basic Precompiler	UniFLEX 68000 Cross Assembler	Fortran & Relocating Assembler (pkg. deal) 450.00
UniFLEX Text Propessor	Enhanced Printer Spooler	

The above software is from MICROWARE and TSC. Numerous offerings of languages (e.g. C, PASCAL, FORTH), utilities (e.g. spelling dictionaries, cross assemblers, disassemblers) and application packages (e.g. word processing, data base management, accounting), are available from many other software houses.

8" DISK CABINET and POWER SUPPLY. The cabinet features the same quality, styling, and finish as the GIMIX MAINFRAME and mounts two standard size 8" floppy and/or winchester disk drives. It will also hold 4 thinline 8" floppys or a combination of 2

thinline floppys and an 8" winchester.

To provide an easy means of controlling the power to an entire system from one switch, three accessory outlets, one for the computer and two for peripherals (terminals, printer, etc.), are provided. The back panel mounted power switch selects either OFF, ON, or the AUTO mode. In the AUTO mode, the power supply and two of the accessory outlets are controlled by the computer (or other device), connected to the third accessory outlet.

controlled by the computer (or other device), connected to the third accessory outlet.

When the computer is turned on or off, the cabinet senses the presence or absence of current flow to the computer and turns Itself and the other accessory outlets on or off. Circuitry Is also provided to turn AC drive motors ON and OFF under computer control. A built in fan with a washable air filter provides cooling for the power supply and drives. The back panel Is punched for 4 connectors (two 50 and two 20 pin) for connections between the cabinet and the computer.

The power supply uses a constant voltage Ferro-resonant transformer for reliability and protection against brownouts and power line noise. It provides +5 Volts at 6 Amps, +24 Volts at 6 Amps, and -5 Volts at 750 Ma. continuously; with ample surge capacity for

drives that require higher starting currents. The supply has two separate 24 V. outputs that can be sequenced to delay starting of the

second drive until the first is up to speed.

All units are fully assembled, burned in, and tested.

8" DUAL DRIVE DISK SYSTEM: includes two double sided 8" disk drives, cabinet, power supply, and all necessary cables to connect to a GIMIX MAINFRAME or controller (see shipping notes on page 8).

\$2598.88
8" DISK CABINET ONLY: Includes power supply and AC & DC power cables Note: Because different drive models require different AC For 50 Hz Export power supply, add DRIVE CABLE: for 8" floppy drives includes connectors for the disk drives and a back panel connector for the 8" disk cabinet. 367.84

with 4 drive connectors.

MAIN FRAME CABLE: for use with the above cable; to connect the disk cabinet to GIMIX MAINFRAMES and disk controllers...\$45.81 \$14.83

## GIMIX 2MHz INPUT / OUTPUT BOARDS

**SERIAL INTERFACE BOARDS** All GIMIX serial interface cards use the versatile 68B50 programmable ACIA that provides software control over: number of data bits, parity, stop bits, and interrupts; plus a full set of error and status flags. They all feature RS-232 compatible input/output with RTS. CTS. and DCD handshake signals. The GIMIX SINGLE PORT serial interface also has 20 Ma. current loop output for use with GIMIX RELAY DRIVER BOARDS, teletypes, etc.

All serial boards have gold plated, header type connectors for corrosion resistance and reliable operation.

PARALLEL INTERFACE BOARDS All GIMIX parallel boards use the 6821 PIA for compatibility and versatility. Each 6821 provides two 8 bit ports with a variety of handshake and interrupt generation modes.

Optional cable sets are available to provide 25 pin "D" type data connectors for back-panel mounting.

#### SINGLE PORT SERIAL INTERFACE

DIP-switches provide full control over I/O and handshaking configuration — easily accessible, no soldering necessary for:

- AS-232 or Current Loop select
- . One of five baud rates or an external clock
- . Optional connection to the Interrupt Request tine
- Override of the DCD and CTS modem control signals

On-card regulators for +5, +12, and -12 volts provide power at the connector for modems, cassette interlaces, etc.

RS-232 and current loop drivers and receivers keep output from the GIMIX Serial Interface powerful and clean.

#### OTHER FEATURES INCLUDE:

- Modem Control Signals has data carrier detect and clear to send inputs.
- Cassette Interlace Control has a diode-protected external clock input and a separate clock output.
   Secondary RS-232 input and output channels
- . Current loop input and output . Reader Control output . Request to send output

#### TWO PORT PARALLEL INTERFACE CARD

#### EACH PORT HAS:

- Eight data I/O lines fully buffered, with Schmidt-frigger inputs for high noise immunity
- DIP-switch setection, of either input or output
- its own buffered input handshaking line
- its own buffered output handshaking line that is strappable for input.
- OIP-switches for connecting to the Interrupt Request or the Non-Maskable Inerrupt lines.
- Its own professional-quality gold-plated header connector
- Gold Bus Connectors
- Its own DIP-socket for connecting to boards that need an external 8-bit or output port such as the GIMIX Opto board.
- On-card regulators for +5 and 12 volts provide power at the connectors for keyboards, tape readers, etc.

#### 

Solderless jumpers provide easy selection and changing of options.

#### FEATURES

- 2 separate RS-232 ports (with handshake) on a single board
- Jumper programmable connector pinouts for easier connection to external devices. (Connector can be programmed as DCE or DTE)
- Provides direct plug-in of standard RS-232 connectors when used with optional GIMIX cable sets.
- Individual baud rate and interrupt select jumpers for each port.
- Selectable for use with 4, 8, or 16 addresses per slot.

#### 8 PORT SERIAL BOARD

(For the 50 pin bus)......\$318.46

The GIMIX 8 PORT SERIAL INTERFACE has 3 header type connectors for external connections. The center connector provides Transmit Data, Receive Data, and signal ground for all 8 ports. The outer 2 connectors each provide TX, RX, and signal ground as well as the 3 handshake lines RTS, DCD, and CTS for 4 ports.

#### FEATURES

- \* 8 separate RS-232 ports (with handshake) on a single 50 pin board
- Extended address decoding for the SS50C bus
- · Occupies only 16 bytes of address space
- \* DIP-switch addressable to any 16 byte boundary
- \* Individual OIP-switch selectable baud rates and interrupts for each port
- · On board buad rate generator for baud rates from 75 to 38.4K baud

### **8 PORT PARALLEL INTERFACE BOARD**

(For the 50 pin bus)......\$198.45

- Eight 8 bit parallel ports on a single board
- Four 6821 PIAS
- 3 ports bullered for output
- 5 ports bi-directional (not buffered)
- Buill in interrupt generator outputs 1 second or 1 minute interrupts
- · Occupies 16 bytes of address space
- DIP-switch addressable to any 16 byte boundary



#### CABLE SETS FOR ALL ABOVE BOARDS . . . . ea. \$22.95

Cable sets include: Ribbon cable with a matching connector for the I/O board, a 25 pin "D" type data connector for back panel mounting, and mounting hardware.

(Please specify which board when ordering cable sets)

GIMIX UNIVERSAL SYNCHRONOUS & ASYNCHRONOUS SERIAL I/O BOARDS. This 30 pin board is available in three versions: with a 68850 ACIA, a 68852 SSDA (Synchronous Serial Data Adapter) or a 68854 ADLC (Advanced Data-Link Controller). Control logic is provided for loop mode operation of the 68854 ADLC. All three feature jumper selectable RS-232C or RS-423 (single-ended), or RS-422 (Differential) line drivers and receivers for the

Receive data, transmit data, external clock, and handshake signals. External connections can be made through the 26 pin header at the top of the board or, when used with an optimal GIMIX cable set, a 25 pin "D" type data connector. The jumper programmable I/O connector phosits can be arranged to suit a variety of interface configurations.

with 68850 ACIA (\$244.50) with 68852 SSDA (\$254.52) with 68854 ADLC (\$268.54)



Control 31 Separate AC Circuits (20 amps max. ea.)

## RFI AY DRIVER BOARDS FOR A.C. **POWER CONTROL**

4 Boards (124 relays) can be connected to one 20 ma. current loop. Each board controls 31 G.E. RR8 relays.

Use multiple serial ports for additional groups of 124 relays.

SIMPLE TO CONNECT Only two pairs of wires coming from your computer are needed for each set of four Realy Driver Boards, these wires may be the standard telephone type.

REMOTELY LOCATABLE. Relay Driver Boards can be conveniently located for A.C. power distribution — away from the computer and other Relay Driver Boards. The board operates in either the active or the report mode, as specified by the computer. In the active mode, the board interprets the 8-bit data received as a command to turn on or off a particular relay. Following abrief interval to allow the selected realy to operate, the board senses that relay's status (on or off). If the status is other than expected, the computer takes appropriate action, as determined by the program. A command received in the report mode has the same results, except for relay activation. This allows the mode to check relay status at any time.

If the on-board UART detects a transmission error, such as in framing, parity, or overrun, no relays are activated and no status scan occurs.

Clamping terminal blocks for wiring simple SPST-N.O. momentary contact remote switches to individual relays or groups of relays, both on and off, provide manual control as in a normal low voltage switching system, even without the computer. In event of power failures, the relays will remain in the same state that they were in when power is restored. DATA rates up to 1200 baud, allow operating up to 120 relays per second on each port. COMPACT — Only 24" x 5"

Distances and operation of boards and relays are dependent upon wire length and gauge, and type of transformer.

#### RELAY DRIVER BOARD ACCESSORIES

MOUNTING BRACKET \* custom designed to hold a Relay Driver Board and 31 relays. The bracket (26" x 81/4" x 4") and transformer will fit in a standard electrical cabinet (extra room needed for wiring) creating a neat and easily installed system.

TRANSFORMER \* 2 Amp., 24 volts. Custom manufactured to our specs for powering a Relay Oriver Board and 31 G.E. RR8 relays.

G.E. RR8 RELAYS \* 24 volt, split coil, mechanical latching type. Once ON they stay ON (drawing no current) until they are powered OFF, and vice-versa. Each relay can handle 20 AMPS for switching lights, motors, machinery, etc. up to 277 V.A.C. - UL listed.

#### **PRICES**

RELAY DRIVER BOARD ONLY	TRANSFORMER
8RACKET \$ 38.21	RELAY DRIVER PACKAGE
	(Relay Driver Board, 31 RR 8 Relays, Bracket and Transformer).

#### OPTO-BOARD FOR REMOTE SENSING ....

Links any computer to 34 Outside-World Signals safely Inputs isolated to 1500 volts Perfect for detecting closure of switches and relays Built-in Debouncing.

Signals may range from 5 to 24 volts D.C.

Can detect signals sent by devices such as wall switches, hidden floor switches, electric eyes, alarms, smoke detector, thermostats, and a multiplicity of other applications.

All switch ports are constantly scanned by an on-board circuit. No processor time is required. A built-in memory buller saves up to 64 closed-switch signals, permitting the processor to complete lengthy tasks between interruptions. FULL HANDSHAKING LOGIC: DATA READY output DATA ACCEPTED input BUFFER FULL output RESET input

ALL OUTPUTS ARE BUFFERED AND TTL COMPATIBLE

#### PARTS AND CABLE SETS FOR GIMIX BOARDS AND SYSTEMS

BAUD Rate Generator Board \$88.93	5" Disk Cable Set
GIMIX double disk regulator with two 4 amp regulators	I/O Cable Set, each (specify board)
to provide power for 5 1/4" drives	GIMIX 2" D Ring Binder
Filler plates (when no 5" drives are used), 2 required 14.92	GIMIX 3" D Ring Binder
Missing Cycle Detector	
8" Disk Cable and Back Panel Connector Set 29.25	Choice of: Blank; SO-239; BNC; 20 & 50 Pin Header;
8" Disk Cable Set	34 & 40 & 50 Pin Header. Connectors not included 8.60

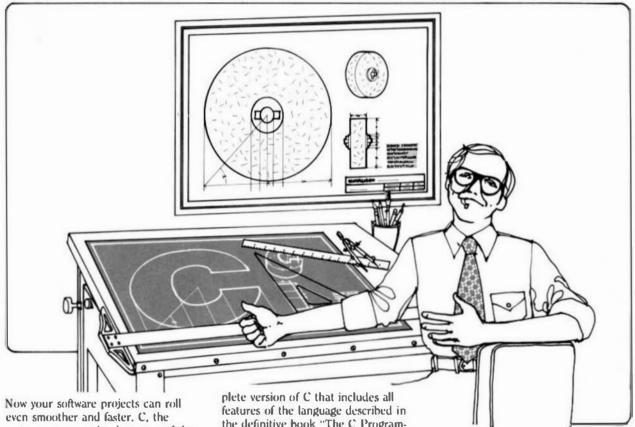
#### GIMIX 50 PIN PROTOTYPING BOARD

- 16 rows of pads on 100 x .300 centers: up to 72 fourteen pin ICs.
   Accepts 4 TO-220 regulators; 2 on the +8V & 1 ea, on the +/- 16 V lines.
   Accepts standard 6. 8, 14, 16, 20, 24, 28, and 40 pin DIP devices.
   Provisions for decoupling caps distributed throughout the array.
- Double sided with plated thru holes and gridded power and ground lines.
   Pads for solder connections or .100 center headers on all 50 bus lines.
- The entire top edge has pads for .100 x .100 header (ribbon) connectors. Can be used with wire wrap, wiring pencil, solder wiring, etc. With gold bus connectors and heat sinks — unassembled



## C:

## The Greatest Invention Since The Wheel



Now your software projects can roll even smoother and faster. C, the systems programming language of the future, is here today for Microware's OS-9 Operating System. Professionals rave about C because it's a structured language that can handle the most demanding real-time applications or painlessly produce simple system software.

## When performance counts ...

Few languages can match C's outstanding ability to produce fast, compact native code. In fact, it is one of a very few languages that is truly efficient enough to be used to produce operating systems, critical real-time programs, and compilers. Because of the richness and variety of C operators and the way they naturally combine, complex functions require less code. Plus the 6809 architecture makes it a superior C machine.

#### Complete and standard . . .

Microware's new C compiler is a com-

plete version of C that includes all features of the language described in the definitive book "The C Programming Language" by Brian Kernighan and Dennis Ritchie, OS-9 C features: preprocessor with conditional compilation; complete standard function library; char. int. long, and float data types: pointers, register variables, arrays, structures, and unions; one-pass compilation; and assembly language source code output.

## The bridge to Unix and the future . . .

Because Microware's C compiler has essentially all features of Unix C, and because the OS-9 operating system is a Unix-type operating system, C programs readily move between OS-9 and Unix. And it is becoming apparent that C will be the preferred programming language for all popular 16-bit microcomputers, As a result, software written in C is inherently protected against processor obsolescence and is assured port-

ability to all latest-generation microprocessors including the 68000.

## Plus the OS-9 connection . . .

C is the latest member of the broadest line of 6809 software tools in the industry: Microware's OS-9 family. All OS-9 system functions are directly callable from C programs. The C compiler utilizes the standard OS-9 Text Editor and Assembler, and can process data files used by Basic 99, Pascal, and Cobol.

Write or call for our free catalog. We accept phone orders and MasterCard and VISA orders.

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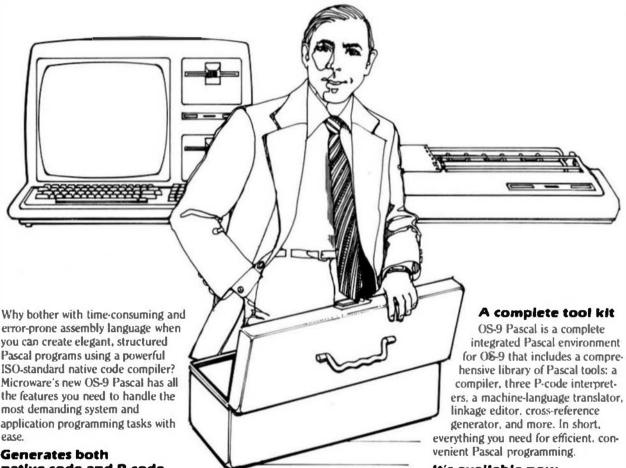
## MICROWARE.

Microware Systems Corporation 5835 Grand Ave., Des Moines, IA 50312 (515) 279-8844 Telex 910-520-2535

OS-9 Seminar May 14-16, 1982 Contact MICROWARE For Further Information

## **OS-9 PASCAL:**

## **A New Programming Tool** For Experts



## native code and P-code

With OS-9 Pascal you don't have to make that difficult choice between easy-to-use P-code Pascal or fast native-code Pascal. You can compile your Pascal program to pure 6809 assembly language source code. OS-9 Pascal performs extensive local and global code optimization which results in incredibly fast and compact machine language programs. Or if you prefer, OS-9 Pascal can generate P-code for interpretive execution to simplify program debugging and testing. There's also a Virtual Memory P-code Interpreter that can run huge Pascal programs that other microcomputers can't touch. In fact, you can run programs using any combination of P-code, compiled machine language, or handwritten assembly language procedures.

#### **ISO Standard Pascal Plus**

OS-9 Pascal conforms to the ISO industry standard for Pascal, so you are assured of portability to or from any other computer that uses standard Pascal. OS-9 Pascal protects your software investment and gives you access to a vast body of existing Pascal software. Beyond the standard, we've added natural extensions to OS-9 Pascal to make it even more versatile. such as: relaxed identifier syntax: separate procedure compilation; random access file and interactive I/O; bitwise logical operators; runtime error handling; and much more. And because it runs under OS-9, it is inherently multiuser and multi-

#### It's available now

OS-9 Pascal is now available off-theshelf in all OS-9 disk formats. It can be used on any disk-based 6809 computer running OS-9 Level One or Level Two. Each OS-9 Pascal package includes the compiler, machine language translator, P-code interpreters, runtime support packages, linkage editor, demonstration programs, and a comprehensive 120-page User's Manual. Write or call for our free catalog. We accept phone orders and MasterCard and VISA orders.

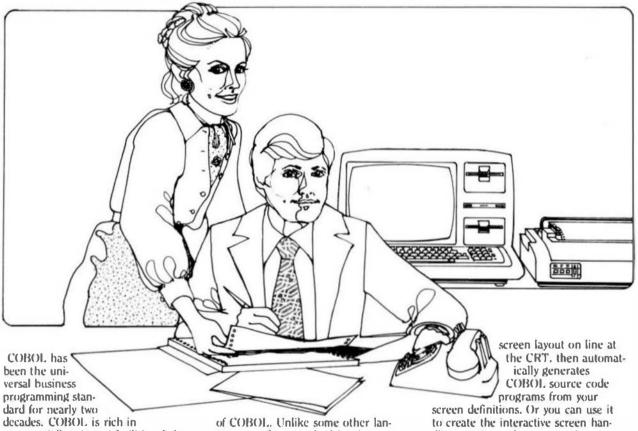
OS-9 Pascal and OS-9 are trademarks of Microware.

## MICROWARE.

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## CIS COBOL:

## **The Natural Choice** For Business Software



commercially-oriented facilities. It has powerful file handling, formatted printing, and data structure capabilities. It is English-like, so that programs are easy to read and maintain. By far, most organizations use COBOL as their main business programming language. One effect of this is that more professional business programmers know and use COBOL. and the best business application software is written in COBOL.

Microware has developed the 6809 version of Micro Focus's proven CIS COBOL compiler to allow you to run ANSI 1974 standard COBOL on your 0S-9 based computer system. It's been certified as such by the U.S. General Services Administration, following stringent testing. This assures that CIS COBOL is compatable with standard minicomputer or mainframe COBOLs. And CIS COBOL has been proven on thousands of micro and mini systems all over the world.

Stability is an important advantage

guages, a firm standard has been established. Because of this. COBOL programs can be transferred from one machine to another with a minimum of modifications. COBOL users can take advantage of the mass of existing programs written in COBOL.

CIS stands for Compact. Interactive, and Standard-the most desirable qualities for microcomputer COBOL. And CIS COBOL offers you much more! It has been specially designed for interactive operation and efficient use on small computers, CIS COBOL has multi-user capability that allows more than one COBOL program to be run simultaneously, CIS COBOL extensions for conversational applications, screen control, interactive debugging, and OS-9's device-independent I/O system.

CIS COBOL's optional FORMS 2 program generator eliminates the need to write simple data entry and inquiry programs. It lets you build a

dling portions of more complex pro-

CIS COBOL and FORMS 2 can be used with any disk-based 6809 computer system having at least 48K of user RAM running Microware's 0S-9 Level One or OS-9 Level Two operating systems.

If you need to create business applications. COBOL is your natural choice. And if you want to run COBOL on your 6809 system-or want easy to use interactive business programming facilities—that means 6809 CIS COBOL.

Write or call for our free catalog. We accept phone orders and MasterCard and VISA orders.

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## **EPSON DOT MATRIX PRINTERS** The Epson MX-100 The Epson MX-80



80-Column \$495.00

MX-70

\$395.00



136-Column \$798.00

**MX-80 FT** 

\$625.00

## FACTORY FRESH—LATEST PRODUCTION—BRAND NEW

SWTPC-Motorola, MP32 Dynamic Memory Board

## **ONLY \$199.00**

Shipping - Disk Drives or Cabinets with Power Supply, Add \$7.50 each

> Memory Boards or Cables, Add \$4.50 each

> Printers - Epson Add \$7.50 Other Freight COO

### Assembled & Tested

Thousands sold at \$650.00 Save over \$450.00 1 MHZ - No extended addressing Can be set up for \$0-7FFF or 8000-FFFF Limited Quantity (less than 50)

#### \*\* DISK DRIVES & CABINETS W/PS \*\*

5 1/4"

TEAC - Single Sided, Double Density, 40 Track....\$249.95

TEAC - Double Sided, Double
Density, 40 Track.....\$349.95

CABINET - Single Drive with Power Supply..... 79.95

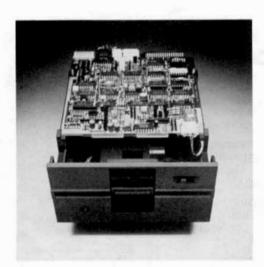
CABINET - Double Drive with Power Supply..... \$ 99.95

CABLE - Single Drive.....\$ 24.95

CABLE - Double Drive.....\$ 34.95

NOTE - When ordering cables please specify S50 Bus or Other1!!

Call or write for disk controller Board information.



## SOUTH EAST MEDIA

P.O. Box 794 Chattanooga TN 37443

1-615-842-4601

## DATABASE MANAGEMENT

DATAMAN + combines the capabilities of DATAMAN, DATARAND and DATACALC. If has been redeveloped from the ground up after over 12 months of experience with the DATAMAN system. DATAMAN + is a powerful RANDOM Database Menegement system under FLEX, UNIFLEX and OS-9.

DATAMAN + is for BUSINESS systems with 56K user ram. You can use DATAMAN + for inventory control, work scheduling, mailing lists, sales reports and much more. The powerful report writer lets you create invoices, statements, form letters, and any other type of report your business needs. You can perform calculations with your data and print out the results. Special printer handling allows use of any size column output, not just 80 and 132.

DATAMAN + is password protected at the menu level so that redundent password promots are eliminated, we've added the human fouch with the use of the operators name and calculator style input. DATAMAN + checks for valid data types on input thus eliminating erroneous data in your database.

DATAMAN + is report writer has added intelligence so that separate select programs need not be run to create different reports from the same database. As a matter of fact, the report writer is so lexible that you can use it to create invoices, statements, even form tellers using data from the database, You can even perform calculations with the data and put the results in the report;

Setting up your system to run DATAMAN + (s very easy and eutomatic. The entire system has been designed with the interpreted user in mind. The operation of the system is 50 easy that atthough a manual is provided none is required to run OATAMAN +...

It's easy to create databases and reports with DATAMAN + Full editing capability has been added to make it a snap, DATAMAN + is the lirst truly RANDO DBM system to allow any size record and any number of fields. fields

Modifying DATAMAN + is easy because DATAMAN + comes with every fine of source on disk! and an eas. to use manual with sections on each program for the programmer who wants to make modifications or customize it.

An upgrade will be available for users with DATAMAN, FL X version available now, \$199.95
UniFLEX and OS9 versions soon thereafter.



#### ONE PROGRAM THAT DOES IT ALL!

#### THE BILL PAYER SYSTEM™

THE BILL PAYER is a package of 10 menu driven programs in TSC Extended Basic. This powerful system response to the process of your bills. You can create a vendor list, enter involces to be peld, generate reports about them, print your checks and much more. Uses random access files.

Explore Package now included at the same gride.
THE PURCHASE ORDER system adds purchase orders. to the BILL PAYER. This package of programs adds another level of cohilot to your expenditures. Prints out purchase orders and keeps track of purchases. Requires the Bill Peyer to work.

INCOMEJEXPENSE LEDGER. This valuable package is most appreciated at tay time. Allowe up to 99 income and expense numbers. Tiga into the PURCHASE ORDER system, and the Bitl Pa er.

Includes manual and source supplied on draw in TSC Ex-

landed Basic.

THE BILL PAYER \$89.95 BUY ALL THREE AT ONCE PURCHASE ORDER \$49.95 FOR \$109.95 AND SAVE \$19.90

#### SOME COMMON BASIC PROGRAMS In TSC XBASIC

76 XBASIC programs at less than \$1 each. Stand alone or used as subroutines in your applications. These 76 programs are converted from the OSBORNEIMoGraw-Hill book to TSC XBASIC. They support your printer, are fully tested and debugged. Use the AppleII or Wang version of the book (not included) for documentation. The programs are very well commented with a large number of REMarks for easy use. The source is included on the disk. Supplied on TWO 5 inch or ONE 8 inch disk.

76 programs for only \$69.95

### READTAPE

#### READ TRS-80 LEVEL II BASIC TAPES

This program, with an easy to make interface" will read TRS-80 LEVEL II BASIC tapes and convert the programs to TS-8 6ASIC. Those things that can't be converted are fragged so that you can lind them easily with the TSC test editor. Noe you can use all that TRS-80 software out till week. 6809 asembly language includes acurces on DISKI \$54.95

'(instructions and schematic included - cost about \$2 to

#### **PLOT**

Now you can have GRAPHICS added to all your programs. Just write the data out to a virtual array and call PLOT. PLOT is written in TSC XBASIC and the source is included on the disk thriente RESOLUTION GRAPHICS ON YOUR TERMINAL OR PRINTER. HISTOGRAMS, BARGRAPHS, XY PLOTS PLIC TYMERS.

PLUS OTHERS.
IN TSC XBASIC SOURCE INCLUDED ON DISK, \$44.95

6502 TRANSLATOR Translator 6502 code to 6809 \$75.00

**INVENTORY** with MATERIAL **REQUISITION PLANNING** \$100,00

SUPER SLEUTH Disassembler for 6800/6809 or Z60 \$99.00

> **TABULA RASA Electronic Spreedsheet** \$100.00

#### SOFTWARE CATALOG

	OBJECT	ALUTA I	•
1440 2000	ONLY	WITH	
PROGRAM DATAMAN +	UNLY	SOURCE	
BILLPAYER		199.95	×
*Purchase Order		89.95	×
		49.95	×
'Income/Expense		49.95	×
PLOT		169.96	
		44.95	
TABU A RASA		100.00	
Mailing List		99.95	×
Forms Display		49.95	×
Inventory with Material Requisition Planning		.00 00	
Some Common BASIC Program	_	100,00	K
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#### FLEX COMPATIBLE FORTH

### BY Chuck Ester, Ph.D. K-PORTH NOTES

If you are considering buying FORTH, You are probably trying to decide which of the two available for 687X to choose. Here are some of the major differences between the two. Unlike the other FORTH, X-FORTH runs in the FLEX (or 0.5) environment and uses the same files as any other FLEX grogram, which makes it compatable with other programs or utilities you may have.

X-FORTH at \$149.96 is note or less the same package as the other FORTH costing \$250.00 X-FORTH is about 25% faster, although exact timing tests hav nit been run yet. It is faster because many of the important things are coded in assembler, not high level FORTH.

X-FORTH documentation is undoubledy the best available for any FORTH on any computer. The manual is divided into lour major sections: (1) Tutorial on FORTH in general; (2) Ext in sions added for filex; (3) Users manual; (4) Giossary which lists alphabetically all the words described in it users manual with empted description.

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FORTH FOR THE TAS-80 COLOR COMPUTER BISK SYSTEM TYPING to get control of your Color Comput ??? Tired of translating HCX to decimal?? Tired of remembering where the VDG and SAM are end how to program them?? Want to write machine language code with assembly language mnemonics instead of POKES??

Want to write programs in half the time?? Want to write role of small pieces of code that you can put together in seconds to do BIG JOBS??? Want to language that is at least 5 to 10 times taster than BASIC??? Want to team everything there is to know about FORTH, with the best manual on the market. I cituding lots of examples of FORTH applications, and detailed explanations of how everything works??



#### COLOR TOOLKIT

Utility and Diagnostic Disk Programs by Dick Bartholomew

The COLOR Toolki is a set of Disk diagnostics and Disk utilities for the TRS-80 Color Computer Disk Systum. Dick Bartholomew, well known for his utility programs for FLEX systems, has created a package of invaluable tools for the serious programmer. These include, Reading FLEX disks, Writing FLEX disks, Repeatin. Radio Shack disks, Extended directory, and many, meny more.

PRICE ONLY \$49.95 on RS disk.



## **MULTI CPU CROSS ASSEMBLER FOR 6809**

#### by Frank Hoffman

by Frank Hoffman

CAASMB is a conditional macro assembler with the capability to use different CPU overlays in order to cross assemble. These CPU overlays called 'CPU PERSOMALITY MODULES' (CPM's) can be call dition a source tile, thereby making it easy to crear object code for avariety of CPU's. It is also possible to create new CPM's yourself for any 8 or 8 bit CPU. The information needed is included in the manual. If ou decide to do this, it would be advisable to purchase the source for one of the CPM's and modify it rather than starting from acraich.

CPM's are currently available for the followings CPU's: 6809, 6800, 6805, 5002, 280/6080, 1802, and others coming.

#### **PRICE \$139.95**

Includes one 8 bit CPM of your choice (not source)
Additional CPM's
8 Bir \$ 25.00 Source \$ 25.00 extre,
16 Bit \$100.00 Source \$100.00 extre, Inquire about a 6800 version

#### **FAST 6809 MACHINE CODE**

SPELLTEST is the most versatile 68XX spelling checker

SPELLTEST is the most versatile 68XX spelling checker available.

MENUS MAKE OPERATION EASY, From the menu you may. Print a list of suspect words. Print is list of valid words. Check each suspect word one by one. Read your text, stopping to check suspect words; Use additional dictionaries for more thorough checking or special applications; Bulld an additional dictionary of newly accepted words, Write correct text file to disk. While checking you may. Accept the suspect word. Accept and save in like dictionar. Replace with correct spelling. Designed to be used by the layman, SPELLTEST is right at nome in the office. Ease of use and speed will recover the cost in days.

22.000 word dictionary covers the first 25.000 entires in the American Heritage listing of the most common English words.

words
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specific to our field, filter the text and allows a large life
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PRICE \$199.00 object only \$299.00 with source on disk

#### Basic Programmers Toolkil by Dick Bartholomew

The Basic Programmers Foolkit gives the BASIC programmer the power and flexibility never before aciti ved under FLEX. The features include:

#### EDIT

EDIT EDIT The editor experde to TSC BASIC or can be called from disk when you load BASIC into memory. It allows editing any line of your program! It automatically relocates itself to the top of memory. Totally invisible when not in use, it can even be used to enter new lines into your program. Its commands are: move cursor left or right, delete or insert characters, change string1 to string2.

#### DECOMPIL

Change BAC files to BAS files!
The Decomiler takes BAC files and creates a BAS file that can be modified and then recompiled. This is very useful for making small changes to programs that you don't have the BAS type file for. You can send the output to the printer or disk.

#### XRFF

Cross reference for BASIC programs?
The Cross referencer is an invaluable tool for linding largets for GOTO's, and GOSUB's plus all the varie les and where they are used.

PRICE \$49.95 object only \$69.95 with source on disk!

## TOOLKITM

#### The Programmers Toolkit by Dick Bartholomew

The Programmers Toolkit is a package of utilities and programs that ext. Indithe capabilities of FLEX to the utmost. The programs are:

#### REPAIR

Repair any sector on a Olsk! Repair gives you the tollowing op ions: Read, Write, Find a Dyte, Display, Empty, Next in th. In, Next sequential sector, change drive-number and more.

#### SEGMAP

Graphic display of the actor tragmentation or scattering of a disk file or the track air on the disk. This is done with a Graphic display on the terminal. See LNKMAT.

#### LNKMAT

Sort and reformat the free chain into sequential order! LNKMAT will reformat the disk's free chain into sequential erder. If you do a toto of editing or doleling at files this will speed access time by reducing seek times. This often all minates the need to format a new disk and copy files from one to the otner.

#### FDIR

Full Directory programs with all the information about ONNEO title the acreen with all the information about your disk, such as: Name, Date, # of Files, Largest, Smallest, Free space, Linked Irlename, Format of the disk

Display the addresses of a Filal Display the lowest address, Highest address, size in cycles. Transfer address, elset of record indicators and

#### CUSTOMIO

IzO allows terminal and printer Custom staridardization!!

standardization!!

The Custom I/O program acts as a transleter between your programs and the printar and terminal, With it ou can use ommon control codes at the program level and configure the I/O program to handle the printar and terminal. Whenever a I/O device is changed only the I/O package need be changed and not all your programs, how one version of your program will work with all devices!

PRICE \$49.95 object only \$69.95 with source on disk!

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## Flex User Notes

BY: RONALD W. ANDERSON 3540 STRUBRIDGE COURT ANN ARBOR, MI 48105

MORE ON 6809 ASSEMBLER TECHNIQUES

Last column I included some information on using the 6809 instruction set more effectively when programming in assembler. I asked for some reader feedback at that time, but of course you realize that I have about three of these columns written by the time the first is published, so it will be a couple more months after writing this column before the last is published and I see your comments. Just after I wrote that last column, I added a PROM programmer to my system, and decided to test it out by burning a utility that I use frequently into the prom and plugging it into my NP-09 board. I decided to make the utility position independent so I could plug the prom in anywhere in the address space.

initial results were instant failure. I had of course forgotten that the customary place for variables in FLEX utilities is at the beginning of the program. That was done to keep them as wall as the program in the FLEX utility space and out of the way of any program in user memory. Naturally the variables would have to be moved to RAM, but where would be a good place to put them and still not interfere with the user program space. Putting the user stack at the location found in FLEX MEMEND wouldn't be a bad idea, but I think I had a better one. Why not put the variables on the System Stack? In the case of this test routine, a memory dump favorite of mine, there were only two bytes. After a few initial failures, and a consultation with a friend, we decided that the best procedure would be to push a couple of bytes on the system stack at the start of the program, and then point the User stack pointer at them. Now any subroutines used by the program would result in return addresses being pushed on the stack after the variables. The User Stack Pointer would remain pointing at the variable regardless of the subroutine level, so all would work properly. It does, and the program listing is given here as modified for ROM operation.

The use of the User Stack pointer for the variables allows indexed addressing for access, which reduces the byte count to the same as if direct page addressing had been used, as I pointed out in last month's column. This was position independent code to begin with, and the use of the stack for variables has not changed that. In fact, I had one Prom and no eraser, but my prom programming program fills all unused bytes with \$FF, so i moved the program up by \$100 bytes and burned it again. This time it ran.

This technique gives us the possibility of utilities in ROM with no chance of interference with a user program. The only caution would be not to have so many variables that the space allotted for the system stack in FLEX would overflow. FLEX allows 128 bytes for the stack, down from \$CO7F to \$CO00. In fact, this could be overcome by saving the stack pointer at the beginning of the program and moving the stack to a place where enough room is available for all the variables. In this case, it would be easier to leave the system stack alone and put the User stack somewhere else for the variables.

#### MORE SPELLING CHECKERS

I've just received a copy of Dale Puckett's spelling check program, which is being distributed by Frank Hogg Laboratories. My preliminary look resulted in some communication with Dale, and an improvement in

the operation of the software. Dale has incorporated a "smart dictionary", or rather a number of them. When your text file is read by Spell-Test, it is tested against a list of about 500 very common words, and these are eliminated immediately from the list of words found In your file. Spell-Test gives you a report of how many "common words" you have used, and how many remain at that point unfound. You may then run the program with a number of dictionaries, graded in order from the most used words to the least, and after each you get a report of the number of words still suspect of being misspelled. Since I am interested in finding all the words I can, to make the suspect list smaller, and since I have an 8" disk system, I appended all the dictionaries together, (which Dale suggests as a possibility), and ran a text of about 1500 words through Spell-Test. It had only 39 suspects in just over three minutes to read my file and compare it against a 22,000 word dictionary. I found three obvious typos, and the remaining 36 words were either proper names, or somewhat peculiar to my writing vocabulary. Optimize end optimization were among those.

The user has several options in viewing the suspect words. They may be looked at individually or within the context of the text file, as the file is output to the screen stopping at each suspect. If you correct a word, it is put into the proper place in the text, and output continues until another suspect is found. You, of course have the option to replace the word or let it stand as it is spelled in the original text. Operation is simple and completely prompted, so that you would hardly need to bother reading the manual except to get the information about the various dictionaries and instructions on how to set up the software for your terminal. The output is somewhat "screen oriented", and it is necessary for you to tell Spell-Test what control characters your terminal requires for cursor moves and screen erase.

Spell-Test is available with or without Source Listing. See Frank Hogg Laboratory Ad. In this Issue.

I have one minor bone to pick with Spell-Test. It formats the text it reads from your file automatically. That is, it was prepared to work with text editors that don't keep carriage returns internally in a paragraph of text (such as Stylograph). My text included the CR's since I had used another editor, and the text was neatly formatted to less than 78 characters per line. The auto formatting would invariably force a new line just before the last word in my original line. That results in alternate long lines and one word lines, which, though no problem in reading the text, just looks a little ragged. The solution is to fool Spell-Test by increasing the constant for the line length when using it for already formatted text, and to decrease that constant when using it with already formatted text. Unfortunately, that means two versions of Spell-Test. Spell-Test is customized for your terminal by assembling and appending a small file containing equates, to the main body of Spell-Test. Perhaps I am the only 6809 user alive who uses more than one editor, and no one else will see this as a problem.

Aside from this slightly ragged appearance of the text with a pre-formatted text file, operation was flawless. The 22,000 word dictionary occupies over 700 sectors, and therefore cannot be appended into one large file if you have a 35 or 40 track single sided 5<sup>th</sup> disk system. I should mention that Speil-Test allows you to add words to the dictionary or create your own. There is even a special dictionary called "MYWORDS" that can be read each time. You may use it for such things as your name, address, city, company name, and in general any words that might appear frequently in your texts that may be peculiar to you as an individual. You can list the suspect words (or for that matter the good words) to your printer if you desire. This is a good solid piece of software that will be around for a long time.

#### EPSON MANUAL

A friend has bought an EPSON MX-80 printer, and since I need some nice clean text for my book manuscript, I've negotiated to use it for the book. The manual is written in what Art Weller calls the Dick and Jane style. Maybe things have changed, but when I was in First Grade, we had a Reader that went something like "Dick. See Dick run. Jane. See Jane run. Run Jane, run." etc. It is perhaps just what is called for by the average new TRS-80 user. For the user who has had a printer or two and just wants to see what it can do, however, it is not so nice. I finally found the specifications on page 99. I wanted to know what character widths were supported. That I tem isn't included in the specifications. By looking at the control codes in Appendix I, managed to find two codes that were defined as "sets the enlarged printing" and "sets the condensed printing. Now I knew that there were two widths. That is at least a start. By reading several chapters of the Dick and Jane style, I at last found out that? could print 10 CP! and 16.5 CPI, rather disappointing because I happen to use 12 CPI most of the time with my Paper Tiger. However, the Epson produces such nice crisp output that the 16.5 is very nice looking.

In general, I have the above criticism of manuals written in simplistic style or otherwise. Yes, supplier of hardware or software, do supply a readable manual. Please, though, somewhere that is easy to find, put ALL the data an experienced user will need. While you are at it, make that page easy to find. I see nothing wrong with a specification sheet at the beginning of a manual. If I buy another Pascal Compiler, (I already have used four for the 6809 and one for a Z-80 system), I really don't need to read 100 pages of discussion of what has been implemented and how. Just give me a page indicating what features of Standard Pascal have NOT been implemented, and another page or two that describe any EXTENSIONS that have been included. If you like, call this chapter "For Experienced Pascal Programmers", and put it at the end of the manual if you like, Just let me know that it exists. The first chapter might start out with "This manual describes this implementation of Pascal in detail. For experienced programmers already familiar with Standard (Jensen and Wirth) Pascal, Chapter 9 deals with the use of this compiler."

Now! don't have to wade through 21 chapters of information I already know, just to find out what has been left out, what has been added, and how to use the compiler. In the case of Epson, the character widths should be included on the specification page, and in the control code descriptions. "Set Normal (10 CPI) width" and "Set Condensed (16.5 CPI)" would do nicely. While I won't name the Pascal with the manual on which the above is based, I will say that the TSC Pascal manual is very well done for an experienced programmer.

The table of contents reads:

- 1. Introduction
- 2. How to compile and run Pascal programs
- Standard features not supported by our Pascal system
- 4. Non-standard features
- 5. Adapting to your system
- 6. Appendix

That just about covers all the important things a Pascal programmer needs. For those who are not familiar with Pascal, TSC includes a Jensen and Wirth Standard. Perhaps TSC has gone to the other extreme of preparing a very useful manual for a Pascal programmer and one that will be very difficult for an inexperienced programmer. I suspect that the

differences in style of the Epson manual and the TSC reflect the market for which their products are aimed. Still, though, I would think a manual could be written that would be useful to both novices and experienced users of similar products.

See page 23 for program

## **68XX CONVENTION**

#### First 68XX Convention

As I have rumored for the past year or so we are soon to hold our first 68XX convention. The date has been set to coincide with the Atlanta (Georgia) HAM-COMPUTERFEST, which is a two day event, June 12-13 1982.

For the past five years we have exhibited at this show and of all the shows we attend this is the most enjoyable show of them all. For instance there (s no hassie getting set up and the show liaison Mr. Chaz Cone and his excellent crew go out of their way to make both exhibitors and visitors 'feel at home'! We have accomplished more missionary type get acquainted with the 68XX and Standard S50 Bus work at this show than about any other we attend, which is about all.

Last year there were 5 Standard S50 Bus exhibitors at the Atlanta show and this year we hope to have many more, all located in one place and demonstrating the full line of 6800-6809 systems (and just maybe a 68000) as well as the RS Color Computer running FLEX with all its power. If you the readers and you the vendors make an effort to attend, we can start what has been needed for a long time; a getting together of users (hobby and business) with the manufacturers and many of those who make and sell all those things we spend our money for.

The agenda has been set up as follows; the regular show dates are June the 12 and 13th 1982 at the Downtown Marriott Hotel in Atlanta. The actual show opens Friday morning (12th) and closes about 2:00 p.m. Sunday (13th), after the show closes, about 4:00 p.m. Sunday afternoon we will have a large (as necessary) hall reserved where we will hold our first convention after a we eat (dutch), it is anticipated that a panel of speakers will be available and also it will give us an opportunity to meet many, eyeball to eyeball, whom we have all read about but never had the chance to meet in person. It is anticipated that you will all get a chance to ask soma quesions and get answers straight from the sources. This meeting can last as long as necesssary and for this the first time most all is very flexible. Remember this is just for you, sellers, buyers and most important 68XX users. If this works out, only you can determine that, then we will do it every year. I will have more on this next month.

For you manufacturer's and vendors please contact me for booth prices and any other information you might need. I have been told by some of you that you plan to exhibit, I need to have an idea soon (within next 15-20 days) so I can get all the booths together, this way will be much better. If you have exhibited at Atlanta before or have already made booth reservations please let me know about this also, I will try to get us all in one area and tied together with streamers or something. But most of all meed to know SOONED O.K.?

Also you can contact Chaz Cone directly by telephoning 404 238-4334, which is his office number or his home number is 404 394-9638. Either way is ok by me but I would like to know who is going to exhibit and who is not, many readers have asked me to let them know who will be there. I feel very strong about this sort of get together, we have needed it for too long. I believe that this can go a long way in fostering and helping to cement

that spirit of fellowship that I find among 68XX users. The success of this is up to all of you, I have done all I can by myself.

OMW - - -

## **COLOR User Notes**

ROBERT L. NAY 4429 Plantation Lane Norcross, GA 30071

Another month is upon us. We have several more interesting subjects to broach, so let's get started.

First, ANOTHER different physical format for the Column. Old Faithful finally let me down; I lost the Power Transformer in the Power Supply and haven't gotten hold of another one yet (no sign of heat damage, etc., and my system has been running cooler than average, so right now I think it was just one of those things). So, this month's edition is being written on a full-blown, 80 x 24 screen, Word Processing System and being printed with a Delsy Wheel (actually, to give due credit, on a RS MOO II with Scripsit), so maybe the reproduction will be easy to read.

FLEX 9.0 is now available for the Color Computer - see the discussion and details after 1 clear up a few points about the MC6883 SAM chip that I discussed last month. We'll get a first look at Radio Shacks' Disk System for the Color Computer - a Double Density 5 1/4" System that is now working pretty good. We take a Quick Look at a POWERFUL Terminal Program from Nelson Software (and Dan has promised The SUPER "COLOR" WRITER in the next week or so - we'll get a Quick Look at it next month, hopefully). Finally, another FANTASTIC Game - Computerware's PAC ATTACK - It's really SUPER. So, onward and upward -

#### MC6883 CLARIFICATION!!

Most of you, I'm sure, are familiar with the weakest link in any Computer System; the Man/Machine Interface. The most lucid description of this problem is "Garbage IN, Garbage OUT". Between the mess of moving and trying to beat publishing deadlines, I MAY not have been very clear about what the 6883's capabilities are, as installed in the Color Computer, and what is POSSIBLE with some hardware mods, etc. Primarily, I'm referring to the Screen Memory and Display. The discussion of locating Screen Memory in the \$E000 and up area, and have BASIC operating in Page 0 and FLEX in Page 1, wasn't too clear. This would be great, BUT IS NOT POSSIBLE WITHOUT SOME MARDWARE MODS. If you think about It, the SAM only has control of the two 32K Pages; therefore, It can not possibly control the Display Mamory at \$E000 because it is not in control of THAT memory. How could you get around this groblem??? How about using the SAM in the Type #1 Memory Map (64K RAM) and using the 32K from Exatron for the Page 2 memory. This problem??? could be accomplished by borrowing an idea

from the Exatron Unit; use a Filp-Flop (for instance, a 74LS74) to control the Paging by hooking the Q output to one 32K bank of the Memorles Enable Line, and the NOT-Q to the other bank. Toggle this Flip-Flop through one of the I/O Ports when switching between BASIC and FLEX. NOW, we have a clean lower RAM for "Standard" FLEX, and Display Memory out of the way above \$E000. Switch the Screen back to \$0400 when calling BASIC, and it will be happy, also, with any Display Screen that might have been used previously still intact.

The idea of having 96K memory OPERATIONAL in the Color Computer is no dream; it's EASY and WORKS. I have run my System that way, with the 64K Mod from Atomtronics discussed lest month and the Exatron Expansion unit, which has it's own 32K with it's own memory refresh system, operating in the  $\underline{Type} \not\equiv 0$  Memory made. The use of the 96K Memory is NO problem; the Wolfbug Monitor works good, but still has a few "bugs". The Page Controlling works OK; the problem shows up when anything is plugged into the Cartridge Slot. The RESET function does not correctly determine the amount of memory installed in the Computer (it would not locate 96K in any event, because if we are using BASICs' RESET routine, it is going to run into it's own ROM at either \$8000 or \$A000), but, with something in the Cart. Slot, Wolfbug comes up with \$0FFF as the "top of memory" (4K) with some of BASICS's pointers in "Never-never Land". This severely restricts your capabilities with the Computer, to say the least. The "bug is being chased", as the saying goes, and should be solved shortly.

l have been running my Color Computer in the Type #1 Mode with BASIC living in RAM quite a blt. This makes the Single Step function in Data Soft's SIGMON operational for working through the BASIC ROM's (reviewed in an earlier column; an Excellent Program). We'll start reporting how various functions work in future columns; I have a lot of data to assimilate before I can present it In a coherent manner. One tidbit I'll throw out that I haven't seen mentioned before: the reason the Tape System on the Color Computer is so reliable is that the information is not transmitted to the Tape as Square Waves (which requires very high frequency response capabilities to record properly), but as sort of a distorted sine wave. This allows the high transfer rates we are used too, while still allowing the use of lower priced Tapes. Don't get me wrong; you need a "Data" quality Tape for reliable operation; just not an expensive one. (This also means that an Interrupt Driven Tape SAVE is not feasible without changing the Hardware.) As I have stated before, the BASIC in the Color Computer is an Excellent version of the language, and makes good use of the 6809's capabilities. Microsoft's experience shows up more and more as we get deeper into this

One other point I may have mentioned before, but it won't hurt to repeat. If you have a Radio Shack CTR-80A Cassette Recorder, you can adjust the "PiAY" head on the unit to allow reading a Tape recorded on a Tape Unit other than your own. The "RECORD" heads may not be aligned the same as yours, causing the recording on the Tapa to appear to be

slightly "tilted" in relation to your "READ" head. This means that you don't get a good copy, or strong signal, when you try to read a Tapa, causing I/O Errors. Turning up the volume works sometimes, but can also cause distortion, and more Errors. The solution is to adjust the "READ" head to eliminate the distortion. This is accomplished by inserting a SMALL screwdriver through the tiny hole located on the top of the Cassette Recorder above the PLAY button, near the Cassette Lid. This hole is about 5/64th inch in diameter: I normally input AUDIO ON: MOTOR ON (ENTER) end edjust for the most volume. Then you can rewind the Tape, and get e good program LOAD. Just remember, you will have to readjust the head to play your own tapas.

#### FLEX 9.0 IS NOW AVAILABLE for the Color Computer

As you are probably aware, if you have been following this column, I have been running Steve Odneals' FLEX Conversion on the Color Computer for the last three months. Steve has signed an agreement with

> Computer Publishing Inc. 5900 Cassandra Smith PO Box 849 Hixson, Tn. 37343 615 842-4600

CPI will be handling all sales and advertising for this FLEX 9.0 Conversion, and has the whola system available either separately or as a complete package. Pricing ls as follows:

- 1. TSC's General FLEX 9.0 Package \$150.00
- 2. Exatron Expansion Unit with Disk \$299.95 Controller
- 3. F-Mate (Conversion Package for the Color Computer) (when purchased with the FLEX System) \$49.95
- --- (when purchased with out the FLEX System) \$59.95
- 4. "Screen Clean" kit noise reducer for the Exatron or Radio Shack Disk Systems
- 5. Single-Sided 40 Track Disk System with Case and Power Supply \$329.95
- 6. Radio Shack Color Computer 16K with Extended BASIC \$595.00
- 7. Other items available see advertisement

Let's look at this package for a minute. The General FLEX 9.0 Package (since the Color Computer uses the 6809 Computer Chip, ALL references to FLEX refer to the 6809 Version, FLEX 9.0) includes an excellent set of Manuels, the Core of FLEX (the actual Operating System), and various Utilities to allow setting the System up on various Computers. It ALSO includes the EDITOR and a full MACRO ASSEMBLER, so you actually get three full Programs at \$50.00 a Program, which is an EXCELLENT price for programs of

this caliber. "F-Mate" includes a 5 1/4", Single Density disk, with the conversion programs which can be read initially with the Exatron Disk Controller and the Exatron DOS and a set of conversion instructions. Disk Includes the Input/Output Routine Programs (Disk, Keyboard, and Printer) written for the Color Computer, and a Boot Program which allows the finalized FLEX/SYS to Boot Up with the Exatron Controller, and than bring the rest of FLEX on board. A special NEWDISK Program and PATCHES to several of the normal FLEX Utilities, such as ASN, SAVE.LOW, APPEND.CMD, ASMB.CMD (the Assembler), EDIT.OMD (the Editor), and COPY.CMD are also Included. PATCHES for other Utilities and Programs are being developed as time permits; I understand that XBASIC is about ready, which will REALLY axpand tha programs available to the Color Computer User. (A note about FORMATTING problems: If you hit a troublesome disk, try using a Bulk Tepe Eraser to really CLEAN a Disk that gives FORMATTING ERRORS; often you can salvage one that you thought wes physically damaged and unusable with this technique. Usa it just like you were erasing a Tape.)

#### RADTO SHACK DISK SYSTEM

I have been promising you a look at the Radio Shack Disk System as soon as possible; hera it is. My first imprassion is: COID. The System works fine (now that the support chip problem has been solved); the DISK BASIC is straight-forward and fairly complete with a few real "goodies" thrown in; and the Manual that comes with the system is STUNNING, to say the least, after the disappointments of the BASIC and EXTENDED BASIC Manuels. This Manual gives you some REAL information. Now that this System Is becoming available, you will start seeing a lot of GCOO Software showing up on the market for the Color Computer. I know of several programs that have been held up waiting for the RS DOS, so that they could be made compatible. And, the Programmers have a compatible. system that is going to be easy to work with, both in BASIC and Machine Language.

#### DISK BASIC Commands Include:

DSKINI for formatting a blank Disk BACKUP for duplicating the contents of one Disk to another (which must have been formatted first)

COPY a file KILL a file

RENAME IL

LOAD a BASIC Program (adding a ,R after the "filename" also RUNs it)
LOADM for a Mach. Lang. Program
SAVE a BASIC Program, (add ,A to SAVE it in ASCII)

SAVEM for Mach. Lang. or Binary files VERIFY ON or VERIFY OFF

DIR displays the Directory

FREE returns the number of free granules

(discussed later) on the disk
DRIVE d (where d is the Drive Number) changes the default drive number to the

one you specify RUN filename "Loads and Goes", while adding a ,R causes all open files to remain open (interesting and highly useful!!)

UNLOAD a drive number ... huh?? This closes all open files on any one drive to allow eafe, easy disk changes - nice!!

MERGE loads an ASCli file from disk and merges it with the existing program in memory; adding the ,R causes the program to RUN when merged
FILES tells the Computer how many buffers

to reserve, and how blo to make them.

DISK BASIC Functions include the normal Buffer controls with:

OPEN "moda", buffer #, filename, record length which opens a buffer for Sequential Input or Output or opens a Direct (Random) access file for either. Up to 15 Buffers can be used simultaneously. You can

CLOSE any buffer,

WRITE or ---

PRINT data to it (and also use the PRINT #buffer, USING format; data list PRINT Instruction), and

PUT or

GET a record number. You can FIELD a direct access buffer, LSET or

RSET the data into It,

INPUT or

LINE INPUT data from the buffer as a variable, get the current record number of any buffer with LOC(buffar), or obtain the highest numbered record of a specific buffer

LOF(buffer). Finally, you can convert a number to a string with

MON\$ and vice versa with

CVN.

EOF returns a 0 if there is more data in the buffer, or a -1 if it is empty.

I've been saving the best for last. Here are a couple I haven't run into yet, end they really look interesting. They are called

DSKi\$ and

DSKO\$ and provide DIRECT access to ANY sector on the disk.

Their format looks like this:

DSKI\$ drive #, track, eactor, string ver.1, etring ver.2

· · · l.e. - - -

D5K1\$ 0, 12, 3, M\$, N\$

which gate the 256 bytes of track 12, sector 3, of drive 0, putting the first 128 bytes in M\$ and the second 128 bytes in N\$.

DS40\$ works thus:

DSKO\$ driva#, track, aac, stringl, string2 which, in real life, looks like this

DSHO\$ 0, 2, 1, "FIRST DATA", "SECOND DATA".

This deposits FiRST DATA's 128 bytes and SECOND DATA's 128 bytes on track 2, sector 1, of Drive O.

For the Machine Language buffs, I'll summerize the Technical Information - you can obtain a the Diek System Manual for full datalle. The RSDOS uses the "granule" concept for file allocation, and uses two granules, of 9 sectors each, per track.

Track IT contains the Directory, Teaving 34 Tracks with 68 granules for data storage. Each granule provides 2,304 bytes of data storage. This is my one small complaint with the system; 2K flies will mean a lot of wasted space (conversely, 68 files on a 5 1/4" Disk ought to be enough - you would almost need a sorted DIR display to find a n y t h l n g ) . S i n c e the DOS lives completely in ROM, no Disk Space is required for "System" utilization.

The first 32 bytes of each Track are utilized for System Controls, the next 6,084 make up the Sectors, the rest, which is variable, contain \$4E. Each Sector contains 338 bytes, organized as follows: 0.55 = system controls, 56-311 = data, and 312-337 =system controls. The system control bytes are organized like so:

0-7 \$00 8-10 \$F5 11 SFE 12 Track Number 13 \$00 14 Sector Number 15 \$01 16-17 Cyclic Redundancy Check (ORC) 18-39 \$4E \$00 40-51 52-54 \$F5 55 \$FB 312-313 CRC 314-337 SAE

Track 17, Sector 2 contains the File Allocation Table; Sectors 3-11 the Directory Entries. Each Directory Entry is 32 bytes long, consisting of: 0-7 Filener

Filename - If Byte 0 is 0, the file has been deleted; if FF, this and all following entries have not been used.

8-10 Filename Extension

File Type:

0 = BASiC program 1 = BASiC data fila

2 = Machine Language program 3 = Text Editor source file

ASCII flag:

0 = Binary

FF = ASCIL

11

13 The first granule number of the file (0-67)

14-15 The number of bytes in use in the last sector of the file.

Reserved for future use. 16-31

The Fila Allocation Table uses the first 68 bytes (one for each of the 68 granules), the rest of the sector contains zeroes. Each byte corresponds to It's own granule, and contains the following Information:

This granule is free 0-\$43. This number, converted to decimal, points to the next granule in \$00-\$43 the file.

I-C9 This is the last granule in a file. The value in bits 0-5 indicates how many sectors of this granule are \$C0-C9 used in the file.

The Disk System Control Routine is called DSKCON. It's entry address is stored at \$C004-C005 and is called as a Subroutine (JSR [\$C004]). DSKCON'e parameters are stored in a table pointed too by location \$C006-C007. The Table is organized thusly:

DCOPC RMB 1 - Operation Code
DCDRV RMB 1 - Drive Number
DCTRK RMB 1 - Track Number
DSEC RMB 1 - Sector Number
DCBPT RMB 2 - Buffer Pointer
DCSTA RMB 1 - Status

These parameters are used just like the FLEX FMS. The Status byte is probably the WD1793 status codes, since the Controller uses this chip. Storing a 0 at \$FF40 will turn the Drive Motor off after an access.

Generally, this appears to be a good DOS, that provides the basic capabilities, while also providing some new commands that greatly improves It's power. It is a "lower level" DOS, which I personally applaud, because you are not strapped to a system that does not allow easy expansion. For example, if you could only work with "words" in a programming language, you would lose all the power of "bit" and "byte" manipulations. This DOS allows you to work at the "bit" level, yielding maximum flexibility. the disappointments of some of the initial games and utilities that came out of Fort Worth, it is heartening to see a well structured program, with features like the DSKCON Routine, in a Program from them (like someone is learning to program this chip out there). Finally, I get the strong impression that whoever wrote the Disk System Manual actually knows what he is doing - the Technical Information is presented in a clear, concise manner so that you don't have to read between the lines to get good, solid information. The rest of the Manual follows their example of "leading" a new user through the use of the system, and again, should provide no problems for those users. All in ali, I'm impressed and happy to see some so I id I in formation beginning to appear from one of the largest Computer Manufactures in the World.

QUIOK LOOK:

Nelson Software Systems
P.O. Box 19096
Minneapolis, Mn. 55419

SUPER "COLOR" TERMINAL

Tape - \$24.95 ROMPAK - \$34.95 Disk - \$44.95

Well, I have the first item from Nelson Software Systems to report on. Many of you will remember that we were expecting to review their SUPER \*COLOR\* WRITER a few months ago, but still haven't received it. Dan Nelson sent a note with this Program, the SUPER \*COLOR\* TERMINAL, stating that the Word Processor would be out shortly; so those of you that have ordered it, hang in there a little longer. I think It will be worth the walt. In fairness to Dan, the hold-up hasn't been all his fault. Any serious Software written specifically for the Color Computer must be compatible with the Radio Shack Color Computer Disk System, and that has caused him to hold up finalizing SUPER "COLOR" WRITER until he could be certain that It WAS compatible. Now that the Disk System is beginning to become available, he should have that Program out shortly. I know that he had

a couple of the original units that he was having trouble keeping running (Radio Shack now has that problem licked).

The SUPER "COLOR" TERMINAL is a full smart terminal program that allows the user to communicate with almost any host Computer System that has RS-232 cepabilities. We received Version 1.0 on Tape. (If you are running a 4K Color Computer, be sure to obtain the ROM version, because the Tape Program does not leave enough memory for an "input buffer" to accept incoming information.) It contains the following basic functions:

Full UPPER CASE, LOWER CASE, and CONTROL CODE capabilities with ESCAPE and LINE BREAK.

The capability to set the RS-232 PARAMETERS from the Keyboard to allow communication with almost any protocol.

Allows Hardcopy Printing of received information directly if a Printer hookup is available.

Provides for the storage of received information on Tape or Disk (with the Disk System).

Receive and/or Send BASIC, Mach. Lang., or ASCI1 files.

Create filas (Tape with the Tape Program or Disk with the Disk Program) that are compatible with the SUPER "COLOR" WRITER Word Processor.

SUPER "COLOR" TERMINAL is a "Menu Driven" Program; i.e., it comes up in a Master Menu, from which you can go to the COMMUNICATIONS mode, CHG PARAMETERS mode, CREATE BUFFER mode, TAPE TO BUFFER mode, BUFFER TO TAPE mode, DISPLAY BUFFER mode, or LPRINT BUFFER mode (if you have the capability to drive a printer OTHER THAN the normal RS-232 I/O connection on the Color Computer, which is being used for the Communications with the other Computer System).

The COMMUNICATIONS mode provides a full set of CONTROL KEYS thru the use of the CCLEAR> key in a "two-key" mode; i.e., push CCLEAR> and then CC> for "CONTROL C". 32 CONTROL KEYS are provided through the usa of A-Z and the ARROW Keys and SHIFT>(CLEAR> (CLEAR>-SHIFT>CLEAR> provides CONTROL 28). The top line on the Display Screen is normally used as a "status line" in many of the modes, along with the Cursor Color. For example, a Blue Cursor indicates you are sending a CONTROL COOE. An "9" symbol in the top left corner of the screen means that the BUFFER is closed; when the indicator turns or ange, the BUFFER is open for Input or output. COMPUSERVE users should send a CONTROL C for Compuserva Information Service.

SUPER "COLOR" TERMINAL appears to provide a Complete Communications Package, and I think you will find it to be a very popular package. It is an interrupt Driven Program that has many powerful features. Those of you who have been following this Column are aware of my "preaching" the provision of CONTROL CODES for the Color Computer; this one provides them. You get your first hint of the Interrupt Driven capabilities when you load the Tape the first time - a very colorful Logo comes on the Display Screen WHILE the Tape is LOADING. The Program's power derives from the use of a

software UART style of communications, and basically only utilizes ASCII Code for all communications. This makes it extremely portable; it has been used for communication with many of the major Computer Systems in operation today (and generally, by the Manufactures of those systems), with no problems. Set the Baud Rate at 4800 and direct-connect to a Radio Shack MODEL II or III and it pures like a kitten, for example.

As I said before, I think you will find SUPER "COLOR" TERMINAL to be a POWERFUL Program that will find major usage as the industry grows.

#### COLUR PAC ATTACK

Computerware
1472 Incinitas Blvd - Box 668
Encinitas, Calif. 92024
(7147 436-3517

Tape - \$24.95 Disk - \$29.95 Requires 16K Memory

Computerware advertises COLOR PAK ATTACK as "an incredibly challenging version of the popular arcade game. Great graphics, sound, and action for hours of FUN!!". All I can add is "AMEN"!! If you are a Pac Man "freak", DO NOT begin this game if you don't have several hours to spend - for instance, if you want to watch the football game tomorrow, LOCK PAK ATTACK IN THE LARGEST SAFE YOU CAN FIND AND THEOW AWAY THE KEY. Take a weeks vacation, AND THEN, AND ONLY THEN, get it out and "load it up". OK, I've warned you - don't blame me for the bent Control Stick and blisters.

You may have guessed that I think this CHALLENGING - and doesn't get "old" after you have played it a while. We are approaching the 10,000 point mark in the "HARD" mode (you chose EASY, HARD, or TUFF starting each game), but, in self defense, we have only had the game a couple days, and therefore have only played it a couple thousand times; I expect to do better once I develop a "feel" for the game and get it broken in. (Oh yes; you had better pick up a few spare Controllers for the Computer first chance you get - they can't be Indestructible ----- I don't think?)

What more can I say -- get one YESTERDAY!!!

## COLOR INFO

"Color Merge", by Sid Kahn appears courtesy of the Cincinatti TRS-80 Users Group NL, and is reprinted from the Marin County TRS-80 User Group NL,11/81.

The following program is a merge program for the color computer. It works by peeking the end of a program pointer and then poking it in the start of the program pointer and doing a CLOAD. The Color Computer does not lose its place when this is done like the Mod one will. To get the sum total of the programs you must still poke the start pointer into MEM manually. A suggestion that will make things easier would be to poke 25 with a 6, and 26 with a 0, this will load the program @ 0600H and in effect give you 1.3K more mem space. A warning with this program is to make sure there are no line number conflicts and be careful of RESTORE and DATA state ments.

10 CLS:PRINT"MEMORY AVAILABLE:"MEM 'COLOR MERGE

20 A1=PEEK(25);A2=PEEK(26);A3=A1\*256+A2 22 B1=PEEK(27);B2=PEEK(28);B3=B1\*256+B2 24 PRINT;PRINT"START OF PROGRAM ;"A3" "HEX\$(A3)

26 PRINT"END OF PROGRAM :"B3"
"HEX\$(B3)

28 PRINT:PRINT"TYPE FOLLOWING AFTER LOADING PRO

GRAMS!"

29 PRINT" POKE 25,"A1":POKE 26,"A2
30 PRINT:PRINT"TYPE 'CLOAD' TO LOAD
PROGRAM AND

TYPE 'END' TO

32 INPUT IS

34 IF I\$="CLOAD" THEN 40

**36 END** 

40 IF B2<2 THEN 50

42 POKE 25,B1:POKE 26,B2-2:CLOAD

50 POKE 25,B1-1:POKE 26,B2+254;CLOAD

"ROMPACK READING", by Sid Kahn appears courtesy of the CINTUG NL 11/81 and reprinted from the MCTUG NL, 11/81.

To COPY ROM-PACKS to cassette you have to disconnect pin 40 on the cartridge connector. Some have done it with scotch tape and some with a SPST switch. Now all you have to do is type 'POKE &HFF23,36' to gain control of the ROM-PAK. To go back to NORMAL (READ the ROM-PAK) type 'POKE &HFF23,37'. This will save the cost of a "ROM BACK-UP Adapter or similar devices from Exatron (in a year's time BIGBYTE and others like it must have saved me HUNDREDS of dollars). However, be warned that if you insert and remove the ROM-PAK with the POWER ON, you RUN the RISK of BLOWING out the ROM inside. I have destroved my VIDEOTEX pack in just this manner, It would be best to do it with the POWER OFF. The Radio Shack PAKS have the 5 volt lead cut shorter than the TRACES on the edge connector, other PACKS may or may not have this precaution.

```
I OURPROM, POSITION INDEPENDENT OURP PROGRAM HODIFIED
                         # FOR USE IN ROM, A JUMP TO STARE ABORESS WILL RESULT # IN THE PROMPT, 'COMPANIE', ENTER NUFOR WEST, AND PAGE
                                                                                                       100
                                                                                                                                 A SURBOUT INFS FOR LOW A
                                                                                                                                  I OUTPUT FOUR HET DIBITS AND A SPACE
                                                                                                       101
                         I MUNTER. FOR EXAMPLE NOT WILL SWIP 0100-01FF
                                                                                                             0077 BB CD3C
                                                                                                                                 OUTAKS JSR
                                                                                                                                                DETHER
                         I AFSOR A PAGE IS OMPED, ENTED F 10 CONTINUE
10
                         I FORWARD, S TO BACK UP A PAGE, HII TO GO TO PAGE II
                                                                                                       106
                                                                                                             0075 30
                                                                                                                       01
                                                                                                                                         IFAL L.1
12
                         I CR E TO EXTE TO DOS.
                                                                                                       105
                                                                                                                                 I DUIPUT IND HEE DIGITS AND A SPACE
22
                         I YOU CAN USE THE STANDARD FLET PRINT COMPAND WITH
                                                                                                        107
                                                                                                             0077 Sb CD3C
                                                                                                                                 DUTCHS ISA OUTHET
15
                         1 THIS UTILITY. SUPPOSE IT IS IN ROM AT GEROO.
                                                                                                       108
                         I P. JUMP ESOO WILL EMBLE THE PRINTER AND GO TO THE
                                                                                                              007A 30
                                                                                                                                         LEAL 1.1
                                                                                                        109
                                                                                                                       01
Ló
                         I MAY PROSON. PROMPTS WILL BE ROUTED TO THE TENNINAL
                                                                                                       110
17
                        # AND DUTPUT HILL BE TO THE PRINTER. THE PROBRAM RETURNS
# TO FLEX ON EXIT. AND THE L/O SHITCHING WILL BE RESTORES.
                                                                                                                                 I DUTPUT I SPACE
                                                                                                       111
112
19
                                                                                                       113
                                                                                                             007C B6
                                                                                                                      20
                                                                                                                                 OUTS
                                                                                                                                                 8120
20
                                                                                                                                                 PUTCHE
                                                                                                                                                           *#100EN 815*
                                                                                                                       CBE
                                                                                                                                          JAP
21
                        I BASED ON A VERY EARLY SHIPE UTILITY.
                                                                                                       114
                                                                                                              007E 7E
22
                        I TAPE VERSION FOR 4800.
                                                                                                       115
                                                                                                       116
                                                                                                                                 I USE FLET PORLE SE PAUSE IS HOMBRED
73
                        I SYSIEM EQUALES
                                                                                                       117
24
                                                                                                             0081 7E CD24
                                                                                                                                       JHP PORLE
25
                                                                                                       119
                  COSS GETCHA EQU
26
27
                                        40015
                                                                                                       120
                                                                                                                                 # GET ONE BYTE FROM TERMINAL. IMPUT AS THE HET BIGITS
                  COIR PUICHR
                                EOU
                                        16018
                                                                                                       121
                                        37836
                                                                                                       122
                                                                                                             0084 BD
                                                                                                                       11
                                                                                                                                 BY TE
                                                                                                                                         BSR
                                                                                                                                                 IMPET
                                                                                                                                                           GET A HEL DIBIT
29
                  CC22
                        SHLTON
                                EQU
                                        6CE22
                                        oC003
                                                                                                       123
                                                                                                              008A 25
                                                                                                                        OE
                                                                                                                                          BCS
                                                                                                                                                 DOMBYE
                                                                                                                                                           RETURN ON FRIGOR
                  CD03
                        WARKS
                                EBU
30
                                                                                                       125
                                                                                                             0000 48
                                                                                                                                                           MAKE IT HIGH ORDER HIBBLE
                                                                                                                                          ASL A
31
                        DOTHED
                                        SCD3C
                                                                                                       125
                                                                                                              RL PROD
                                                                                                                                          401
                                                                                                                                                           MAKE II XIGH ORDER HIDDLE
                  CB24 PBRLS
                                 EDU
                                        10024
                                                                                                       1.25
                                                                                                             81 4800
                                                                                                                                         19.9
                                                                                                                                                           BACE IT KIGH DROFE MIRM F
33
                                                                                                       125
                                                                                                                                                           NAKE 11 HIGH ORDER HIBBLE
                                                                                                                                          ASLA
                                                   OFFSEIS FROM U FOR VARIABLES
                                                                                                       126
                                                                                                              008C 1F
                                                                                                                                                           SAVE HE HEDDLE
35
                   0001 ADDRLD EQU
                                                                                                       127
                                                                                                              DORF BD
                                                                                                                       87
                                                                                                                                          150
                                                                                                                                                 HHET
34
37
                                                                                                       128
                                                                                                             0090 25
                                                                                                                       04
                                                                                                                                         RES
                                                                                                                                                 DOMENT
    0000
                                                                                                       129
                                                                                                             0092 34
                                                                                                                       04 ABE0
                                                                                                                                                           ADD LOW HIBBLE
                                                                                                                                 DOIGNY RTS
                        I CODE IS POSSTION INDEPENDENT. ORS MAY BE WHATEVER IS REQUIRED BY
39
                        1 YOUR PROM PROBAMBILING SOFTWARE.
                                                                                                       13)
80
                                                                                                                                 I SET A HET BIGIL AND TEST FOR VALIDATE
                                                                                                       132
41
                                                                                                       133
     0000 34
                         BEGLA
                                                   HORKSPACE FOR VARIABLE
                                 P945
                                                                                                       134
43
44
     77 S000
                                 1 FAU
                                        0.5
                                                   USER POINTER AS INDEX
                                                                                                             6097 En
                                                                                                                       £D15
                                                                                                                                         JSR
                                                                                                                                                GETTOM
                                                                                                       135
     0004 C6
                                LDB
                                                                                                                                                           REMOVE ASCIT BIAS
                                                                                                       136
                                                                                                             009A BO
                                                                                                                                          SUBA
                                                                                                                                                 4130
               CC22
                                 STB
                                        SILITON
                                                  PROMPT TO TERMENAL EVEN IF PRINTER ENABLED
                                                                                                       137
                                                                                                             001C 2B
               89 00A5
46
     0009 30
                                 LEAS
                                        COMMIN.PCH
                                                  PRINT RESSAGE
                                                                                                       138
                                                                                                             DOSE BL
                                                                                                                                          CMPA
                                                                                                                                                 909
                                 JSR
                                        P918M6
     800A RD
               COLE
                                                                                                                                                 DOMET
                                                                                                                                                           MANBER FROM 0 TO 9
                                                                                                       139
                                                                                                             0000 2F
                                                                                                                                         BLE
     0010 LOSE 0010
                                LDY
                                        916
                                                  FOR LINE COUNT
                                                                                                              00A2 B1
                                                                                                                                                 411
     0014 80
                                 1998
                                        LECR
                                ISA
                                                                                                       141
                                                                                                             0004 28
                                                                                                                                          1MB
                                                                                                                                                 FRROR
                                                                                                                                                           CHARACTER RETWEEN 9 AND ASCIT A
              CDIS
    001A BD
                                        GETCHA
                                                  FORMARD TO MESS MEMORY BLOCK
                                                                                                       147
                                                                                                             OGAA BI
                                                                                                                                         CMPA
                                                                                                                                                4614
                                                                                                                                                           SEE IF AF
    0019 81
                                                                                                                                                           DUT OF RAMGE A TO F
                                 BEO
                                        HEM RM
                                                                                                       143
                                                                                                             00A9 2E
                                                                                                                                         BST
                                                                                                                                                ERROR
                                                                                                             COMA BO
                                                                                                                                                           ADJUST TO PROPER HET VALUE FOR A THROUGH S
    0018 BI
               45
                                 CHPA
                                        B'E
                                                  FI11 TO 805
                                                                                                       145
                                                                                                             OOAC IC
                                                                                                                       FE
                                                                                                                                         CLC
                                        filis
    001F 27
                                BE 0
                                                                                                             00AE 39
                                                  GO BACK ONE REMONY BLOCK
                                                                                                       146
                                                                                                                                         RTS
    9921 81
    0023 28
               0.0
                                BILE
                                        SESPI
                                                                                                       140
                                                                                                             000F 1A
                                                                                                                       01
                                                                                                                                 ERROA
                                                                                                                                         SEC
                                 DEC
                                        ABORNILU USE NOP FOR B BYTE PER LINE
    0025 AA
               F4
                                       ADDRHI.U
                                                                                                       149
                                                                                                             00BL 39
                                DEC
    0029 20
              80
                                RRA
                                       MEMBER
                                PULS
                                                  BESTORE WORKSPACE
                                                                                                       151
                                                                                                                                 PROMPT STRING
    0078 35
              C002
                       EIII
                                        HADEC
                                                                                                       157
    9020 7E
                                                                                                             0082 43 4F 48 48
                                                                                                                                CORAND FOC
                                                                                                                                                /COMMAND?/
                                                                                                       153
                                                  BET MEN START ABOR. HI DROER
                                                                                                             0086 41 4E 44 SF
    0030 80
                                BSR
              52
                        SK 1P1
                                       STARTO
                                                  SAD HET MUNBER, REPROMPT
                                                                                                       154
                                                                                                             0088 04
                                                                                                                                         FER
    0037 25
                                DCS
                                        ADDAHLU SAVE IT
                                STA
                                                                                                       155
    0034 R7
                                        ABORLO, U START AT BEGINNING OF A PAGE
                                                                                                                                                          TRANSFER ADDRESS FOR FLEX
                                                                                                       156
    0036 SF
               41
                                CLR
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'68' Micro Journal.

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Indicated that of all their advertising outlets, 68 Micro Journal gets the "Job done"! It is because you the readers trust the advertising in 68 Micro Journal, which makes us all feel good.

Over the past four years we have watched GIMIX grow into a major manufacturer of Standard S50 Bus computers; from replacement type boards to 'state of the art! complete heavy duty 68XX computers and disk systems. A close review of the catalog in the front of this issue is a prime example of what close quality control and dedication to customer input can accomplish. From this seat, as publisher of 68 Micro Journal, I am proud of the advances that have occured over the past 4 or 5 years in the 68XX community. GIMIX has been a factor in this accomplishment; the next five should be even better. Good Luck GIMIX!

P.S. Please note that this is GIMIX's COMPLETE catalog, so you don't have to write for one - HERE IT IS on page 3.

# A NEW C COMPILER

Our C compiler is finally ready to ship! Since we didn't know how long it would take, we waited until we had a deliverable product before we started our ad campaign. The frustration of having a product ready to ship for two months before anyone knows about it is easier to take than having orders that can't be filled (or worse, shipping a product that isn't ready).

One of the things we spent a great deal of time on was the user's guide. Our goal was a guide that was logically arranged, complete but short and easy to use, and correct. My personal pet peeve is an example given in a manual which has never been tested, so I spent the time necessary to actually type in each and every example in the "How to Use" section to verify that the example was correct, and that the explanation matched what the program did.

You will note from the ad elsewhere in this issue that our compiler is low in price. We were not interested in trying to get rich from this one effort. We are all gainfully employed and making good livings elsewhere. Our pricing goal was to generate just enough income to keep the project interesting while we work on versions 2 and 3.

The project is far from over. This is still a 'toy' C, with a lot of garbage left over from the 8080 program it came from. One of the things we added to overcome some of this was a separate optimizer program. In version 2, most of the 8080-style code will be gone aliminating the need for separate gone, eliminating the need for separate optimization.

One item we plan to keep, and improve on, is the relocating linking loader. Version 1 uses RLOAD to achieve relocation of modules. We decided that a special version of RLOAD which we had planned was not necessary, so that anyone who has a working copy of RLOAD, whether purchased from Word'a Worth, the '68' library, or from some other source may deduct \$5 from the price of the

FLEX9 release of the compiler. future, we would like to provide an assembler written in C, along with a true linking loader, also written in C, so that we can remove the problems associated with maintaining different releases of the compiler with different capabilities.

In order to use this compiler, you should have at least 48K of memory. More allows compilation of larger programs (more symbol table space), but RLOAD removes the need for writing large modules in the FLEX9 release. Unfortunately, some of the SSB users don't have the relocating assembler, so an absolute version is supplied for them.

Three steps (plus one optional step) are required for the use of the relocating compiler: 1) Compilation; la) Optional optimization; 2) Assembly; and 3) Loading. This may seem bothersome to the BASIC programmer who is accustomed to interactive write-and-go code, but it to interactive write-and-go code, but it is an inherent feature of compiled languages. In return for these time-consuming steps, you get the ability to generate code which is tight, fast, and in small, reusable modules. With good style, C programs also tend to be much more readable and maintainable than BASIC. Obviously, I have an axe to grind here, so I won't push the point any further. any further.

Also included with the compiler is the source to the run-time package, a simple benchmark program called PRIMES, and a C compiler test program called TESTC, which is designed to catch compiler bugs. I hope to see TESTC run against some of the competition's compilers. The other C source routines furnish useful examples of working C code, although they may not be terribly elegant.

Our goal is to produce a compiler which is complete enough to handle the programs available through the public-domain C library. We hope to reach this goal by early next year. The three versions we plan are our guideline. There may actually be more than three versions. We plan to take enough time to be reasonably sure that each version works properly before we release it, even if that means slipping our schedule. We plan to keep our prices as low as is consistent with producing a quality product, and we plan a liberal upgrade policy which will allow a customer to keep current with the latest revision at a reasonable cost.

We also will be working on applications programs written in C, along with the possible establishment of a C library. More on this in a later editorial.

Please address questions, comments, and orders to: Word's Worth, PO Box 28954, Dallas, Texas 75228.

## "C" User Notes

Norm Commo 3 Pryor Road Natick, MA 01760

This month we will look at how data and variables are represented in a C program, the different ways that variables are stored, the scope of variables and a few other topics.

#### **DECLARATIONS**

Most of you already have some concept of data typing. After all, even some Basics allows you to "type" a variable with the addition of the "\$" and "\$" modifiers. These tell the interpreter or complier whether or not a variable is a floating point number, a character or string, or an integer. C merely goes one step further by requiring that you list the type of each veriable in something called a DECLARATION before you use it. The declaration consists of the type name and a list of variable of that type. Some examples are:

char letter, inchar, outchar; int number, size range;

int size, /\* shoe size \*/
width, /\* shoe width \*/
eyelets; /\* number of eyelets per side \*/

The format of a declaration is rather loose as the examples show. A declaration is legal as long as the type name comes first followed by one or more veriable names, separated by commas, and terminated with a semicolon. It is not necessary to list all the veriable of the same type in the same list. If the program is made more readable by making a seperate declaration for every variable, then do it.

Complex data types and certain functions must also be

Complex data types and certain functions must also be declared, but those will be covered as the need arises.

#### NAMES

A name in C can be any collection of letters, digits, or the underscore, "". The only restriction is that the name must start with a letter. The maximum size of the name and the number of characters that are actually significant depends on both the complier and the assembler. Most compliers will let you make a name as big as you want. They just ignore all characters beyond the last significant one. In the case of DUGGER's C, eight characters are significant to the complier, but only six are kept if the name is used for a function, or a variable that will be referenced by the assembler. Most compilers distinguish between upper and lower case. The convention in C has been to use upper case for symbolic constants and lower case for everything else.

#### INT'EGERS

The integer is the fundamental C data type. The size of the integer is the "natural" size of the target machine. For 6809 users that means 16 bits. The integer is a signed quantity with a value ranging from +32767 to -32768. The integer data types are declared as:

int namel, name2;

The integer can be further modified with the adjectives "short", "long" and "unsigned". The long integer is typically twice the width of an integer. An unsigned integer can assume only positive values ranging from 0 to 65535. A short integer is only guaranteed to

be shorter than a long Integer. If that statement stymles you, don't feel alone. The short integer is very dependant on the particular compiler that you are using. These "adjectives" may be combined, which leads to some of the following declarations.

short int vari, var2; long int vari, var2; long vari, var2; unsigned long vari, var2;

Dugger and Intersoft do not presently support short, long or unsigned integers.

#### CHARACTERS

The character is defined as being one byte or big enough to hold one (in our case ASCII) character. The full eight bits are available so that you can use the char for any eight bit quantity. Character data types are declared as:

char name1, name2;

Character data types are sometimes expanded or "promoted" into integers, as when they are used in an expression with integers. They are usually passed to functions as integers. The C standard defines that no character in the local character set (again, ASCII for us) will ever by negative, however arbitrary bit patterns could be sign extended or left unsigned. The choice is either up to the implamentor or dictated by the machine architecture. Both Dugger and intersoft clear the upper byte when promoting a char to an int.

#### FLOATS and DOUBLES

Figat 1s short for floating point, which in C 1s represented as a 32 bit number for most machines. A double is a 64 bit floating point number. The value of a float or a double can range between (10exp+38) to (10exp-38). According to the C standard, all floats are promoted to doubles before being used in a calculation. Floats and doubles are declared as:

float name1; double name1;

Neither Dugger or intersoft presently support floats or doubles, although Dugger claims that release two will support them.

#### ARRAYS

An array In C is the collection of a data type where any given member of the collection can be accessed via an offset from the first member. Arrays are statically declared in C. That means that their size is fixed by the source code and cannot be altered at run time. All arrays in C have zero as their first indice, therefore the declaration for an array of fifty integers would be:

int arg[49];

A statement like this would create 100 bytes of storage; however the storage would not be initialized automatically. It would contain garbage until initialized by you in some segment of code.

The indice of an arrey is always scaled by the size of the data contained in the array. This is done by the compiler.

Arrays cannot be passed to a function as a whole like

they can be in Pascal. What gets passed is either the value or the address of a particular member. Passing the array name to a function defaults to the address of the first member of the array. Here are some examples of how an array and its members are accessed:

```
Int nmb[15]; /* the array declaration */
func(nmb); /* address of first int */
func(nmb[0]); /* value of first int */
func(&nmb[1]); /* address of the "ith" int */
```

When the array is a formal parameter declaration for a function the size of the array needn't be specified. Assume that we want to write a function to skip over any leading nonprintable characters in a line of text. Further, we want to put this function in a library. A character string, which is really an array, always ends in a NULL so we don't need to know the maximum length. We just go until we find a nonprintable or the NULL. Here's what the function could look like.

```
skip_white(stg)
    char stg[];
    {
    int i;
    i = 0;
    while (stg[i] (= ' ' && stg[i] != NULL)
        i++;
    return(&stg[i]);
}
```

There are a couple of things to note here. The first is declaration "char stg[];". This just told the compiler that the function would be working with an array. The second is to realize that the indice is merely added to the base pointer which is considered to be the address of element zero. We could have actually passed the function the address to some middle character of a string. The function doesn't know and doesn't care. What you pass it is considered as the base address.

Arrays may be multi-dimensional. The declaration of a two dimensional array of integers might be:

```
Int numbers | 31(301;
```

Access to an individual element would looks something tike:

```
number[11[]]
```

This somewhat pecular format is used because C would consider number to be a one dimensional array where each element is itself an array. The ordering, or signifigance, of the indices goes from right to left.

When declaring a multi-dimensional array as the formal parameter to a function, the highest order, or leftmost, indice may be left empty but the lower order indices MUST be declared with the correct size. This allows the compiler to calculate the offsets correctly when you vary an indice other than the rightmost one. For example:

```
function(arg)
int arg[][[0][20];
```

The above declaration is a good example of scaling.
The rightmost indice is scaled by two to account for the

Integer data type. The middle indice is scaled by 40 and the leftmost indice is scaled by 400.

#### **POINTERS**

If you like pointers in Pascal, then you'll love them in C. for the uninitiated, a pointer is really the address of some object; that is it "points" to the object, hence the name. In C they are declared as:

```
type *name:
```

Here a few examples:

The leading asterisk implies indirection or pointing. Be aware that data types may vary in size, but a pointer always has the same size, in our case it is 16 bits, the address width of the 6809. Pointers in C can point to anything, including other pointer, arrays, function etc. Let's assume that we want to write a device handler for an ACIA in C. The function could look something like this.

```
ttyini)
{
  char *status, *data;

  status = 0xE004;
  data = 0xE005;

while (*status & 0x01 == 0) ;
  return(*data);
}
```

Here the status and data registers are declared as pointers to 8 bit objects or char's. The pointers are set to the appropriate addresses. The receiver status bit, here assumed as bit #0, is polled until it becomes one, then the data is returned to the caller. The "0xE004" is the notation for a hexidecimal number. The semicolon following the while conditional was necessary to prevent the compiler from using the next statement as the body of the while. What we have made is a polling loop out of the while conditional itself.

Pointers can be manipulated arithmetically. They may be incremented or decremented. They may have variables, expressions or constants added to and subtracted from them. Pointers cannot be used with multiplication or division type operations. As with arrays, the complier will scale arithmetic operations on pointers to account for the data type that is being pointed to. Consider an integer pointer "number" used in the following statements:

```
number++;
number = number + };
```

The first statment adds two to number. That is fairly intuitive. The second statement scales "I" by two also. This may not have been so obvious.

Pointers may be used in comparison, but with caution. They may be compared against other pointers freely and against NULL. Beyond that consult the manual for your particular compiler. You may get some funny results.

Let's rewrite the function skip\_white() using a

Using the pointer results in code that is much more efficient and faster. There is no need to keep an indice around. There is also the benefit of not having to calculate the element address for each of the compares. Still, there will be times when an array is the best way to go. At least in C the choice is yours.

#### CONSTANTS

Like most other languages, C allows you to have literal and symbolic constants. They may be numbers, characters or strings, as shown below:

```
decimal number -- 1234 octal number (leading 0) -- 01234 hex number (leading 0x) -- 0x1234 character literal -- "a" string -- "this is a string".
```

Symbolic constant are set up with the "#define" construct:

```
#define symbol constant
or
#define ESC 0x1B /* define Escape */
```

Symbolic definitions are usually done at the front of the program. Note the convention of using upper case for the symbol name. Commonly used definitions are often kept in a file that is brought in by the compiler via the "finclude" construct. These file are usually referred to

Constants, whether literal or symbolic may assigned to variables and used in comparisons. Special mentions should be made of the string literal. What is actually taken is the storage address of the first character. Hence, a declaration and assignment like:

```
char *p;
p = "this is a string";
```

as header flies.

actually sets  $\boldsymbol{p}$  to the address of the first character of the string.

In general, a constant can be used any where a variable or expression could be; including the string where again the address is used. Note however that string cannot be used directly in a conditional unless you really mean to compare the addresses.

#### STORAGE CLASSES and SCOPE OF VARIABLES

The C language lets you specify how the variable will be stored. The three basic storage classes are automatic, static and register. There is also the extern, or global, which is a special form of static. The scope of a variable is that portion of a program over which the variables exists, which depends on the storage class.

AUTOMATIC variables are those that are declared inside a given function. Space for them is allocated on the stack when the function is entered and removed just prior to returning from the function. The scope is the length of the function in which it is declared.

```
function!()
{
    int num;
    num = 32;
}
function2()
{
    int num;
    num = -762;
}
```

Both function() and function2() have an integer variable called "num". In each case it is unique and exist only while the program is executing the function in which it was declared.

STATIC variables are allocated permanent storage. Their scope, like that of automatic variables, is limited to the function in which they are declared. Statics give you a way of leaving a variable around after the function is left. It will contain the same value when the function is entered again. Let's a write a simple function to keep a count of the number of characters and lines that have been sent to the terminal. We will assume that this function is called by the terminal handler.

```
pretty term(c)
      char c:
      static int total chars = 0;
      static int totallines = 0;
pretty_term(c)
    char c;
    {
   static int total chars = 0:
   static int total lines = 0:
   if (c != CR)
       1
       send(c);
       if (++total chars == 80)
           send(CR);
           total_chars = 0:
           total lines++:
       eise return;
  if (total lines == 60)
      while (total lines++ ( 66)
           send (CR);
      total_lines = 0;
      1
  }
```

REGISTER variables are held in a register of the machine. There are times when it is advantageous to do this, such as when a variable will be used often in the function. A register variable can save code and

increase execution speed. A good example would be a loop counter or maybe a string pointer. Register variables are declared as:

```
function(stg)
  register char 4stg;
{
  while (*stg != NULL)
     toupper(*(stg++));
}
```

The usage of register variables is limited to the formal parameters and the automatic variables of functions. Hence their scope is the function in which they are declared. If you accidently name too many register variable in a function, the compiler will change the excess declarations into automatics. Neither Dugger of intersoft support register variables. The 6809 really doesn't have the spare registers anyway, although it could probably handle one.

EXTERN, or external, variables are the C language globals. They are allocated permanent storage like the static but they must be declared outside of any function. Their scope is defined as being from the point in the source file where they are declared to the end of the file. An extern existing in the source file would be declared as:

```
char buffer (801:
```

On the other hand, if the veriable was raally daclared in an other file but also naeded to be used here also, then it would be declared as:

```
extern char buffer[];
```

More technically, the first form is a definition in that it causes storage to be allocated for the variable. The second form declares to the complier the characteristic of the variable, in this case an array of characters whose size is unknown. This is again an area that is dependent on the particular complier.

Both Intersoft and Dugger allow externs, but not real statics. Note that Dugger claims to have statics, but really means externs or globals.

#### INITITALIZERS

One of the nicer features of C is INITIALIZERS. They allow you to give statics and externs an initial value as part of their declaration. Note that automatic and register variables may not be initialized.

Two simple examples of initializers were given in pretty term(). Here two static integers were cleared to zero. The initialization was done only once, upon program entry. The function then changed them in whatever fashion it needed to.

Even arrays can be intialized, if they are static arrays. Let's write a function that translates the first five digit names from Spanish to English. We will do it with a two dimensional array.

```
translate(stg)
```

```
char #stg; /# pointer to the Spanish #/
(
int i;
static char number[2](5) =
    ("Uno", "Dos", "Tres", "Cuatro", "Sinco"),
    ("One", "Two", "Three", "Four", "Five");
```

```
for {i = 0; i < 5; i++)
    if (match(stg, number[1][i])
        return(number[2][i]);</pre>
```

```
return(0); /# failed -- no match #/
```

The first thing to note is how the array was initialized. There were two lists each containing five names. This is consistant with how C looks at an array. However, each list of names was enclosed in brackets which were in turn separated with a comma. The second thing to note is that the array is really an array of pointers to the strings.

I don't want to spend too much time on initializers. Dugger and intersoft do not support them. Besides, if you really need sophisticated initializers, then you should be reading the C manual.

There is one more data type that we haven't coverad and that is the "struct" or structure. It is analogous to Pascal's racord. It will be covered towards the end of the serias.

I was reading the January issue of BYTE where I notice an ad for another Flex, 6809 C compiler. It is offered by the introl Corp. of Milwaukee, Wisconsin. It is in the \$300 price class and comes with its own assembler, linker and library manager on 8" disk. I called them and had an interesting talk. They claim that the only parts of the language not supported are long, floats, doubles and bit fields.

I would also like to apologize for constantly pointing out what Dugger and Intersoft do not support. It is not meant to disparage their products. It only done because the tutorial should be true to the C standard, not to a particular product. Both Dugger and intersoft offar a reasonable compiler if you do not need the complete language. And they are both planning upgrades.

Next time we will cover C's set of operators. At that point you should have a good enough understanding of the language to try some programming.

#### Bit-Mapped Graphics

by Dr. Samuel I. Green 13052 Ferntrails Lane Creve Coeur, MO 63141 5 January 1982

I am extremely indebted to '68' Micro Journal and Mr. Thomas H. Hunt for his articles on memory-mapped video boards. I have had an F & D video board for several years, and I gave up on trying to get rid of the streaking and flicker. I had it working streak free once by accessing memory on Phase 1 for video display, but it wanted to run at a strange clock frequency and seemed touchy. The first article by Mr. Hunt eliminated white on black streaking. I was thrilled.

I was even more thrilled with his second article which converted the F&D video memory to bit-mapped graphics. I have done the full conversion and it works great. I had been planning to do a similar conversion, and I'm sume Mr. Hunt saved me six month's labor and

frustration. Thank you and thank you again.

There is a fix for the close spacing of characters which resulted from putting a 7 x 9 character in a 8 x 12 cell. IC 36 is a 74163 which divides the dot clock by 8 to get the character clock. I reconfigured the circuit to divide by 10 by unplugging pins 12 and 13 of IC 36 and connecting pin 11 to pin 12 at the socket. This results in a 10 dot wide cell in which the 7 dot wide characters look very good. Two other changes are then required. The crystal must be changed from 12.44 MHz to 13.5 MHz or thereabouts (for 64 characters per line), and the graphics EPROM has to be changed to get equal spacing between graphic elements. Each byte in the graphics EPROM whose second hex character is 'E' is changed to end in '7'. That is each \$0E becomes \$07 and each \$EE becomes \$E7.

I will correct some errors which were found in the articles. Then I will describe a simple change to the driver which makes it more useful and discuss what I've done lately.

In figure 2 of the first article (page 30 of October 81 issue), which describes modifications to the F & D board to minimize access flicker, IC 35 (a 7420) is mislabeled as IC 20. Also pins 1 and 2 of IC 27 (a 7404) are interchanged.

There are some omissions from the circuit shown in figure 1 in the second article (page 26 of December 81 issue). There must be a wire from IC 17 pin 9 to pin 3 of the 74157 which is added piggyDack to IC 22. This wire carries address line A12. Also pins 1 and 4 must be connected at the socket of IC 20 where the

74LS138 now replaces the 74LS139. This allows the address decoding to enable the board.

I had to make one additional change to center the blanking gate on the video. By trial and error I added capacitance to IC 35 pin 9 until an extra vertical line at the left hand edge disappeared and the missing line at the right hand edge fully appeared. The value was 3900 pF.

Mr. Munt's program called GRAPH performs initialization, screen fill, and setting, resetting, and inverting of single pixels and lines. Set, reset, and invert are selected by whether the value of MODE is 0, 1, or greater than 1. I added a few lines of code so that if mode is negative, the value of the pixel is returned without any changes. This is useful in games which will be my main application. Also it is

convenient to be able to start a figure with the LINE routine instead of the PIXEL routine without drawing anything on the way there. The changes include one RMB 1 called STATUS preceding MODE and the addition of five lines of code in the block called = CHANGE PIXEL IN REFIRESH MEMORY starting at \$745C.

0026	STATUS	NE	1
0027	MODE	NB	1
	×		

	x Change	e pixel	in refresh	HEHOTY
	I			
745C DE 00		LDX	TEMPS1	
745E 06 27		LDA B	MODE	
7460 2A 0B		BPL	SET	Positive?
7462 7F 00 26		CLR	STATUS	Status=0
7465 A4 00		AND A	0,X	Pixel value
7467 27 03		BEQ	RETURN	Zero
7469 7C 00 26		INC	STATUS	One One
746C 39	RETURN	RTS		
7460 26 05	SET	BHE	CLEAR	Original
746F AA 00		ORA A	0,X	
7471 A7 00		STA A	0,X	
7473 39		RTS		
7474 C1 01	CLEAR	CHP B	1	

First I used GRAPH to set some points and draw some lines and execute Mr. Hunts DEMO program. After a few days of that I patched the TSC Extended Disk Basic to the Graphics Drivers using PEEK, POKE, and USR functions. Using Basic I wrote a game in which two players input muzzle velocity and elevation angle to cannons in their respectiveive forts and alternately fire at each other over randomized terrain usually including a tall mountain. The shells leave a trail in the air and blast craters in the terrain when they hit. Of course all physical laws are obeyed, and the wind always has a large asymmetrical effect.

I tried doing some moving figures by drawing them from a look up table, erasing them, and redrawing them at a slightly different position. As you can guess, it was slow and flickered badly. The basic interpreter wasn't fast enough.

Then I tried using the A/BASIC compiler. It is patched to FLEX2 by using the instructions in Kilobaud Microcomputing. The June '81 68XX article has the instructions and listing, and the December '81 68XX article has the correct addresses for FLEX2.

A/BASIC is strange and difficult to use at first, but the resulting code is super-fast. Now I have to put in delay loops to keep the Klingons on the screen

long enough to start aiming the gun turret.

I reassembled the Graphics Driver routine to get the base page variables out of the way of A/BASIC and used PEEK, POKE, and CALL(user) again. But A/BASIC lets you specify the location of variables in memory, so I reassembled the Graphics Drivers again. This time I used RMB 2 for the variables to be passed, since A/BASIC uses 16 bit variables. Now I can simply set variables and CALL the appropriate Graphics Driver routine.

I put the shape of a figure to be drawn in a look-up table. In TSC basic the coordinates and status of points and lines can be entered in data statements. In A/BASIC the coordinates and status are entered in two's-complement hex directly into memory where the appropriate variable array was defined. Then to draw an attacking Klingon, the main program passes the center coordinates to a subroutine which erases the old figure and draws the new one scaled to a larger size. There's plenty of time for all this with the compiled Basic.

Thanks again to Mr. Hent.

## SWTPC C

SOUTHWEST TECHNICAL PRODUCTS CORPORATION, a San Antonio, Texas based pioneer in the Micro Computer Industry, has just released its "C" Compiler to the Data processing marketplace. The "C" Compiler is a "full-blown" C developed by James McCosh. With all of the dust rising over the numerous "C" compiler subsets about to hit the scene, SWTPC did it again—living up to its corporate philosophy, of being "OFTEN FIRST—ALWAYS THE BEST."

"C" is the first true serious attempt at standardization. In the past, attempts made with the various standards COBOL, imposed on yet today standardization is far from a reality. PASCAL is also making an attempt for "C", however, is not standard code. hardware dependent--any system or a Unix-base will find "C" produces fully transportable code from one machine to another.

With the release of "C", emerges an entirely new nomenclature for Data Processing techniques. Just like SWTPC was the driving force creating the niche for micro-computers, "C" opens the race for a micro-mini. The recently announced S+ System, with a separate optional I/O

preprocessor and service to 32 serial ports is the beginning. Now about the only difference between a mini and our Micro, is the bottom line on the invoice.

The support of DMA and SMD mass storage devices offer latitude and choice seldom found in this industry. Best of all, upgradability is the name of the game. You can start with an S/09 with 128K of RAM memory, dual 8" diskettes and 1 CRT and expand to 1024K of memory, 2.3 gigabytes of disk storage and 32 terminal devices with I/O preprocessing and VIAbus 408K-baud networking. This is UPGRAD-ABILITY!

\*UNIX is a registered trademark of Bell Laboratories.

## 680X User Notes

PROGRAMMERS FOR THE 6809

The following request for programmers for 6809 machines turned up in the Northwest Computer Club newsletter, 10/81. For those talented and interested in making some money, I'm reprinting the letter below:

letter below:
 "I am writing to give you a brief description of what I am doing. For the last 8 months I have been writing programs for the TRS-80 Color Computer. Most of these programs are in machine language and they are selling well. I am, however, becoming increasingly bogged down by the business end of this venture. I intend to make Spectral Associates a leading producer of high resolution color graphics software with primary emphasis on the 6809. We currently have the new Hitachi MB6809 Personal Computer and will soon get the Fujitsu Micro-8. Neither have been released on the American market and both use the 6809.

I need capable programmers who want to write good, quality software, on a royalty basis for these machines. I have high level contacts at Tandy, Hitachi and Pujitsu which will prove very useful. I have complete development facilities here in Tacoma for those who wish to avail themselves."

Mr. Rosenbaum also said that the Hitachi has Microsoft BASIC and a dissassembler. It has 32K with 17K video control and overhead, 14K for programs. Interested people should contact him at: Spectral Associates, 14H Harvard Ave., Tacoma, WA 98466, (206)565-8483.

NOTES ON THE TRS-80 COLOR COMPUTER

Speaking of the Color computer, I ran accross some notes by Dean Alexander of the Rochester NY TRS-80 Club, in their LLIST newsletter, 10/81. The following is Dean's article:

Contrary to the limited information provided in the Reference Summary section of the Extended Color BASIC Manual for CSAVEM function, the proper numeric input to write out a machine language file to cassette is to use decimal numbers (not hexidecimal as shown. This error is also duplicated on the quick reference card. The proper format is: CSAVEM "Name". Start, End, Transfer. Where "Name" = filename: start, end and transfer are decimal numbers representing the start, end and transfer addresses of the machine language program.

bers representing the start, end and transfer addresses of the machine language program.

There is an excellent program in the April '81 issue of '68" MICRO JOURNAL, page 26. by Ralph Tenny. For the hobbyist who wants to do a little fooling around with machine language programming on the color computer, this program allows the user to examine memory, enter machine code directly in hex, and verify the entry.

This program is similar to the one on page 4-1 of the "6809 ASSEMBLER LANGUAGE PROGRAMMING", by Lance Leventhal and will serve as a basis for working out many of the sample programs described in that excellent publication.

or the sample programs of the sample programs of the temperature of the following table represents the full source and object code structure of a sample program which moves the contents of Memory Location 12010 to Memory Location 12011.

MEMORY DECIMAL	ADDRESS IN HEX	OBJECT CODE	OP CODE	OPERAND
12001	2EE1	B6 2E EA	LDA	\$2EEA
12004	2EE4	B7 2E EB	STA	\$2EEB
12007	2EE7	39	RTS	
12008	2EE8	00		
12009	2EE9	00		
12010	2EEA	FF		data
12011	2EEB	00		

(note: 12008 & 9 are memory locations not used, 12010 is the data to move, and 12011 is the store address.)

Add the following program to the above referenced Machine Code Entry Program from '68' MICRO JOURNAL:

500 REM MACHINE LANGUAGE CONTROL

510 DEF USR0=12001

520 A=USR(0)

530 X=PEEK (12010)

540 PRINTX;

550 Y=PEEK(12011) 560 PRINTY

570 FND

570 END

Type "RUN" (This gets into the Code Entry Program)
Type "E" then "2EE1"

680% USER NOTES, by Dave Baxter

The following notes are from Dave Baxter's 680% column in the November issue of RAMS NL. Dave talks about the TSC FLEX NEWSLETTER.

The Flex Newsletter No. 5 came out recently. It has the usual mix of TSC product descriptions and user tips. The first section is news of new Flex users among the Japanese computer manufacturers. Adtek of Japan has translated the Flex manual set into Japanese for use on their 6809 system aimed primarily at industry and system development. TSC also anticipates that FLEX will be available for both the Hitachi MB6809 and the Fujitsu Micro-8. Both of these machines sport color graphics as a selling point.

Per the newsletter, here are the current version numbers of their major software packages:

PROGRAM	6809 ver.	6800 ver.
6809 PASCAL	3	
Extended BASIC	19	17
Ext. BASIC Precomiler	4	2
BASIC	15	13
BASIC Precompiler	3	2
Text Editor	2	
Assembler	2	
Text Processor	4	
Sort/Merge	3	3
Dehug	17	

If your versions are less than these you may want to take advantage of TSC's liberal update policy. If you have owned the package for over two months, send the original disk and proof of purchase along with ten dollars to TSC and they will send it back updated. Owners under two months, the updates are free. I've done it with my software a couple of times. Be prepared - the BASIC keeps getting bigger each time and your bigger programs may not fit with it.

may not fit with it.

The tips section of the newsletter talked about an interesting feature that I didn't know about the MON command. When using the MON command, it is all right to put additional parameters following the command. That is, if you wanted to run a command such as LIST, but -anted to put in a few patches before running it, the two step process would be as follows. First get the command in the Otility workspace using the GET command (++++ GET

LIST). Second, use the MON command with the additional parameters to get to the monitor to do your patching (+++MON 1.SOURCE.TXT). When finished with the patches, jump to the starting address of the command (SA100 or SC100 usually) and it will run picking up the additional parameters as if input with the command.

The final section of the newsletter is the listing of a Sl load utility. The program in the newsletter could be used to send binary programs with a modem. It could also be used to exchange programs and text with a non-Flex system, such as EXORCISOT OF OS-9.

#### SWTP COMPATABILITY WITH SWTP

SWTP 6809 computers are now all being shipped as 2 MHz units. SWTP offers an EPROM programmer board, the MP-R. The MP-R, and it's software are meant for operation at 1 MHz only. It will not work in a 2MHz system. A call to SWTP about this brought news that the MP-R will only work at 1MHz, and there are no plans to redesign the board to work at 2MHz. The ROM/RAM boards on these systems will probably have a few empty sockets!

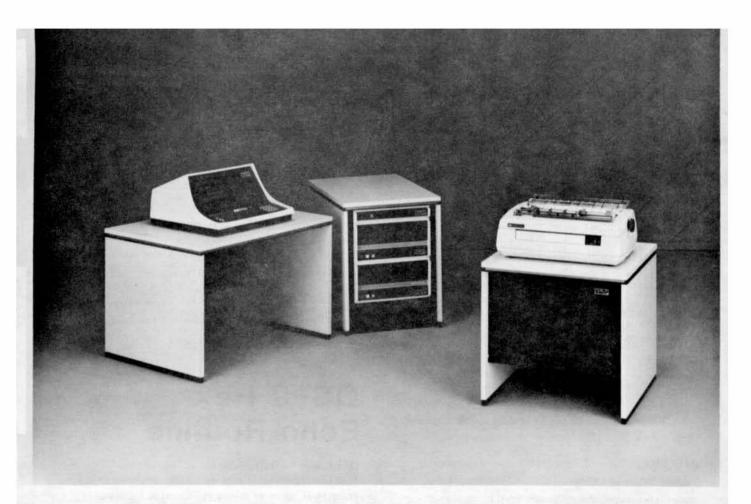
From ACG-NJ Newsletter - Tnks Guys

## OS-9 Hex Echo Routine

This summer I finally received my long awaited OS-9 operating system from MICROWARE. After bringing it up on my hybrid SWTPC system I discovered that there was virtually no method (with the system as supplied) to setup and control my console CRT and system printer. Both of these devices require ASCII control characters in the range of hex 00 to hex IF for initialization and function changes.

The 10 manager supplied with OS-9 for support of serial character devices filters out all but a few ASCII costrol characters. Further, the ECHO routise supplied will only echo characters that were input via the terminal. If you do not have a terminal that can generate all the ASCII costrol characters then you have no way to output these characters. This is the case with my CRT terminal, a modified TI-914 unit.

To overcome this problem I developed a hex echo routine called HECMO. This routine is written in reentrant 6809 assembly code designed to operate as an executable module in the OS-9 environment. The routine works by taking two ASCII characters from the input path and converting them into one hex byte. Any characters inputed that are not in the range of 0 through 9 or A through F will be converted to hex zeros. Also, spaces will be ignored and characters and converting each into a hex nibble in the event that there is an odd number of input characters the last nibble will be defaulted to



## THE COMPLETE BUSINESS SYSTEM \*Multiuser\*Highly Expandable\*Cost Effective

#### S+ THE CONCEPT

The S+ system is a modular computer system in which all portions of the hardware and software are designed to work together in the most efficient way possible. An S+ single user system with floppy disk storage is a competitive and cost effective entry level system. Unlike most other small computers being sold as "personal", or "small business" machines, the S+ system may be expanded to maximum capabilities using this same hardware and software. You cannot end up with a DEAD END system that cannot be expanded and whose software is not compatible with larger machines. A basic S+ system may be expanded to thirty-two users, a megabyte of main memory and hundreds of megabytes of hard disk storage by simply plugging in, or connecting the desired upgrade equipment.

#### TOTAL DESIGN-Hardware and Software

The S+ system is an integrated hardware and software design. The two complement and enhance each other in this system. The UniFLEX® operating

system used in the S+ systems is patterned after the Bell Laboratories UNIX® operating system, one of the most admired and widely used operating systems in the world. Instead of being an afterthought, the software is part of the design of the S+ system. You can be sure that with this approach that all parts of the computer operate with maximum efficiency and cost effectiveness.

#### THE CENTRAL PROCESSOR

The basic S+ system is configured with 256K bytes of memory and can be expanded to more than 1 million bytes. An efficient and fast hardware memory management system is used to allocate the available memory among the users on a dynamic basis. As little as 8K bytes, or the entire memory—if needed—can be used by any individual user. This makes it possible to run very large programs on the system, but it also uses no more memory than necessary for a particular job. The increase in cost effectiveness of this system over crude and outdated bank switching arrangements is dramatic.

The central processor runs in both user and supervisor states. It can detect and reject a defective user program. It is impossible for a user program to go bad and stop the entire system, as can happen quite easily in less sophisticated systems.

Task switching is accomplished by use of a multiple map RAM memory, with sixty-four individual task maps. Each task can access from 4 to 64 K-bytes of memory. Multiple tasks may be used in programs that require more than 64K bytes of memory for execution. When a task is completed the memory is automatically released for other use.

#### SOFTWARE

The S+ operating system, UniFLEX® is a multiuser, multitasking operating system based on the UNIX® operating system that has been used for many years on Digital Equipment Corp. PDP-11 series minicomputers. It is considered one of the most sophisticated and "user friendly" operating systems available. Variations of UNIX® are rapidly becoming standard on mini and larger microcomputers.

A large variety of languages are available for use with the system. These include FORTRAN, COBOL, BASIC, and Pascal. Word processing packages are also available to give you full text processing capability on the system.

Applications programs are available in large quantities in many fields. This includes general business, medical, dental, veterinary, library and real estate management; plus others. Since the system is multiuser it can also be connected to cash registers to produce a point-ofsale terminal system combined with the computer. The possibilities for application of this system are endless.

#### THE I/O SYSTEM

The S+ system is totally interrupt driven. All terminal and printer I/O devices connect to an I/O bus separate from the main bus. Up to thirty-two separate devices may be connected to the I/O bus at any one time. If I/O activity is great enough to cause an unacceptable slowdown in system operation, a separate I/O processor can be installed in the system. This plug-in option removes all I/O handling

overhead from the main processor and allows operation of up to thirty-two external devices at 9,600 baud. Without an integrated total design, as in the S+ system, it would become impractical to use a UNIX®type operating system in a situation with heavy terminal I/O activity.

#### **DISK STORAGE**

A wide range of disk storage capacity is available for the S+ system, from 2.5 M-byte floppy disks to an 80 M-byte Winchester and many sizes between. All disk controllers use direct memory access (DMA) type operations to maximize data transfer and to minimize overhead on the main processor. The Winchester disks also use intelligent controllers along with DMA transfers to preserve the performance that these type devices are capable of giving. Without this distributed intelligence the system performance would be greatly degraded. The UniF LEX® operating system is designed to work at maximum efficiency with this type disk system. The data transfer rates achieved by this combination rival those of large minicomputers.

#### **COMMUNICATIONS**

A high speed local network communications system is available to interconnect S+ systems. The VIA-BUS® network will allow communication between systems at data rates of over 400K baud. Such a system makes it possible to share data between local systems in an efficient and low-cost manner.

#### AVAILABLE SOON

Tape backup—20M-Byte in less than 15 minutes on a standard ¼ inch cartridge.

Mini-Wini-5 and 10 M-Byte Winchesters-5¼ inch package. Winchester performance, for smaller systems in a small package. UniFLEX® compatible design.

Large Capacity—190 and 340 M-8yte Winchesters, plus SMD cartridge drives.

UniFLEX is a registered trademark of Technical Systems Consultants, Inc.

UNIX is a registered trademark of Bell Labs.

VIABUS is a registered trademark of Southwest Technical Products Corporation.



SOUTHWEST TECHNICAL PRODUCTS CORPORATION 219 W. RHAPSODY SAN ANTONIO, TEXAS 78216 (512) 344-0241 00063

Only two types of errors will result in the program module being aborted. These error types are: more than 40 bytes of output generated and OS-9 system errors.

.

Microware 05-9 Assembler 2.1 | 10/31/81 25:08:19
NEE ECHO ROUTINE -

....

IIIII Her character echo sodule

```
This routine converts ASCII characters to the rongo IIIII
                      0 through 4 and A through F into their 0001 calent
               IIIII bes raise of $ tayout F. Other than the above ASCII tittl
               IIIII charactery will be converted to a her &. The ascil
                      characters should be entered in Pairs to avoid
 00000
               .....
                      errors. The input is terminated by a 'Min Line's
                                                                                  ....
               IIIII Character, Spaces are species, All ASCII
 00011
               fill! alphabetic characters out to upper case to be
                                                                                  ....
 80012
               sees recognized.
                                                                                  ....
 00014
               sees the input to the sodale aust be in the command
                                                                                  ....
 60015
                      line. However, the outsut may be redirected to
                                                                                  ....
                      any rated 05-9 fale or device.
                                                                                  11111
 80017
               ....
                                                                                  ....
               sses A typical command sould bes
 0001P
 86619
               ....
                                                                                  ....
                            HECHO >/P 18 45 "HEB LINE"
 00020
               ....
                                                                                  ....
 00022
               11111 This command would cause the ESC E segurace to be
                                                                                  11111
                      sent to the printer. If this printer was a MI-00.
 00023
               ....
                                                                                  ....
                      them is would be smitched to the behanced print o
                                                                                 . 22111
 00025
               ....
                                                                                 ....
 0002a
 00027
               ....
                                               BY
                                                                                  ....
 00028
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 00029
                                        JOHN J. STRONG
 66636
               ....
                                        P.O. BOX 2242
RICHARPSON, 1EXAS 78000
                                                                                  ....
                                                                                  ....
               ....
 00031
               [[000000331]8000030100001018000000180003188003318800018000018000
 00033
 00024
               s Set 05-9 header variables
 66635
 00037
        8010
                                    SFT 200018000 Program moduly identification
         4001
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                                           200000001
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                                    SET
 00039
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         4441
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                                           F.S. 8. H100
00046
00047
00068
00047
00030
00031
00032
                                            HEE ECHO ROUTINE
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                                           NEERD, NAME . PER-COJ. REENT-REV. MAIN. STOR
        0000 B7CB0000
0004 00001831
0000 R0001701
0000 00
0009 48454548
                                                        MOS HEETER, MANE . PRINCOST, REPUT-NEV. NATH. STOR
                                           .HECHO.
         4011 DE
                                                       MANE FOS "NECHO"
90054 90057 9 0000 90058 9 0028 9 0028 9 0029 9 0000 9 9100 90042 90063 90064 0012 90064 0012 90064 9019 90079 9016 90071 9021 90077 9016 90071 9021 90077 9021 90077 9021
              2 Bate areas faintene www.y allocation is 756 bytes!
                                                       Output bafter
                                    973
973
UI
                                                       Character counter
                          STACK
                                          215
              I Cotry point
         0012 3164
                                    LEAY BUFF, U
                                                       Bet address of data area
                                    C.J
         0014 AFC828
                                           COUNT, U
                                                       tlear output boffer country
         0017 4461
                                           0,1
                                                       Bet character from input buffer
         8019 8120
                                     CWA 4520
                                    DED
CAPA
                                          SPACE
         0019 2766
                                                       If yes then 46 to skip seace routing
                                                       Else to 11 4 MEN, LINE character
         0019 B108
                                    BEÐ
                                           COST
                                                       If you then 40 transmit buffer
                                    858
         0021 0053
                                                       Etse on to conversion routine
         0023 6764
                                    ATE
                                           0,1
                                                        Store her byte in output buffer
00073
00074
         0025 3121
                                    LEAN
                                          1.Y
                                                        Increment rep T by 1
        9027 ACC826
                                    INC
                                                       Increased output counter
                                    LH
                                           COLET, N
                                                        Ort mitest count
                                    DEF
                                          840
00076
         0023 C128
                                                        Is output count equal to 40
                                           FIER)
00077
         D02F 271A
                                                       If equal go to error coutine
                                                       Continue processing input
```

00000		l Stig sp	aces row	line		
00001		1				- Company Company
00083		3001 2050	SPACE		1.1	Increases input pointer Continue processing
10004		1		Brest	CEVI	CONTINUE PROCESSING
00003		s Output	buffer re	30114		
66000		1				
20087 20088		300820	CBILL		COUNT .U	Bel address of output counter
00000				E DD	0,1	toad ray 8 with length of output Clear ray A
00000				TER	D. Y	Rove reg B to reg Y
1900	003	3004		LEAT	BUFF, U	Bel address of output Defler
10092	0011	1048		LDA	01	Load cutput path number
		107/80			1910055	
00094		2505		162	<b>ESSUS</b>	Branch of error
10044		t Horeat	return			
0097		:				
0010	6046	¥		CLUB		Set error number to 0
		2002		MA	EM2	Return to 65-4
10100		1 Error	ad a			
0102		1	£/@=			
0103	0048	EAAD	EMI	100	FII	Load "EtCESSIVE VERBARE" error austor
		183F06	ERR?	051	FOETET	Hatura to OS9
0105		!				
10106 10107		Set a b	yte from	the inp	st lier	
90108	8030	Acd a	GE 13	LBA	0.1	Load rug A mith character from moput line
00109			DE 14		8100	Is it 4 MED LIME character
0110	0004	2492		100	614C	If not go to SINC
90111				CLRA		Else clear rep 8
10112			d Fresh	AIS		
10113 10114	9200		\$THC	LEAT	1170	Journal reg s by E Is it a space
0115				E1	ETD.	Set nest short character
0116	005E	39	Œ	RIS		
10117		1				
11100		1 Convert	one ASC	lt byte	to One HE	n:bale
0120		AMEX	HΕΙ	900	8611	Set one seput byte
0121			761		4130	Subtract ASCII 0 from Character
0122				301	IE12	If less that ASCII 0 go clear reg fi
00123				OPA	877	Else is character less than or equal ASCIL
10124				BLE	HED1	The return
0126	0069				05 11 HE82	Else is character less than ASCEE A Them go to clear eeg A
0127					8516	Else is character higher than ASCII F
0128					HE12	then on clear reg A
0129	6671	8007		SUBA	97	Else subtract 7 from theractor
19130			HETI	MTS		
12100			HE12	QPA		Clear character to hes 00
0132 0133		24		ATS		
10134		1 Consert	tup 4001	1 11106	to one P	El bite
0133		1				
			В	09R	₩.I	bet right her appele
10137				ASLA		Shift right
00138 00139		-		ASLA		nibble
	0078			ASLA		ta Laft orbbig
00141	007C			TER	A. D	Save reg A to reg B
10142	007E			DSA		Set left her albbie
10143				P915		Add reg 1
0144				AZDA	,51	tu reg A
0144		3V 572904		EHOB		
10147		BICTER	H€ERÐ			
0143				END		
				at md		
10000 a	- 45 th		allocate			
0000 e		ata butes				
0000 o 0008 0 0100 0	0756 4	ata Dytes ates used	for seed	OL S		
0000 0 0000 0 0100 0	0756 d 0427 b	ytes used				
0000 0 0008 0 0100 0 0101 0	0756 d 0427 b B	oggo p	RUF F	0037 L	17003	0017 L COMMY 0028 S COURT 0040 L EXXI
00000 0 00088 0 0100 0 0108 0	0756 d 0427 b B ERR2	9tes used 0000 B 0000 S	OUFF F1EIII	0037 L 005E L	CONTI BEND NETT	0017 L COMPT 0028 S COURT 0040 L EXHI 0050 L 0ETD 0058 L \$18Z 0008 E 40EH0 0000 S LAMBIT 00121 MAIN 0000 L MAME
	0756 d 0427 b B ERR2 HE1	oggo p	BUFF F1E111 HE11	0037 L 005E L 0074 L	BEND HE12	

## SUPPORT YOUR ADVERTISERS

### **BIT Bucket**

Francis E. Van Horn 418 Estes Street Herfreesboro, TN 37130

January 11, 1982

To The Editor.

In John Tucker's review of the Epson MX-100 in the Nov. 81 issue the following statement was made: "If you need to build a cable, the plug is the common one used with the Centronix and is widely available." This statement caused my printer cable to cost me about 66 more than it would have, if I had been given the correct information.

The plug is specifically a 57-30360 (AMPHENOL). This plug is used with the Centronics 702, 703 and 779, but is not used with several other Centronics printers.

Although this article does not specifically say so, it does imply that the printer comes with both parallel and serial interfaces. According to the manual I received and the printer I purchased, the parallel interface is standard. If you need a serial interface, it is optional and you would do well to make sure you order a printer with a serial interface.

With these exceptions I would agree with most of what Mr. Tucker had to say. It is an outstanding printer. And, the ability to set print size, density, and form length all under software control is a great asset.

#### HELIX ENTERPRISES

503 FORT DRUM DRIVE . AUSTIN, TEXAS 78735

January 11, 1982

Dear Mr. Williams,

Your help is solicited by our firm to locate customers of ours who purchased IBMPAK in Nay, 1981. The initial sales and distribution were handled by a separate firm who agreed to provide us with names and addresses of purchasers so we could provide updates for IBMPAK. This information was not made available to us, therefore we have no way of contacting these early customers.

If purchasers of ISMPAX serial numbers 1225-1235 will contact us, we will be pleased to provide them with updated software.

As we are now handling our own distribution and keeping records of our purchasers, this is no longer a problem. Thank you for your help is asslating us to locate these customers.

James G. Georgouli

CLELL A. DFLDY, JR. 2401 W 27TH ST. Penama City, FL 32405

I am enclosing the lieting of a four voice music program for Tandy's TREBOC signs. Attached to the listing is a hex dump of the music for Country Roads by John Denver.

This program is based on Chamberlin's algorithm, that was published in the Sept 77 issue of Byte. The first table is the digittzed form of one cycle of the organ sound. This consists of the fundemental, second harmonic and the third harmonic shelf amplitude. The second table is the note increment table. This is equivalent to the keyboard of a musical instrument. Note that adjacent increments are related by the twelfth root of two.

The program is written in a pausdo Pascal structure: the main program is at the end and consists of calls to procedures. The first procedure initializes the variables and turns on the sound. The next procedure reads the duration of the note to be played. If the duration is zero, the song is terminated and control is returned to Beaic. Procedure NOTE reads the main four items in the main. These are the values for pointers into the note table. Each will determine what note its voice will be playing.

The procedure PLAY consists of a timed loop of about 145 microseconds duration. Each time the loop is executed a sample of the four voices are summed and outguted to the D/A converter. At the ease time a number corresponding to the Tempo is decreasented and when it reaches zero the duration count is decreasented. This means that the duration of the note played is dependent on the tempo and the duration read from the suesc.

The main program then loops and procures the next note from the music, and on said on  $till\ the duration is zero.$ 

I hope that you and your readers may find some interest in this program. I don't seem to be able to find much published on machine language programs for the TROBOC.

My set up consists of a 88-50 buse computer operating under FLEx9 connected to my 19890C by means of the RS-232 port. The downloading and uploading is by means of Micro Works C8UG. I have been able to do this at 9600 baud.

l also as enclosing a cassette with Country Roads on it. You may be able to find a TRSSSC and play it.

chill O'che

	******			***************
4	• Phillips			PART MUSIC ON
6	:	HI THS	BU-C HICAD	COMPUTER
7		IN ELL	A. DILD	y JR.
B	:	NOV	EMBER 24.	19B1
D		USING	AN ALGORI	THPI BY
2	L FIRELI	SHED IN	THE SEPT	1977 LESIJE OF 1
	:		"BYTE"	
5		•••••	•••••	
7		NOM	MUSICIRSE	IOE
9		DFT	PAB	
0068	TEMPO	EQU	168	
FFFE	REBIRI	EGU	SFFFE BFF 20	RESET VECTOR A-TO-D CONVERTER
FF20 FF23	PORT	EOU	0FF23	TURN ON AUDIO TO TV
	:			
0040		ORB	447	
0040 0042	PLIMORA.	RILL	2	SONO POINTER
0042	VOICE	RHP	3	NOTE DURATION VOICE POINTERS
0046	VOICE2	RHB	3	
0049 004C	VOICE3	RMB	3	
004F	INC1	RMB	2 2	INCREMENTS FOR THE VOICE POINTERS
0051 0053	INC3	RMB	2	THE CTITIONS
0055	INC4	RMB	2	
1000		CARG	61000	
	:			
	I MAVER	CHOT TAE	LE FOR ORG	
1000 OD DE OF 11 1004 12 14 15 16	WFT	FCB		b0F, 511, 612, 614, 615, 616
1008 18 19 18 1C 100C 1D 1F 20 22		FCB		*1B, *1C, *1D, *1F, *20, *22
1010 23 26 27 29 1014 2A 2B 2D 2E		FCB		\$27,\$29,\$2A,\$2B,\$2D,\$2E
1010 30 31 32 24 1010 35 37 38 30		FCB		032, 034, 635, 037, 639, 039
1020 30 3B 3B 3C 1024 3C 3E 3E 3E		FCH		839, 63C, 63C, 63E, 63E, 63
1028 OF 3F 3F 3F 1020 OF 3F 3F 3E		FCB		•3F, •3F, •3F, •3F, •3F, •3E
1030 3E 3E 3C 3C		FCB		*3C, *3C, *3B, *3B, *39, *38
1038 37 37 35 34 1030 32 31 30 25		FCB		0.55, 0.34, 0.32, 0.31, 0.30, 0.25
1040 2E 2D 2B CA 1044 29 27 26 24		FCB		\$28, \$2A, \$29, \$27, \$26, \$24
1040 23 22 20 1F 1040 IF 10 IC 1D		FCB		\$20, \$1F, \$1F, \$1D, \$1C, \$1B
1050 LB 19 19 18 1054 18 16 16 15		FCB		619, 618, 618, 616, 616, 615
1058 15 15 15 15 1050 15 15 15 15		FCB		•15, •15, •15, •15, •15, •15
1060 15 15 15 15 1064 16 16 16 18		FCB	•15, •15,	•15, •15, •16, •16, •16, •18
1066 18 18 19 19 106C 18 18 1C 1C		FCB	•18, •18,	19, 19, 18, 18, 18, 1C, 1C
1070 IC 1D 1D 1F 1074 IF 1F 20 20		FCO		#1D. #1F. #1F. #1F, #20, #20
1078 22 22 22 22 107C 22 23 23 23		FCB		020. 021. 027. 023. 023, 023
1080 23 23 23 23 1084 22 22 22 22		FCB		•23, •23, •22, •22, •22, •22
1088 20 20 20 1F 108C 1F 1D 1D 1C		FCB		620, 61F, 61F, 61D, 61D, 61C
1090 IC 1B 19 19 1094 IB 16 15 15		FCB		619, 616, 618, 616, 615, 615
1098 14 12 12 11 109C OF DE DE OD		FC8		\$12.811.80F.60E,60E,60D
1000 08 08 00 09 1004 08 07 07 06		FED	449, 809,	BDA. 80B. 80B. 807, 807, 806
10AC 03 03 01 61		FCB		004, 403, 803, 403, 401, 401
1080 01 01 01 01 1084 00 00 00 00		FCB		801, 801, 800, 800, 800, 800
1088 GD 01 01 01		FCB		01,801,801,401,801,801
10C0 03 03 03 03 10C4 03 04 04 04		FCB		603, 803, 603, 904, 804, 804
10CB 04 08 06 66		FCB		806, 606, 806, 806, 806, 806
1000 06 06 07 07		FCB		807, 807, 807, 807, 807, 807
1009 07 06 06 06 1000 06 06 06 06		FCB		006, 806, 806, 806, 806, 806
1060 06 06 04 04 1064 04 06 04 08		FCD	606, 606,	804, 804, 604, 604, 604, 604

75	1068 44	04 n4 n4		File	\$04,\$04,	\$04,\$04,\$03,\$03,\$03,\$04	PUSTO	1690C			12-1	5-91 195 at	DEMBLEY	PAGE	3
74	10F0 ()4	03 03 (4 04 04 06 06 06		FCB	\$04,864	s./4, BUG, 804, \$06, \$06, \$14	184	120E 01	41	L 00P	MSR	112.1441	ISE'S DUN	AT LON	
77	10FB 07	07 U7 UB		FCD	<b>6</b> 07, <b>6</b> 07,	\$07,\$08,\$00,\$00,\$08,\$0B	186	1210 26 1212 6E	U4 9E FEFE		DAR.	OVER [RESIALI	-		
79			:				189	1216 BD 1218 BD	BE AB	DAEW	BSE	HOTE	RETURN T		
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83 84		00 02 6F	•		10A 41144		193	1210		METRI	l-rege	1.	MUGIC SI	AFTH IN	KE
85	1104 02	94 02 BB E4 03 10	TABL	FCB		*07, \$65, 602, \$04, 462, \$15	194 195			:					
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87	1114 04	17 04 56 98 04 DD		eC.		844,000,005,028,005,476				by John Denv	,				
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80	1120 05 1 1124 06 1128 07	7F 06 EZ		FCB	2000	\$06,\$21,\$06,\$7F,\$06,\$E2	123C			DA 25 AF	100 000	90 00 00 0 90 40 0C I	0 00 1	-21-	-
84	112E 08 1130 09	7F 08 AC		PCN		907, 999, 808, 93F, 808, 8AC	124C 123C 126C	30 OC 1A	M 00 00	OC 10 50	CD An	33 OC 10 0	D 4A X 1		1.3
91	1134 OA 1138 OB	SO OA ED		FCB		609. 688, 80A. 650, 80A, 6ED	127C 128C	1A 9U 00	30 OC 16	10 22 3	00 10	00 00 82 0	H OC X	R RX	RXJ
92	113C OC	FE OD C4		FCB		10F. 073, 010, 03F, 011, 450	129C	40 18 10	12 30 00	44 40 00	00 00	- 10 10 0	O OO HI	- Decem	J
93	1144 10	SF 11 50		FCB		\$13,\$77,\$14,\$A0,\$15,\$DA	12BC 12CC	OC 1A 00	44 (FD OE	1A 00 44	32 OC	IA 36 44 3	5 oc		102_
94	114C 14	AO 15 DA		FCB	MIND CONTRACTOR	18, 887, \$19, \$FC, \$18, \$88	12DC 12EC	3A 49 OC	22 J2 SA	44 OC 14	00 00	94 OC 14 3	3 22 . 2	1 3 .5	18_"2
95	1154 19 1 1159 ID	FC 18 88		FCD		ILC, 8E7, 820, 8BE, 822, 8B9	12FC 130C	4A 18 14	32 3A 4A	30 14 32	3A 4A	OC 10 00 0	0 00 J2	1 JO 21 J	
76	115C 20 1	BE 22 BO CO 26 EF		FCP	024.0C0,	026. SEF. 029, 440, 028, 494	1310	10 00 00	52 OC 10	40 UO 52	ac 10	40 ee 52 (	C 10 R	a R.	
97	1164 29 1168 2E	4D 31 OE		FCB	\$2E, \$4D,	\$31,40E,433,4F9,437,410	132C 133C	00 44 30	eC 3E 00	44 18 OC	3A 00	40 OC 1A 3	A 44 _BO	c D t	0 10
98	116C 33	57 3D CF		FCD	01A, 057,	130, 4CF, 441. 97C, 445, 969	134C 135C	00 DC 14	3A 40 4A 00 4A 0C	IN 14 34	40 4A	10 10 3A 4	O 010 1	97 197	1930
99	1174 41	81 4D DF		FCD	949, 981.	4D, 90F, 952, 981, 957, 969	136C 137C 138C	32 3A 00	18 10 32	3A 40 01	10 00	32 00 00 1 00 00 18 1	9 1A _133	2.0	2
100	117C 52 1	98 62 1D		FCB	45C, 49B,	\$62, <b>\$</b> 1D	139C 13AC	3A OC LA	32 603A	30 10 32	36 60	OC 14 00 0	0 44 4. 2	A10 218	2D
102	L194 16	0082	BIART	L BRD	PEARLE.	GO TO MAIN PROGRAM	13BC	22 3A 4A 00 00 52	44 01 22	00 00 00	18 22	14 00 00 0 4A 44 52 0 4A 52 01 0	C 14 "iJD	D. 170	JDR
104	1104 10	0002			ALGI L	90 TO PHIN PROGRAM	13DC 13EC					00 18 14 4 18 10 40 4			
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109	1187 86 1187 87	3F FF23	INIT	BTA	GM   TCH	TURN ON AUDIO	141C	40 00 32	OC 10 40	48 4E 30	IE 40	48 4E OC 2	4 00 à.R.	. 21010 . 21	01. 5
110	118G 30 1190 9F	80 FE70		LEA1	WET, POR	INITIALIZE	142C	00 44 0C 00 18 22	24 3C 00 3C 44 4A	44 1B 24 0C 1E 3C	3C 44	4A 01 00 0	0 00 _D_6	DJ <dn< td=""><td>193</td></dn<>	193
112	1192 9F 1194 0F	46		87E	VOICE3		149C	10 4A 3A	00 18 10	4E 3C 00	OC 1A	1A 00 40 4 00 52 00 0	A 18 19	N. J.	B S
115	1149 AL	90 0080		LEAT		HUSIC STARTING PLACE	140C	00 32 3A 00 52 01	00 00 01 00 00 01	57 18 10	40 4A	52 OC 1A O	0 52 R	R9 P	AJ_J R_F
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121	11er %	40	READ	LD1	HUMORIE:	POWER TON	140C	40 10 IE	32 48 00	IR IE 37	4H SC	ec 14 00 4 14 00 4A 4	0 00 14 50	er dede	JD
123	11AL AL 11A3 97	B0 42	THE PARTY.	EDA BTA	DUR.	BET NEW DURATION BITCHE NEW DURATION	14FC 150C	UA 44 UO	46 18 0A	44 38 48	OC 14	4A 00 00 0			1
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127					GET THE	Ert NOTES	153C	18 LA 54 5	MA OF OF	HC 00 SC 00 SC 54	00 UE 1	00 00 50 44 54 50 00 18	16 TV		10
130					VOICES		159C	54 SC 44 6	C LA 00 1	32 50 OC	1A UD	40 58 OC 14	80 .16	100	1
131	TINO NO	80 FF36	NOTE	LDA	IANL, PER	DET NEKT NOTE FOR VOICE !	157C	OC 14 52 0	00 4A OE	DE 14 97	00 OC (	UC 14 57 14	10 R	A I	
133 134 135	11AC EE 11AE DF 11BO A6	4F 80		BTU LDA	INCI , 31	GET INCREMENT FOR VOICE I STORE IT GET MELT NOTE FOR VOICE 2	159C 158C	48 OU 32	00 0C 10 0	10 4E 00	18 40	40 4E 00 2E 00 52 16 16 30 40 10 4	10 J	444 a 8 9 a	N a
136	1182 EE	86 51		L DU	A, Y	GET INCREMENT FOR VOICE 2	1300								HuN
136	1186 A6	80		LDA	A. Y	GET NEET NOTE FOR VOICE 3	13EC			44 44		(IC 06 48 00 00 UI 48 38 44 48 00 16 48 30 00 20			7000
140	11BA DF	53		ETU LOA	INC3	GET MEXT HOTE FOR VOICE 4	1600	46 40 OC	24 GU 4A 4	14 18 22	44 00	AN IN SE 44	3C 33 4	_10n_1	0(
142	11BE EE	55		ETH	ALY INC4	GET INCREMENT FOR VOICE 4 STORE IT	161C	SE OC LA S	M 00 00 0	C 14 48	00 3A (	0E 1A 4A 06 10 4A 40 36 00 00 3E 06	3C N J	Ja 1	Ja.
145	11C2 9F 11C4 39	40		RTB	-	SAVE MUSIC POINTER END NOTE	163C 164C	IA 44 00 4	10 OC LA	14 00 3A	10 (V)	00 00 3E 06	1A J	J :	
146 147 148			1	MIDE TO	PLAY THE	m 15.15	165C	30 UU UC	A 40 00	10 10 3A	40 32	00 00 50 00 40 5A 00 14 36 00 1A 00 00 1A 00 44	00 1_16	7.52	b_
149 150	11C5 108E	0048	PLAY	LDY	STERPO	mes etc.	160C	10 52 5A 6	10 OC 14 (	00 00 00	0E 14	00 10 00 44 10 30 40 33 00 00 32 16 30 40 00 23	14 219	102_	1920
151	11C9 A6	9F 0043	PLAY1	ADDA		ADD UP & VOICE BAPLES	F 455-002	44 44 44	4 140 00	a of ta	25, 1701 .	AM 12 14 25	300 10	2.0	61
153	1101 A9	9F 0049		ADCA	( VOI 06 3 )		16DC	10 dn 00 d	12 OC 10 (	kr (n) 57	OC Lo 4	N 40 46 14	IE JA	A B 4	100
155	11D9 B7	FF20		LDD		SUM TO ATO-A CONVERTER AND INCOMPENTS TO POINTERS	16EC	60 48 48 6 00 44 19 6	E OE OO O	00 44 CE	OC 32 0	00 44 50 00 06 02 1A 34 00 1A 00 00	37. area	0 2	Direct
157	11DE D3	4F		STD	VOICE1+L	VOLCEI									
159	11E2 DC 11E4 D3	47 51		ADDD	FNCS FNCS COICES+1	unices.	171C 172C	1A 00 00 0	00 4A 18 1	A 3A 40	4a 30 1	10 TA 40 44	0C _ 1 .	1.93(1	1 22
161 162 163	11E8 DC 11E8 DC	47 48 53		LDD	VOICE3+1	VOICE2	173C	37 38 40 6 44 00 0C	10 00 (	00 0C IA	10 32 3 32 44	3A 40 OC 1A	00 210 40 0	2D 2D1	20
164	TIEC DD	4A 4D		810 L00	VOICE3+1	AOICE 3	176C	38 30 10 3 18 14 38 4	18 44 UI I	14 40 00	00 44 0	DE 14 3A 00 22 3A 4A 44	44 10 21		15
166	11F0 D3	55		ADDD	INC4	VOICE4	179C	3A 00 52	18 14 44	M 52 01	00 44	12 3A 00 16 3A 40 0C 18 3A 0C 18 37 0C 14 3A 06 22 3A 4A 44 00 00 52 00 4A 52 18 14	44 1 R	DJR_DJ	R_b
168	11F4 31 11F6 26	A2 06		LEAY	TIMAS	DECREPENT & CHEW TEMPO	1700	AF OF IO		m	** **				Marin St. Art
170	11FB 0A 11FA 27	0C		DEC	DUR ENDNOT	JUMP OUT IF END OF NOTE	17CC 17DC	10 00 00 1 40 48 4E	52 OC 10 4	10 00 52 18 4E 00	OC 10 4	1E 40 48 4E 40 00 52 00 00 44 0C 24	10 R	9 R 2	R.
172	11FC 20 11FE A6	C7 84	TLIMAS	LDA	PLAY	PADOINO TO MAYE	17FC	4A OC IE	SC 44 4E	C 1E 3A	40 40	OC 18 30 40	40 1	DN +3.1	- D
174 175 176	1200 20 1202 20 1204 20	00	MABTEL MABTEZ	BRA BRA	MABIES MABIES	LODPS THE SAME	181C	10 4E 3C	00 OC 1A	00 52 00	4A 18	10 4A 3A 00	18	R	Ri
177	1206 20 1208 39	Ci	MARTES ENDNOT	PRA R1s	PLAVI	CONTINUE PLAYING END PLAY	182C 183C 184C	00 52 40	10 40 4A	52 4A 18 52 OC 1A	10 4A 00 52	00 52 3A 00 00 52 01 10 00 0C 1A 00 0C 0A 4A 00	00 R9	RJ J	R_R
179 180			:				186C	A AR DO	34 00 46	AR 00 AA	00 10	AF 00 AB 11	15 8 4		4
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183	1204 17	FF7B	21 8UM	LBSA	INIT	INITIALIZE	109C	00 18 LE	32 48 3C	OC 14 00	44 00	OC 14 00 4	32	*J	_32
26															

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191E 192E 193E 194C 194C 194C 197C 198C 199C 194C 196C 196C 196C 196C 196C 196C	40 S2 00 0C 0C 0A 48 0C	98 OE 119 10 114 52 44 44 40 00 34 40 18 0	00 00 00 00 00 18 00 00 00 00 00 00 00 00 00 00 00 00 00	00 40 40 32 3A 0C 0C 0A 10 40 40 00 32 3C 32 0C 0C 0C 1A 44 00 4A 3A 0C 1B 10 1A 00 00 4	92 00 06 00 4A 3C 1B 1A	4A 00 18 10 4B 4A 00 0C 1E 4A 40	18 00 00 00 00 1E 00 00 3C 1B 24 44 00 3A 0C	1A 00 3C 3C	48 48 48 40 18 0C 44 4A 4E 0C 1A 00 00	52 0C 1A 4A 00 52 0C 0A 0A 4A 40 0C 1A 4A 00 00	0C 1A 32 00 5 0C 10 4A 3C 0C 1A 4A 00 0C	1A 32 00 00 00 00 40 00 24 4A 00 00 18 14	00 00 48 00 10 00 52 48 00 40 00 40 00 10 38	R
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IBIE IDZC ID3C ID4C ID5C ID6C ID6C ID6C ID6C ID6C ID6C ID6C ID6	52 48 4A 3A 6 4A 0C 0C 14 0C 00 16 0C 00 5 5C 00 6 5C	40 00	14 00 00 00 00 00 00 00 00 00 00 00 00 00	00 OC	10	0A 4A 58 44 18 0C 54 4A 52 0C 0C 0C 4B 4B	92	52	30	00	52	00	32	M 0 N M RM RMT J J2 J1 JD D J D1 J J J 1 J D J J J S J S J S J S J S J S J S J S J S
1EIC 1E2C 1E3C 1E5C 1E5C 1E6C 1EBC 1EBC 1EBC 1EBC 1EBC 1EBC 1EBC 1EB	48 00 1 4A 00 1 4A 19 1 0C 24 1 22 44 1 32 00 0 60 4A 1 4A 0C 0C 18 1 10 3C 1 52 0C 1 0C 1A 1 10 40 1 3A 00 1	52 OC 0C OC 1A 44 00 4A 00 4A 00 4A 1B 10 1A 40 4E 18 0C 10 14 4B 0C 10 14 4B 32 00 4A 0A 0A 0A 0A 0A 0A 0A 0A 0A 0	06 00 4A 10 00 4A 10 00 4A 10 10 1A	48 00 4A 3C 00 18 0C 24 13A 00 3A 4A 3D 0C 118 14 10 40 48 40 00 4E 14 10 40 40 40 10 40 10 40	4E 0C 1A 0C 3C 4A 0C 1A 40 0C 4E 0C 1B 3A 3A 4A	18 0C 44 4A 4E 0C 1A 00 4E 0C 10 44 0C	0A 00 4A 40 0C 1A 0C 10 4A 0C 10 4B 0C 10	40 3C 0C 1A 40 0C 10 00 4A 0C 10 00 1A 0C 1A	00 40 00 24 00 00 4A 0C 10 4B 00 4E 00 00 00	4A 0C 24 00 00 4A 0C 18 00 00 4E 0C 1A 40 00 4B	0C 0C 00 4A 18 1A 52 0C 14 0C 14 0C 0C 0C 0C 0C 0C 0C 0C 0C 0C 0C 0C 0C	0C 10 10 18 10 18 10 18 18 18 18 18 18 18 18 18 18 18 18 18	00 18 18 40 00 18 30 00 44 18 18	R H N J J J D N J S N J
IFIC IF2C IF3C IF4C IF5C IF6C IFFC IFFC	00 4A 4E 0C 18 1E 10 3C 148 00 4A 4A 18 0C 1A 1A 40 1A	0C 18 10 3C 48 00 00 4E 52 0C 0C 1A 10 40 32 00 3A 4A	4E 00 4E 0C 10 32 00 4A 1E	00 4A 4E 0C 18 1E 10 40 48 00 00 4A 4A 18 0C 1A 00 00	18 10 48 00 4E 0C 10 3A 00	14 40 40 4E 1B 1A 40 00	40 00 4E 0C 10 3A 3A 4A 00	00 4E 0C 10 44 00 4A 0C 00	4A 0C 10 4B 00 4A 0C 1A 00	0C 10 00 00 4A 0C 1A 40 00	10 48 00 4E 0C 1A 00 00 00	00 4E 0C 1A 40 00 48 00	00 4E 0C 10 00 4A 18 0C	J N J 9 J N < N 2 N H N K N 6 N H N K N 5 N H N J 2 J 1 3 5 J 6 J 3 6 J 2 J 1 5
PORT TABLE	9005 90040 #F20	RE TE VC	AD	1187 1186 119F 0048 0046	- 1	UEB1	RT BAL	FFE	E B	PI Si	AA.	ī	10:	SHITON FF23



GMIX FC. 1337 WEST 37th PLACE • CHICAGO, ILLINOIS 60609 • (312) 927-5510 • TWX 910-221-4055

Larry W1[|1000 60 Ricro Joannel P O Box 849 Histor, TN, 37343

SIMIS waits your readers and our users to have the advantage of seeing our entire Lotalog. Therefore we are running it as our ed in the March 82 issue on the 68 hired Journal.

He know of no bellar may to got this catalog into their hands in a timely may. It certainly beats asking them to phone or write in for it.

Our thanks also to Ken Keplen Of Micromers and Frank Mong of Frank Mong Laboratory for their comperation in relocating their ads to make this symmelble.

with best personal regards.

Ficherd Don



#### PRESS RELEASE INFORMATION

Windrush Micro Systems Limited announce the immediate availability of their UNIVERSAL EPRON PROCESSME which has been purpose designed for 8800/6809 software/Needwars development systems. The programmer, which costs 8375.00, (including registered eir mail costs), has the following features:

- PROGRAMMES AND VERIFIES 2708 (TRI-WOLT), 2516/2716 (SINGLE YOLT), 2532, 2732, 2732A, 256A, 276A AND THE S2BK TMS252B.
- \* TWO JERO IMBERTION FORCE SOCKETS WITH MORE SELECTOR SWITCHES ARE PROVIDED....... NO ADDITIONAL PERSONALITY MODULES ARE REQUIRED.
- \* TWIN BOARDS WITH FOUR FEET OF TWISTED PAIR CABLE COMPLETE WITH 180 COMMETCHORS KEEPS THE LID ON COOLING PROBLEMS AND MAKES THE PROGRAMMER MICH LESS TIME CONSUMING TO USE THAN OLD SINGLE BOARD DESIGNS. THIS DESIGN ALSO ENABLES THE PROGRAMMER TO BE INTERFACED WITH A 6821 PIA IN SYSTEMS THAT BO BOT HAVE THE 8-30 BUS.
- \* ERTENSIVE "SOFTWARE MENU" PROVIDES THE FOLLOWING FACILITIES:
- A. ROYE IK THROUGH 16K BLOCKS OF MEMORY FROMITO SPECIFIED ADDRESSES.

  B. READ EPROM INTO BUFFER.
  C. PROGRAM PROOF FROM BUFFER (AUTOMATICALLY VERIFIES PEGGRAMENG).
  D. PROGRAM A SELECTED AREA OF EPROM.
  E. VERIFY EPROM AGAINST BUFFER.
  T. EXAMENE AND CHANGE BUFFER.

- FORMATTED DUMP OF BUFFER
- M. FILL SELECTED ARED OF BUFFER WITH A SPECIFIED FILL CHARACTON.
- \* SOFTWARE AVAILABLE FOR 6800 AND 6809 BASED SYSTEMS. (SSE DOS OR FLER)
- \* SOFTWARE SOURCE FILE INCLUDED ON DISK... ENABLES CUSTOMIZATION.
- . FULLY DOCUMENTER USER MANUAL PROVIDES INFORMATION FOR ADAPTING THE SOFTWARE PACKAGE TO A VARIETY OF SYSTEMS (SSB, SUTP. MSI, GIMIN).
- . PROFESSIONALLY FINISHED PCB 4/SOLDER RESIST (HOTH SIDES), AND SALK SCREENED COMPONENT DYERLAY.
- \* BARE PER'S ARE ALSO AVAILABLE FOR THE HORBY MARKET.



PRESS RELEASE

January 11, 1981 - For Lumminata Release Contact: Januar Tuniu (515) 275-8844 HICHERER TO BOLD 08-9 SEPTERER USER SEPLEMA

Microware Systems Corporation, a leading developer of micro-processor software, will hold its first annual OS-9 User's Seminar on May 14-16 at the Des Moines Marriott. This Seminar is designed to allow the users of the OS-9 Operating System to meet with the designers of this 6809 programming environment for training and briefing sessions. The seminar will also be highlighted by exhibits by major 6809 hardware manufacturers and producers of applications software available under OS-9.

Attandes check-in will be Priday, May 14th. A Recomption and Dinner will be held from 7 be 9 NM, giving the perticipunts a chance to seet, aschange ideas and information.

Chince to seet, sentence to the architects of 08-9 and key figures in the 6809 microprocessor world. These lectures will command Shorthy, Ray 19th at 9:00 an end from throughout the day mutil 5:00 PM. STORTHY for a new Bors lement meab. Instruction and discussion will cover a variety of subjects. Individual sessions will focus on the 08-9 Operating System and the BasicO9, Pascal. Cobol. and C programming languages. A hospitality suits opposed by Historoura will open at 8:100 materiety night to provide on informal environment of discussion. Stunding appriling at 10:00 their will be Good; speakers at a farswall brunch.

Advance pre-registration by mail is required. The registration fee for all seesions is 75 dollers. Persons interested in attending or estibiliting at he Seminar should contact Nicromare as 800n as possible for further info matter and registration materials.

CAMBONT is a very unique contrider for the TRS-80 Color Computer. It contains four twenty four Din sockets and ac address decoder on a high quality, double sided, plated through printed circuit board. Depending on the version you get. | version 1.1 for EPROES and version 1.2 for RMS). You are able to select either 2716 Eraseable Programmable Read Only Memories (EFROMS) or Random Access Memories (RAMS). You are also shis to mis in 2% stage FPROVS and RAMS from the cartridge address COCO Hex (49152) thru FEFF (65279).

installing 6K of RAM or ROM is very simple, just plug them in the sockets provided. Installing 16% of RAW or RCM requires the piggreecking of four chins on top of the first four installed into the societs. Then adding the chip select lines for the four pigg/tecked chips. When Piggbacking the ships you must be careful not to overheat the chips, as the heat will demage them. Make sure the top chips are pushed down tight sgainst the first or bottom chips so that the top of the cartridge will fit properly. CATAORY is built differently than the normal ROM Packs available to allow for the piecybacking of chips inside,

CAMBACRY contridge is orange in color said approximately the same physical outside size as other RCM PACKS. But the main difference is incide. CAPAR installs essily into the color computers cartridge slot. with no alignment problems that night be associated with ROM PACKS that ace mailer than the original design.

The on-board address decoding selects eddress normally essociated with the certrides siot. Even though these addresses are not continous with the lower, on-board computer memory, this is not a problem. Memory e: the certridge location maybe accessed using hesic with PORE and USR romands. Machine language programs maybe loosed directly into the

I have installed 16K of RAM into My DEPMORY contridge, this gives me 10,127 bytes of memory, ( 0000 through FEFF-) The last 255 bytes of bency is not accessable. Because, Besic uses these addresses for imput putput and vectors. I have now relocated most of TV mechine language traffers to load into CMEMORY location. Which frees up most of my lower 52% of memory for use by my programs. Almost any position independent nochine language program should load and execute at this new location, And this additional memory gives my computer almost 48% of pemory. I have found this amount of memory very useful at times. Try CMOMORY, think you will like it, I do.

CLORARY and the recommended ROME and RAMS are available from Micro Labe. Inc. 902 Finecrest, Richerdson, Tz. 75080, DATACHY without memory is \$24.95. 214-235-0915

> Dougles R. Cook 1895 W. Westland Dr. W. Jordan, Wah 84084



#### **Press Release**

GIMIE will be exhibiting at NCC in Houston June 7-10, 1982. We will have guest titlets available that align the user to dain from adminsion to the Enhipita. Places call requests for those to SiMiz. (337 N. 37th Place, Chicago 11 40409.

SIMIE will also be represented at the Microsoft (51-9 section in Doe Reines from May 14-15. For intermation, contact ficrosoft.



#### COMPUTER SYSTEMS CENTER

13461 OLIVE BLVD. CHESTERFELD, MO. 63017 1314) 576-5020

January 13, 1982

Mr. Don Williams, Publisher 68 Micro Journal 5988 Caseandra Smith Road Binson, TW 37343

Dear Don.

The new version 1.8 of Southwest Technical Products Corp.'s BBUG [tm] 6889 monitor contains some unpleasant surprises for those who have developed the questionable habit of using undocumented SBUG entry

SMTPC has published only one manual for SBUG, and that is for the original version 1.5, which was distributed in mesked-ROM from summer of 1979 until the clock speed was upped to 2 MMz. In 1989. At that time, they began sending out version 1.7 In EPROM, which has a longer minifloppy boot delay constant for the fameer clock speed. Version 1.5 will not boot a minifloppy et 2 MMz. Also the NMI (ABDRT button) is vectored through the SMI RAM address. I don't know what heppened to version 1.6.

Bo far, no big deal, mints nothing has moved around. Enter version 1.3 and the fun begins: SMTPC made a marginally useful change at the expense of re-assembling the whole program. Version 1.8 allows lower-case command and her inpute, where earlier versions required upper case. As long as you use only the documented entry points (which are in a jump table and require indirect addressing), there is no problem. However, there are lots of undocumented quodies that have now moved. Mere is a list of some of the likely ones:

ENTRY	old	1.0	
START	P814	PB14	cold SBUG entry (unchanged)
PRERE	PA9C	PB61 FAA2	commend loop (unchanged) print error measage
GETRNG		FD27	get address range
GET 4NX	PD2B	LD35	get address
GET2HX	FD3C	FD43	get hem byte
<b>GET1HX</b>	PD4F	FD56	get Bingle her character
PR4HEX	FD6A	FD7C	print address
PR2HEX	FD72	PD# 4	print hex byte
PRINEX	FD7E	FD91	print single ben character
PMESS	FD9E	F 00 F	print CRLP, then string
CRLP	SDW5	PDB 4	print CR and LF
PRDATA	PDAE	FDCF	print string w/o CRLF
INECHO	PDBA	PDCC	input cher, with echo
INCHAR	PDCe	FD02	input alogie char. w/o echo
C BCK	FDDe	FDE2	check if key struck
OUTSEP	PDDB	FDED	output 2 spaces
DUTSP	FDDD	FDEF	output single spece
OUTCHR	FDDF	FDF1	output a cheracter
DITIKE	PDF1	PE03	initialize ACIA

Also please note a long-standing typo on page 5 of the SEUG il: all references to addresses SDSxx should be to SDFxx.

The above information is provided for your convenience. Use it your own risk....SBUC will probably change again.

Sincerely.

H Joseph Turner, Jr.

Bon williams Mog., 768\* Micro Journal, 5900 Cassandru Shith, Computer Publishing Center, PO Box 849. Risson, TW 37343. D.S.A.



Dear Don.

Tou have published several programs to set and reset the flam peace feature but all are transient communds. Surely the quick and easy way which does not fraview exerting up the disk or loading the head sto. is to use the memory resident near commund table provided for by TSC. The table can be placed in ECOM or EAR (minor's in RAW and loads as PART of the STARTUP) and is simply

	CEC F28 CEC FCB	8 90F0 8 30F0 8 7055	(Tell FLER 2 to refer to user table) (or wherever table is) (table start) '8° for hold. i.e. set passes
	PCB PCB	PA1308	'9' for run. 1.c. pouce off
	TO TO	PADSOF	table terminator
PAUSON	LDAA BRA	# # PF STAP	126 High Street (Barrie N. Smith)
PAUSOF	CLRA		Walt
STAP	STAA JMP	# AD09 # AD03	CTS SJY England.

January La. 1982 946 Evans Rd Nashville, TH 37204

Mr. Oen Hilliams. Sr. 168' Micro Journol 5900 Gessandra Smith Rd. Histon, NI 3734J

Dear Siri

Mn T H Hunt's article on "Those Wonderful Nemory-happed Under Boards" in the Dotober (asset of 689) was serve interesting and steful. except that the circuit presented in fisure 2 on said 30 wor't work Please set the steached for a corrected service, mixed the same third can be accomplished by eliminating the 7474 dual D film film and sormetine the output from the 7404 insents to the clear insult of the 74186 shift nemister. Be sure to cut the trace from Note to the clear insult.

In Hunt did not mention the worst fault of the FED Associates board, usion is that the refresh memory and 5885 controller additions to be clear this is the the clear did steel to be clear. This is a because the 5845 chir select circuit incompletely decodes addresses.

Willia PHarle H Ilian F. Hamblen

1027 1016 1 4-1 + 1C 34-10 1/2 7474 1035 7420

CORRECTED FIG. 2

7432 7404 74166

#### ALTERNATIVE SOCUTION

68 Micro Journal 5900 Cassandra Smith Rd. Mixaon, TN 37343

Dear Mr. Williams,

I've got one here that goes back e long way. In volume 1, isaue 3 on page 39 an excerpt from the Flex Newsletter described a problem with deleting files from a full disk directory. A full explanation of what I have learned isn't in order here; just let it euffice to say that the initialization of each new directory entry is never done completely, relying on an empty entry at the end of the directory to do it instead. This will never happen, however, with a full directory and causes all sorts of unpleasantries.

Well, if anyone out there is still interested in Well, if anyone out there is still interested in Mini-Flex, I have a remedy for this problem. As shown in the listing, the fix resides in the free apace between FMS and the disk drivers. One instruction in Flex at \$700F is all that has to be changed (to JSR \$78A3). Implementers can use their favorite method of patching the DOS.5YS file. With this fix installed files can be deleted from a full directory and new ones added to it, and everything will be done properly.

In case you're interested, here is a partial description of my system: Original SWTP MP-68 with a constant-voltage transformer, heavy duty power supply and cooling fan; two MP-8 mother boards; 46% of contiguous RAM, the top 6% being a color graphica contiguoua RAM, the top 6K being a color graphica board; 16 I/O slots addressed \$D000-\$D03F. Ny DOS is in EPROM at \$E000-\$FPFF, now taking up about SK. System variables, \$CB, and transient area are in 2K of dedicated RAM at \$D800. It is my only operating system and all the power of DOS/FMS is there at power-on. I may not be compatible, but I have a lot of fun being creative. Some of my system software includes SWTB BASIC 3.0 and TSC fast casaette BASIC (both adapted to my OS), a screen text editor and (don't hate me) a 280 cross-assembler (both my own creations, are you interested in the cross-assembler?).

So-long for now.

Sincerely.

Kandy & Kron Kalona, Iowa 52247

01/02/82 10:48:32 AM DIREIX PAGE 801

00010					NAM		DIRFIX	
00020					OPT		O, NDG	
00030		78	OB	CURFCB	EQU		67BOB	current FCB
00040	7EA3				ORG		67EA3	
00050	7EA3	Ca	06	CLEAR	LDA	В	W6	
00060	7EA5	6F	11	CLRLP	CLR		#11.X	clear first and last
00070	7EA7	08			INX			disk address and file
000B0					DEC	B		6128 in new directory entry
00090			FA		BNE		CLRLP	six bytes in all
00100					LDX		CURFCB	restore X
00110					JHP		67C91	write new entry to disk
00120				*				
00130				a new	the I	pat	ch in Fla	PH
00140							R 67C91	
00150								
00160	7DOF			5	DRB		\$7DOF	
00170		BD	7FA3		JSR		CLEAR	
00180	, 541		rens		END		522	

TOTAL ERRERS 00000

REVIEW OF HARDWARE MODIFICATIONS AND ALL-IN-ONE TEXT EDITOR FROM AAA CHICAGO COMPUTER CENTER

Several months ago I purchased a used Midwest Scientific Instruments (MSI) 6800 microcomputer system. The system included a Southwest Technical Products (SWTP) microcomputer, an MSI FD-8 disk drive, an ACT IV terminal, and a dlablo printer. I was delighted because the cost of the entire system as less than the cost of a new Diablo printer. The computer system and software allowed me to develop machine language and BASIC programs that cold produce output to the terminal or the printer. The only substantial problem was that MSI ould not supply software for the system. Technical Systems Consultants (TSC) software for MSI systems does not work in the original MSI system.

#### HARDWARE MODIFICATIONS

AAA Chicago Computer Center (see advertisement in every issue of the 68 Micro Journal) saved the day. The Computer Center inexpensively modified my microcomputer so that it could run software using the FIEX 2 operating syst as well as the older MSI operating system. The modification included hardware changes, software in PROM, FIEX 2 (purchased from AAA Chicago Co puter Center), and special drivers so that FIEX would interface with the MSI hardware. The people at AAA Chicago Computer Center have informed me that they have special drivers that allow full capacity use of FIEX 2 with dual headed 6800 Smoke Signal Broadcasting (SSB) disk systems. Coupled with the Microworks mixer kit, these drivers permit SSB users to use both sides of a diskette and intermix 51/4 and 8 inch drives on the same controller with auto-configuration.

I had my microcomputer system modified so that I could purchase a text editing program. Since AAA Chicago Computer enter was the only vendor that was able to modify my system, I thought that It was reasonable to purchase their text editing program. After using the editing system I have decided that it is the best 6800/6809 editor available. The AAA All-in-One Editor is a line editor that has most of the advantages of a line editor combined with the advantages of a screen editor. When the editor is in the input mode, lines of text are e tered. The text appears on the monitor in the same way as text appears on the monitor of a system with a screen editor. That is, the editor does not require (or

supply) line numbers for the lines of text that are entered. A y line of text can be modified before the carriage return key is depressed.

When the escape key is depressed, the editor enters the command mode. Commands such as U(P), N(EXT), T(OP), B(OTTOM), and F(IND) allow the user to locate any line in the text. For example, U5 moves the line pointer up five lines. A powerful O(VERLAY) command system allows the user to insert characters in a line, delete parts of a line, and move from one position to another in the line just as one would i a screen editor. The O(VERLAY) command also allows the user to change a portion of the line and retain or delete the remainder of the line. The commands for locating lines make it easy to locate the line that is to be modified by the O(VERLAY) command. One can effectively reenter the i put mode by using the IN(SERT) command. The user can start to IN(SERT) text at any Point in the file.

After margins are S(ET) the W(IGGLE) command is used to move the text until each line has an optimum number of words. The W(IGGLE) command eliminates ragged margins and can be used in conjunction with the J(USTIFY) command to right justify margis.

The FIEX version of the editor is called i the same way as any disk utility command. The editor has its own commands for interacting with the disk operating system. For example, entering Q(UIT) will cause the the system to request FIE? If one enters Q.PARTI, the editor will create a file named PARTI.TXT using disk drive D. This file will contain the entire text that was entered in the editor buffer. If the disk already has a file named PARTI.TXT, the editor will initiate appropriate prompts to determine if the file should be deleted. Commands such as A(PPEND) allow the user to create a file that is much longer than a file that can be an antained in the editor buffer. A file can be A(PPEND)ed to a second file at any location in the second file. If a file is longer than the text buffer, the M(ORE) command is used after one completes modifications of the part of the file that is in the text buffer. In long text files, portions can be moved from one location to another by creating any number of "third" disk files for later reinsertion into the text.

I have only touched on some of the features of the editor. For example, it has complete provisions for changing strings of text, deleting any number of lines of text, inserting text, setting magnisesting page length, tabulating, and producing hard copy. The editor can also be used to create mailing lists and labels.

It is difficult to communicate the care that went into the design of the editor. Commands can be given with upper or lower case letters. In addition, more than one command can be given before entering a carriage return. I structions are supplied so that

the user can modify the letter or symbol for many commands. For example, the user can switch from A to another symbol for indicating the insertion of text. Most changes of symbols can be made in the command mode without having to modify machine language code.

Once the basics of the editor are mastered, the manual for the editor is very easy to use. The commands are described in alphabetical order to facilitate locating a specific command. Important machine language program locations that can be modified by the user are given at the end of the man 1. heartily recommend the AAA Chicago Computer enter All-in-One Editor. The people at AAA Chicago Computer enter will take the time to explain what their software (and hardware) can do. They are available to help users with any questions they may have.

Allen H. Wolach, Pn. D. Illinois Institute of Technology

E. M. (Bud) Page, Ph. D. COMPUTER SYSTEMS CONSULTANTS 1454 Latte Lane, N. W. Conyers, GA 3D207

The ottached program allows direct loading of Metorola "S1" formatted files into FLEX [tm Technical Systems Consultants] binary files. It is especially useful for loading object programs which conflict with FLEX or would load into non-RAM address ranges. It is called as follows:

#### SICONV INPUTFIL OUTPUTFILE

where INPUTFILE is the name of the "Si" file (with default auffis of .T T) and OUTPUTFILE is the name of the binary file (with default suffix of .BIN). Although the program was written for the 6889, it may be made to run on the 6888 without too much

When the program encounters an "S1" beginning a line, it generates a code segment for the indicated address, length, and contents. When it encounters an "S9", it generates a transfer address and terminates execution. If it encounters an I/O error, it prints the standard FLEX error message and terminates. The hex digits and checksums are not checked for validity.

Bud Pass E.H. (Bud) Pass President

51 TO FLEX GBJECT CONVERSION

			CONV	P T attack	As at 1 mie. Di	LEX OBJECT FURKAT
			CONV	E 1 PRO	N 21 10 F	TEX BESECT FORMAL
			•			
			* M 2N	JISK E	GUATES	
		C848	FCB	EQU	SCR D	
		D406	*MS	EQU	\$D406	
		D403	<b>FMSCLS</b>	EQU	SD403	
		CDØ3	WARMS	EQU	SCD@3	
		CD15	GETCHA		\$CD15	
		CDIR	PUTCHR	BOU	\$CD1B	
		CDIE	PSTRNG	EQU	\$CD1E	
		CD24	PCRLF	EQU	\$CD24	
		CD2D	GETFIL	EQU	\$CD2D	
		CD33	SETEXT	EQU	\$CD33	
		CD45	RPTERR	EQU	\$CD3F	
		CD43	OUTABR	EQU	\$CD45	
Club				ORG	SCION	
				ORG	20100	
			* MAIN	ST RTS	HE Y	
				21 610	III L	
>C180	16	0001	SICONV	1.BK	CVI	
			•			
C103	נט		VN	FCB	1	VERSION NUMBER
					•	TOROZON KBI BEK
C194		C64B	CAI	LDX	# CB	POINT TO YCO
C107		CD2D		JSR	GETFIL	G T THE PILE NAME
CIØA		0C		BCS	DSKERR	
CIBC		01		LDA	41	S T FOR RE
CIBE	A7	84		STA	B,X	SAVE IN PCB
C113		CD 3 3		JSR	SETEXT	SET DEPAULT EXT
CIIG		0486 0C		JSK	FMS	CALI. FNS
C118		CD3F	DOMMON	BEO	CV2	
CIIB		D403	DSKERR	JSR	RPTERR	REPORT ERROR
CILE		CD24	DSKER2	JSR	FMSCLS	CLOSE ALL FILES
C121	7E	CD03		JSR JHP	PCRLF	
C124		C234	CV2	LDX	WARMS	RETURN TO PLEX
C127		CD2D	CVZ	JSR	♦FCB2	POINT TO PCB
C12A		EC		BCS	DEKERR	GET FILE NAME ERROR?
C1 2C	4F			CLRA	DBUERR	SET EXTENSION
C12D		CD33		JSR	SETEXT	PC: EVIEWATOR
C136	86	02		LDA	#2	OP N POR WRITE
C132	A7	84		STA	0,X	O. C. IOR WRITE
C134		D496		JSR	FMS	CALL PHS
C137		DF		BNE	DSKERR	ERROR?
C139		PP		LDA	• SFF	NO COMP ESSION
C13B		80 30		STA	59,X	
C13E		CD24		JSR	PCRLF	
>C141		002C		LBRA	NEXT	GET STARTED
C144	BE	C234	PUT	LOX	4 PCB 2	POINT TO WRITE
C147	BD	0496		JSR	PMS	WRITE CHARACTER
C14A		CC		BNE	DSKERR	ERROR?
C1 4C			ET	RTS		RETURN
C14D		C649	GET	LDX	FCB	POINT TO READ
C150	34	B4		PSHS	В	
C152	BD	D486		JSR	FNS	GET CHARACTES
C155	35	84		PULS	0	
C157		F3		BEQ	RET	ERROR?
C159		61		LDA	1.X	CHECK ERROR
C15B C15D	26	88 B9		CMPA	●8	IS IT BOP?
C15F	8E	C234	G! 655		DSKERR	
C162		84	CLOSE	LDK	FCB2	POINT TO WRITE
C164		84		STA	14	CLOSE PILE
C166		D486		JSR	, X PHS	CALL THE
C169	1826	FFAB		LBNE	DSKERR	ERROR?
C16D	17	PPAB		LBSR	DEKER2	RETURN TO PLEX
	17	PPDA	NEXT	LBSR	GET	GET A SYTE
	81	53		CMPA	e\$53	CHECK FOR S
C175	26	F9		BNE	HEXT	SHEER EVE S
C177	17	PPD3		LBBR	GET	
C17A		31		CMPA	0531	CHECK FOR 1

C17C	27	38		BEQ	G0751	
C17E	91	39		CMPA	.839	CHECK OR 9
C188	26	EE		BNE	N KT	
C100	20	FFC8	GOTS9	LBSR	GET	IGNORE COUNT
>C182	17	FFCB	00159	LBSR	SET	IUBORE LDUB!
>C185	17	FFC5				
C188		16		LDA	4\$16	SET XPER ADDR
>C18A		FFB7		LBSR	PUT	
>C18D	17	FFBD		LBSR	GET	GET ADDRESS
C198	17	6683		LBSR	ASCHX )	
>C193	17	FFB7		LBSR	GET	
C196	17	668C		LBSR	ASCHOL 2	
>C199	17	FFA8		LBSR		
10100	17	FFAE		LBSR		
CLAC	**	8874				
ACTAL.	1.4	FFA8		LBSR	GET	
CIAZ	17			LBSK	ASCHOT 2	
>C19F >C1A2 >C1A5 >C1A8 >C1A8	17	887D				
>CIA8	17	FF99		LBSR	PUT	
>Clab	16	FFB1		LBRA	CLOSE	STOP
CIAE	86	02	COTE	LDA	1502	MARK COD SECHEMI
>C1B0	17	FF91		LBSR	PUT	
>C1B3		PP97		LBSR	GET	GET COUNT
>C1B6		605D		LBSR	ASCHX1	
>C1B9	17	FF91		LBSR	GET	
>C1BC	17	0066		LBSR	ASCHX2	
CIBC	24	63		SUBA	<b>•\$03</b>	
CIBF	00			STA	A34	
C1C1 >C1C4 >C1C7 >C1CA	B7	C103				GET ADDRESS
>C1C4	17	FF86		LBSR	GET	CEI VADVESS
>C1C7	17	004C		LBSR	ASCHX1	
>C1CA	17	FF80		LBSR	GET	
>CICD	17	8655		LBSR	ASCHX2	
CIDE	B7	C232		STA	ADDRES	
C1D3	17	FFGE		LBSR	PUT	
C1D6		FF74		LBSR	GET	
>C1D9	12	003A		LBSR	ASCH 1	
CIDS	17	FF6E		LBSR	G T	
CIDC >CIDF	17					
>CIDF	17	0043		LBSR	ABCH 2	
CIE2	B7	C233		STA	ADDRB6+L	
ClES	17	FF5C		LBSR	PUT	
CIES	B6	C103		LDA	VH	SET COUNT
CIEB	17	PF56		LBSR	PUT	
CIEE	8E	C232		LDX	PADDRES	
CIFI	BD	CD45		JSR	OUTAGE	
CIFI CIF4	86	ØD		LDAA	0580	
C1F6	BD	CD18		JSR	PUTC R	
C1F9	17	FF51	OOP	LBSR	GET	GET A BYTE
CIFC	17	0017	OOF.	LBSR	ASCHA 1	OLI W BITE
CIPE	12	FF4B		LBSR	CET	
C1FF >C202	17	8828		LBSR	ASCHE 2	
>C202	17					
C205	17	FF3C		LBSR	PUT	
C208	7A	C103		DEC	VN	
CZØB	26	EC		BNE	LOOP	
C20D		FF3D		LBSR	GET	IGNORE CKSUK
C210	17	FF3A		LBSR	GET	
C213	16	FF5A		LBRA	WE T	BACK OR NEXT LINE
C216	80	30	ASCHX1	SUBA	0538	FIRST HEX DIGIT
C218		09		CMPA	1509	
C21A		02		BLE	ASCHY1	
C21C		87		SUBA	0507	
	1F		ASCHY!	TZR	A, 8	
		6.9	Macusi		AIP	
C226				ASLB		
C221						
C222	58			ASLB		
C223	58			ASLU		
C224				RT5		
C225		30	APCIOCS	SUBA	0\$30	SECOND HEX DIGIT
C227		09		CHPA	4509	
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CZZF		EØ		ADDA	. 54	
C231		20		RTS		
C231	39			WID		
0275	41		ADDRES	mba		
C535	OFFID		ADDRES	PDB	20000	
-424					7.04	
C234			FCB2	RMB	350	
			•			
				END	SICONV	

CALCOMP 143 DISK DRIVES WITH /09 SYSTEM

#### H. Kitazume

Here I would like to report my struggle around /09 system. I hope that this is useful to other readers.

I had

1) 6800 with 40 K RAM from SWTPc
2) DMFA-1 (Calcomp 143 ) 8' disk system.
3) H-9 terminal from Heath kit (modified to 24 lines )

 4) Dot matrix printer from Okidata.
 5) IBM selectric printer.
 I was using that system for 3 years except Okidata printer, which I added one year ago.

In June 1981 I bought 1) /09 6809 computer with 56 K RAM ( 2 MHz clock

2) DMF-2 disk controller board from SNTPc.
The front panel of /09 was damaged during the shipment so new replacement was sent soon. The /09 was working properly and I was delighted with the rapid advance of technology even in hobby world.

In July 1981 I bought

1) FLEX09 with Editor and Assembler

2) Text processor 3) Basic interpreter from TSC.

connected the DMF - 2 Meanwhile Meanwhile I connected the DMF-2 disk controller board to the Calcomp 143 disk drives and run the diagnostic program from SWTPc. This did not work well at all. Nothing was mentioned for upgrading DFMA-1 in the manual so I studied myself and learned following.

i) pin configuration of different. motor control

2) pin assignment of TG43 ( track above 43 ) different.

3) track access time of Calcomp (6 msec.) is slower than that of Data Track 8 (3 msec.). I modified these hardware and software and worked well.

I tried to boot FLEX09 from TSC but never succeeded. I changed the clock to 1 MHz and tried to patch loader but results and tried to patch loader but never worked well. I contacted to SWTPc and TSC but no help at all. SWTPc checked my DMF-2 controller and TSC checked FLEXO9 diskette and both were all right.

Finally I traded off with following system which worked.

1) /09 with 1 MHz clock.

refferded to AN#102.

formating.

controller board was useless.

DMFA-1 disk controller, modified the address to F000. This was so far satisfactory, although DMF-2

I needed PROM programmer and I learned that programs for PR-M program board is available as READPROM. CMD and WRITPROM. CMD as utility programs in FLEXO9 from SWTPc. I bought the FLEXO9 ver 2.8 and I realized that the FLEX from SWTPc has much more utility programs and better printer handling routine but still cheap. Also I noticed that this FLEX can be bootable with DPM-2 controller connected to Calcomp 143. The software patches were included and "hardware modification" was refferded to ANGIO2.

I succeeded to boot the FLEX with my system and confirmed that my modification of DFM-2 board was adequate. Then I tried to change to 2 MHz clock and this was almost successful. Only thing that has not been solved is improper functioning of NEWDISK command for which I have to switch back the system clock to 1 MHz in each time I need disk

Now my current system is
1) /09 with switchiable 1 and 2 MHz clock.
2) DMF-2 disk controller with Calcomp 143 drives.

Following are the suggestions to use Calcomp 143 in /09 system.
1) To use DMF-2 board with Calcomp 143

(1) modification of motor control and TG43 ( track above 43 ) pin configurations -1).
(2) software patch for 6 msec. track access time

(3)

(3) software patch to increase the waiting time in DCHCK disk diver routine as follows ( for FLEXO9 ver 2.8 ) DF93 from 8D 00 to DF93 BF DF9C

DF96 7A DF9C DF95 39 DF99 26 FB DF9B 39

2 ) To use DFMA-1 system

(1) make sure to place a jumper BASBS at MP-09 CPII board.

(2) change the address of controller to F000 -3).
(3) software patch -1).

(4) I am not sure if it runs at 2 MHz.

I realized that I wasted time since nobody from SNTPc gave me enough and adequate information for upgrading DMFA-1 system by DMF-2 controller board or nothing was done by TSC to make FLEX09 bootable at Calcomp disk drives.

I still feel that both companies are good since they have been trying to release powerful systems at affordable price. If they can be ( and they must do, I believe ) more kind and consciencous to customers, they are the best.

am using my system for 1) data base management, 2) statistical analysis for my research projects and 3) data analysis and word processing. I played games at 6800 but I do not have programs for 6809.

I am planning to upgrade my system to S/09 ( 128 K RAM ) and run FORTRAN compiler.

I have another project which is to upgrade the old 6800 system to 6809 and make it compact to fit in one case which contains 6809 CPU board, 32K+16K RAM, 8' disk controller from GIMIX and one Siemens 8' single density disk drive. I destroyed old MP-AZ CPU card during the modification and probably have to buy6809 CPU board from SWTPc.

Refferences
1) SMTPC modification - application notice AN #125A Jan. 8, 1981. 2) SWTPC modification - application notice

102 Nov. 21, 1979. 3) 68 Micro journal: page 30, March 1980. #102 Nov.

#### 6502/6809 MICRO

The April Issue of the 6502/6809 Micro is directed toward the 6809, so we have been informed. Many have complained, to us in the past, that there has been very little 6809 in this magazine, I trust that those who have subscribed will be more satisfied in the future.

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### HFI P

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DEAR SIR, WOULD LIKE TO SEE SOME SSB PROGRAMS IN THE MAGAZINE. DON'T SEEM TO BE ANYTHING BUT FLEX LATELY. THANK'S FOR YOUR HELP. H.C. FIELDS W5SGX

ED'S NOTE: SORRY WE DON'T GET MANY SSB, HOW ABOUT SOME I DMW ...

DO YOU KNOW OF ANYONE WHO IS WRITING AGRICULTURAL PROGRAMS FOR THE 6800 FLEX EXTENDED BASIC OPERATING IF SO I WOULD LIKE TO KNOW HOW TO GET IN HITH THEM. MERLE BERTSCH RT. 1 BOX 137 SYSTEM? TOUCH WITH THEM. MILLER, S.D. 57362

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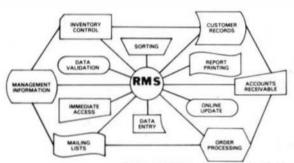
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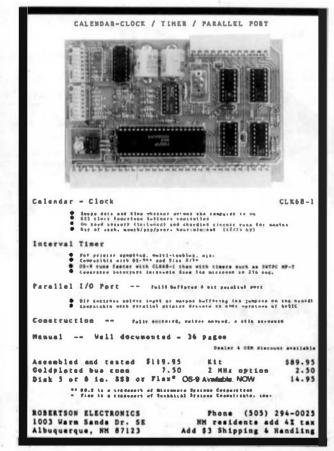
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- Downloading programs from other computers

The Microtext module is a program pack containing not only firmware but a second serial port so that both your printer and modern can be connected at the same time. Microtext can be configured for any serial printer that with work with the Color Computer, even if it requires line feeds! But even if you don't have a printer, you can keep a permanent copy of your data by storing to cassette tape. Also, any Radio Shack/ Centronics-compatible parallel printer may be used by adding the Micro Works' PI80C parallel Interface.

For those of you with special terminal applications, Microfext has selectable parity; if sends odd, even, mark or space. With mark parity (which is default) you can send to computers requiring either seven or eight bits. All 128 ASCII codes can be sent. Exchange programs with other Color Computer users! Basic programs may be downloaded from other computers or timesharing systems.

You'll find many uses for this versatile module! Available in ROMPACK, ready-to-use, for \$59.95.

#### MACHINE LANGUAGE

MONITOR TAPE: A casselle tape which allows you to directly access memory. I/O and registers with a termatted hex display. Great for machine tanguage programming, debugging and learning. It can also send/receive RS232 at up to 9600 baud, including host system download/upload. 19 commands in all. Relocatable and reentrant, CBUG Tape Price: \$29.95

MONITOR ROM: The same program as above, supplied in 2716 EPROM. This allows you to use the entire RAM space. And you don't need to re-load the monitor each time you use it. The EPROM plugs into the Extended Basic ROM Socket or the Romless Pak I. CRUG ROM Price: \$39.95

SOURCE GENERATOR: This package is a disassembler which runs on the color computer and generates your own source listing of the BASIC interpreter ROM. Also included is a documentation package which gives useful ROM entry points, complete memory map, I/O hardware details and more. A 16K system is required for the use of this cassette. 80C Disassembler Price; \$49.95

#### **LEARN 6809**!

6809 ASSEMBLY LANGUAGE PROGRAMMING, by Lance Leventhai, contains the most comprehensive reference material available for programming your Cotor Computer. Price: \$15.95

#### PARALLEL D!

USE A PARALLEL PRINTER with your Cofor Computer! Adaptor box plugs into the serial port and allows use of Centronics/Radio Stack-compatible printers with parallel interface. Assembled and tested. PI80C Price: \$69.96

ROMLESS PAK1 — is an empty program pack capable of holding two 2716 or 2732 EPROMS, allowing you up to 8K of program! The PC board inside comes with sockets installed, neady to go with the addition of your custom EPROMs. Price: \$24.95

SPARE PARTS — SAMS, 6809Es. RAMS, PIAS, Call for prices.

#### 32K RAM!

MEMORY UPGRADE KITS: Consisting of 4116 200ns, integrated dircuits, with instructions for installation, 4K-16K Kit Price: \$39.95, 16K-32K Kit (requires soldering experience) Price: \$39.95

GAMES

Star Blaster — Blast your way through an asteroid field in this action-packed Hi-Res graphics game! Available in ROMPACK: requires 16K. Price:

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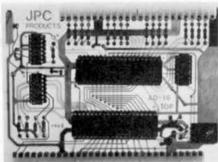
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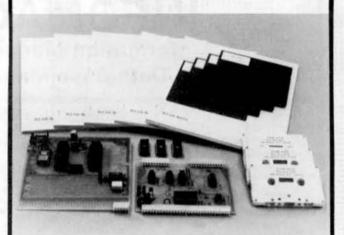
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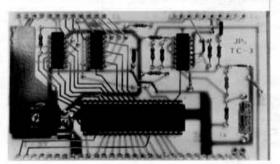
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PAGE ONE

### ALFORD AND ASSOCIATES - GOOD NEWS!

ANNO . DOMINI - NINETEEM - NUNDAED - EIGHTY - ONE

TUESDAY, DECEMBER 1st.

#### COLOR COMPUTER TALKS!

Alford a Associates is now anipping its SP-1 "SPEARER PACE" for the Radio Shack Color Computer, The SP-1 pings directly into the ROM-pack alot, and its use requires no wireles, computer modification or ejectrosics knowledge?

The moficare provided gives your computer the power of appear using solbing more than Basic Pook and Poke ristements! The Sr-1 can add a new dimension to your cames, huslessed programs or CAI drille. Just about any application can behalf if from the Sp-1

The SP-1 alloes uplimited speech. Also, the SV-1 requiree less memory overhead for speech than any other type of unlimited voice speachemer on the market today. Typically, fewer bytes of etc., age with one of the today to the speech of the the oddivision at sumber of leitwin in logisch Text! Banket data statements suffice to story most any test you hand also, so exacting the suggest outlines are seeded to drive to

The SP-1 somes eith sample software in Basic to demonstrate the 90°Per of this fastsetic device, Alford's even each pilvs a version of their YVX-ZD1TVM to allow tases who have ldd of emmony to edit epech files quickly and emaily (Extended Basic is NOT required)!

The SP-1 includes a comprehensive uneual which provides speech theory, use of the included software, phoness code charts, sample programs and much more!

#### COMPUTERS SPEAK TEXT!

Alford's has been on the lookout for a good speech covertor program for use with their SP-1 and VS-1 speech systhematics, and usport having finally found sit The profram taken Endlish test in ASCII form, converts it, and then directly drives the wyntheniam? By sonding the approprise control codes, you can even switch between test conversion or upwalting letters or speaking etrains the subscesses in formation!

The standard vorsion is designed to work just tike ONTH or OUTEK. In all codes, you call the converter with characters in the A-MUDISTER, in conversion ende complete, then it is converted and spoken, in letter made each character is rested as a MSCII character and prosuced. In phonem mode, each character is treated as a single inflected phonem code.

The Color Computer version is even used for to use. Once louded, Masic has a new verb — SAT. To the ise converter, you simply when the command to wake the computer talk; i.e., SAT THIS IS A COMPUTER SYMMETRY, and the Color Computer talk; i.e., SAT THIS IS.

#### TREK-69. COLOR-TREK

Ever store Alford's antendamed TRAIL-08 last year, we've been ashift tham to do a version for 680% users and for the eer TRS-88 Color Computer. They said that they would have, but itself old however they would have but itself old however one night not have been comparable to not have been comparable last her than take a change, they had on the training they rested to buy, we'll, they finally decided on which sprain they rested to buy, we'll, they finally decided on which sprain they rested to buy, we'll, they finally decided on without they wanted to buy, we'll, they finally decided as a change of the second to the second they we will be they come to the second they we'll be they come to the second they we'll be they come to the second they we'll be they we'll be they come to the second they we'll be they we'll be they come they we'll be they are they

if you have a MEMORT-MAPPED BISPLAY then THE-60 or TREL-60 are for you. If you have a color computer with 15K memory (Latended Beats not required), then you should ask for COLOR-TREE.

They took the claumic trek-game and rewrote it completely in assembly code, remains it rus is RESL-TIME. The result is, we feel, the finest TREK game available!

Eaceny destroyers chase you even as you move about the quadrant. Tour Whitple buttle plans make them hard to evaded You dodge corpedees and return lire. Onamer is equationed and repairs occut as you play. Messakes flash on and off. Agets, all in REAL THES?

Game difficulty levels rum from SIMPLE (for beginners) to a level which, to our knowless, only one person other than the author himself has succeeded in winning This is not a simple same. The best mining it may recorded for the seful SURCIDE OFFICE in twonty-nime minutes. The featest loss we have seen octured in only eleves secondal

Have a MEMORI-MAPPED DISPLAT? Then all

#### SPRAKER BARE BOARDS!

A)ford and Associatas secesily lowered the price on its VS-1 synthesizer. A company spotbaman attend that the reduction was due to the Great response that the 35-35 community has given the board. Nos they are going one step further. Tou can awe buy a bare berd. manual, dust or wastenizer chip seperately. See the price list for details.

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#### NEW SCREEN EDITOR!!

Does your terminal mave an addressable cursor like the Soroc [Q-120° Does your tarkinal acroit shes you do a ove line on the bottom line? Does your terminal run full-dupter? If not, then your about deligh this of, the you have beready for SCHEDITOR lift.

Som equid you like an addior that will bandle a 252-column spread sheet? Or one that milows you to more margins anywhore and at my time? Or that handles true multi-solumn edit jobs like tis Da86? Or that formate teat as you type? Or that milows you to met to releer tobs at any time, anywhere, with a mindle keystroke? SCENDITON BILL CAN!

Like to be able to define what singlehey operations you do not meant aingle beye? Or for that matter, what commands made you mant to give the commands? YOU CAN WITH SCREDING 111?

Wouldn't it be sice to be able to define up to teatr-ein editing micros, eith a macro length of up to 1000 characters? Or to be able to edia commende, operations and teat, all in the same macro? Or even to display and edit the macros thomselves just like teat? Or sawe and load your macros from sisak files? TOC CAN SITE SCHEDITOR 11st

Row about file tandding, sould you take to edit unimisted-mised tiles? Of to be able to reas welsaled liose out of one file into another? Or how about conditional preserved enading to lat you one too lines be fore; seering them? Or be able to write lines out to five files or to specify where to mist tending or writing, and how much at a time, and bow many times? Tou CAS ATTE SCREDING 111:

Think about it. Thirty-two control-sodie operations. About fifty other commands, and the number is growing. Twelve justification commands slone? ONLY WITH THE ALL-XYS SCREDITON 118.

As if all of this, and each more than se have foom for here, isn't sough, this over editor is available for PLEX 10, FLEX 2.0, FLEX-9, 00888 and 003591 D8-9 version to be available soos (maybe as you read the, even).

Is talking to John Alford, proprietor of Alford and Associates, we were told that he is tired of writing editors. He tadicates that he knows only two ways to atop; cet out of the business, or write the ultimate editor. It deem't appear that he is muns out of business condi-

If this hamn't convinced you that you should be using SCREDITOR III, then call or write for more details, or for the complete SCREDITOR III spec sheet. Our only question is, why continue to edit, whom you can SCREDITOR.

SCREDITOR III is available for onet serial terminals, and all demony-mapped displays, 8800 and 6809 versions are ready sow."

#### SSR DOS UTILITIES

As many of you know, Smoke Signal Broadcasting's DOS is one of the best around, There was, however, one thing we felt to be lacking...disk nemes!

Afford and Amontstew has finally found have to go about manning a disk in a non-destructive and socure way, and started to write some utilities using the disk information record. Their MAME utility allows you to mame your disks. The information ector tooludes the disk tame, earth number, creation date, last update date, a comment field, and last but wor least, a disk file access code.

The access code led them to the second program, LOCK, bith this program you can write, delete, and LIST LOCK your files!

With their LIST program, the limit-locked files do not list usless you give the access code for the disk: In addition, you do not have to look at a pile of timbslest commands daleds you want to, as LIST slows you the option the liming for certain files, LIST oven late you list the disk information process.

The UPDATE Profirm lets you chasse the information recurd! They also include PURCE to cleam by dishs. DLMP to make pretty core langues, and TITLE to print title pages on all of rows limiting.

The UTILITIES #1 disk is available for OOSSS, versions 4.0 and up, and for all versions of OOSSS. The manual itself is a cood reason for buying this parkage, as it has a batch or information on SSS disk Executive.

#### THE LAST WORD ...

This year (our third) has been the sont according war. I would the to take as opportunity to thank our masy fibe customore for their support and understanding.

Most of all though, I would like to exprease Wy thanks to God, she is the SAJOT "Associate" in My Distorms. At this time of year it means especially appropriate to putset to give thanks and to rummaber the many wireless which He has performed for all oits. Too often, we take Nie grace for gratad.

In the season, so seighters the Miralis of the stermal light and the miracle of the birth of the Messiah. Both stand for hope for the birth of the Messiah. Both stand for Cod's light is our life, and both abow Rim love for us, in that He provides for our nords in many wafe, especially in these of darhames.

I gray that in this most year, my walk with Him sill lead me clower to the place He wenia me, said that I might become even better able to serve every one of you, my customers. I also hope that each the of you will pray for me and my family so that see misht grow as he wants as to, and that we might be more worthy of your mitromaps.

Thank You again, to love end in prayer, from all of us, and especially from ...

John L. Alford (preprietor)
Sally Anse Alford (most everything else)
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#### GENERAL INFO

All of Alford's coftware le evaliable on 5- or 8-toch disk vaccet abre cotted. Also, except where noted. All actware to available for FLIX 1.0, FLIX 2.0, FLIX.9. 00360 or 00569. Versions for 08-9 are comiss coon. Software orders are normally shipped sithin three days. Bardeare rune from atock to 30 days.

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RICEDORD (RP), Today, 46 year old Harler R. Aumquat was observed biting a mongrel dog in Hoseos Park. When ask d by this respected which a singular activity, Harley replied that he had been coming to the Park delley for fiftees years, and that on each and every outing, the dog had bitten him. At law "e sould stand as" "In kind

PROGY WITHER, AR — This reporter was praviously of the Opinion that he had see everything, but found that there is truly something new under the ann, an the booorable Renator Bilge Pamp was caught in the very act of tailing the truth to his consistuants.

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#### **ELEKTRA CPU 8/9**

Chaice of 6808 or 6809 CPU

(6808 is software compatible with the 6800 or the opcode level)

DEVICE 6809 ADDRESS 6808 ADDRESS
3 2716 Eproms Eprom #3 F800-FFFF F800-FFFF and E000-E7FF

Eprom #2 F000-F7FF F000-F7FF

Eprom #1 E800-EFFF E800-EFFF

1K SOUTCO AND E400-E7FF A400-A7FF and A000-A3FF MC6840 Triple Timer E210-E217 8200-8287

MC14411 Boud Rate Generator producing boud rates of: low Range 110, 150, 300, 600, 1200, 4800, and 9600

High Range 440, 600, 1200, 2400, 4600, 19200, and 38400

The board does not contain a DAT and does not support extended addressing.

The board supports DWA by either HALT or BUSREQ when a 6809 CPU is used.

DMA to the devices on the CPIJ cord is not supported.

The board will run any of the MIKBUG" compatible manitors in the 6808 made and SBUG-E, HILMIZIG (Special Version), and GMXBUG-09 in the 6809 mode. The ELECTRA CPU 8/9 will run any of the GIMX® disk controller boards with the appropriate GIMX® version of FLEX®. A special version of OS-9° L-1 is available.

Core board: \$50.00\*

Kit: \$225.00°

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Ats the srandard 30 pln SS-50 bus I/O slor

Can be cartigated for 4 addresses per part with the B part 2 addresses higher than the Apart or for 16 addresses per part with the B part 4 addresses higher than the Apart.

Each part is terminated at two 16 pirt dip sockers, one socket configured for modern and the other socket configured for terminal or printer. RTS, CTS, DTR, DCD, DTR are appropriately insplemented.

Each part has independent selection of bould rare

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Bare board: \$20.00° Kitz \$60.00° Assembled: \$80.00

Assembled cobile (two required for each interface board) \$20.00 each

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Can be configured for 4 addresses per pair or 16 addresses per pair (accupying the first four addresses of the I/O star).

The direction of the TTL buffers can be controlled by either on board jumper connectors or by a signal from the peripherals,

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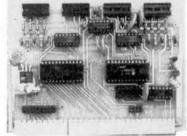
Assembled: \$80.00

Assembled cable (two required for each interface board): \$20.00 each

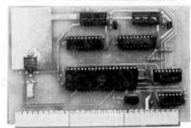
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DPS Dual



DPP Due Port

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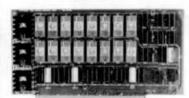
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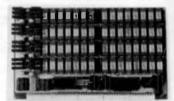
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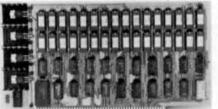
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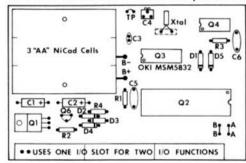
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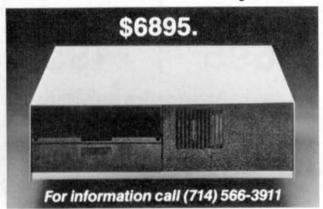
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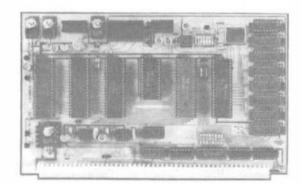
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### word's worth

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PATCHES: patch and make functional various standard TSC utilities and programs including APPEND ASMB COPY EDIT PUTLDR SAVE and others. A special NEWDISK (single side, single density, 35 track) routine allows disks made on the Color Computer to be reed or written on other FLEX\* systems, insuring complete transfer of disks from Standard S50 Bus computers.

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\*\*\* TSC's Extended BASIC (XBASIC) patches are now running. Others to follow; call for current information.

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#### \*\*\*\*NEWs\*\*\*

NOTE: Also ready by the time this is published will be the F-Mate" version for the Radio Shack Color Computer Disk Systemit



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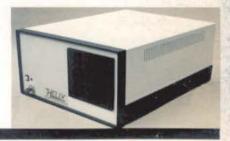
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